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L A N O D E - 2 8 6

Processor	80286, P9
Processor Speed	12MHz
Video Processor	Unidentified
Chipset	101SX, 111SX
Maximum Onboard Memory	8MB DRAM
Maximum Video Memory	None
Cache	None
BIOS	AMI
Dimensions	300 x 200mm
I/O Options	Floppy drive interface, IDE interface, parallel port, serial ports (2), video port
NPU Options	80287
Video Types Supported	Monochrome
Highest Resolution Supported	Unidentified
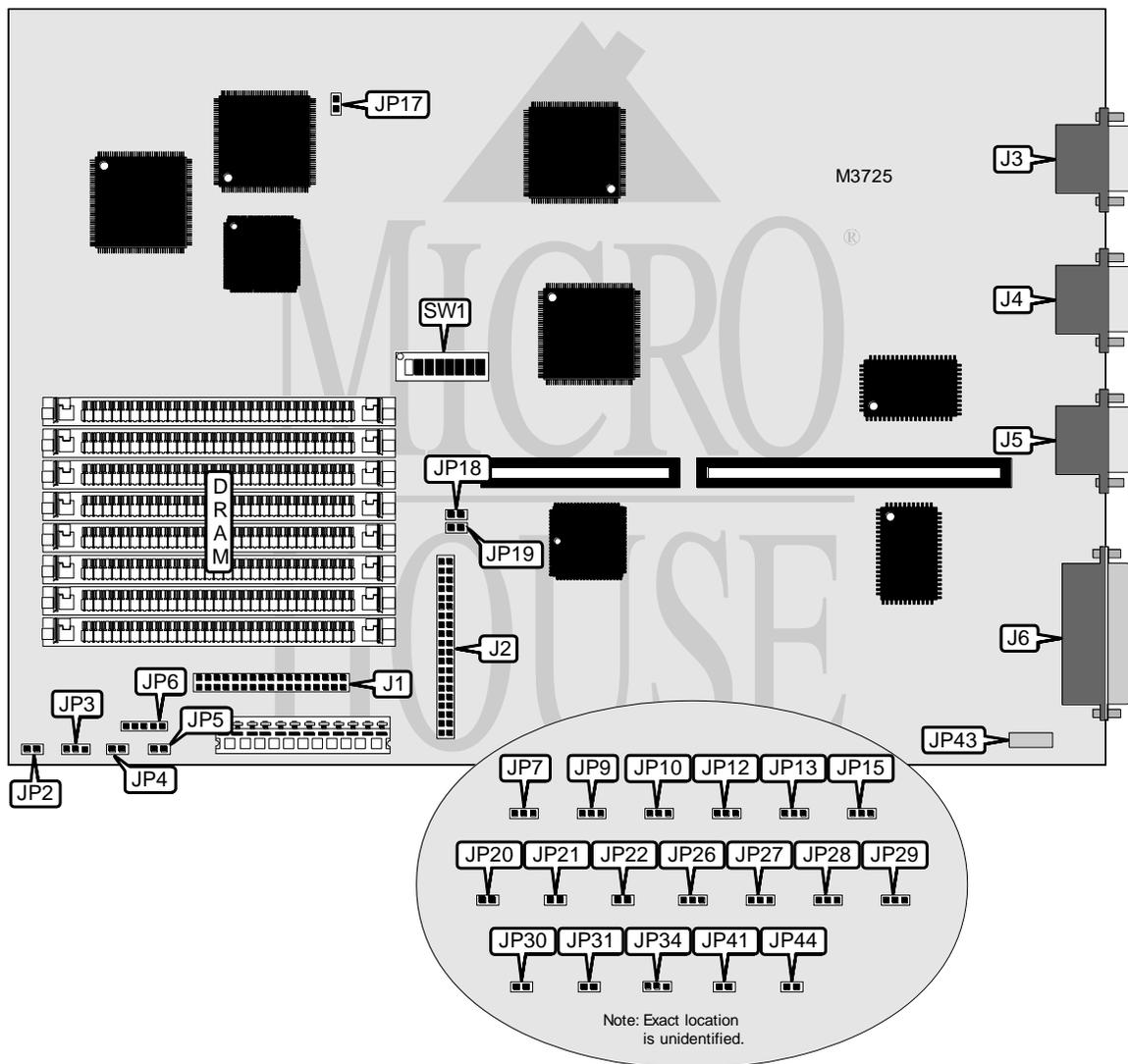
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UNIDENTIFIED
L A N O D E - 2 8 6

CONNECTIONS			
Function	**Label**	**Function**	**Label**
Floppy drive interface	J1	Reset switch	JP4
IDE interface	J2	Chassis fan power	JP5
Video port	J3	Keylock connector	JP6
Serial port 1	J4	Turbo switch	JP17
Serial port 2	J5	Turbo LED	JP18
Parallel port	J6	IDE interface LED	JP19
Network LED	JP2	Keyboard connector	JP43
Power LED	JP3		

USER CONFIGURABLE SETTINGS		
Setting	**Label**	**Position**
101SX chipset installed	JP10	Pins 2 & 3 closed
111SX chipset installed	JP10	Pins 1 & 2 closed
NPU synchronous with CPU	JP13	Pins 1 & 2 closed
NPU asynchronous with CPU	JP13	Pins 2 & 3 closed
BIOS type select 27128	JP15	Pins 2 & 3 closed
BIOS type select 27256	JP15	Pins 1 & 2 closed
Bus clock dependent	JP21	Closed
Bus clock independent	JP21	Open
í Factory configured - do not alter	JP22	Closed
On board video enabled	JP27	Pins 1 & 2 closed
On board video disabled	JP27	Pins 2 & 3 closed
C.G signal internal	JP28	Pins 1 & 2 closed
C.G signal external	JP28	Pins 2 & 3 closed
Select color display mode on color monitor	JP29	Pins 2 & 3 closed
Select color display emulation on monochrome monitor	JP29	Pins 1 & 2 closed
Zero wait states enabled	JP30	Closed
Zero wait states disabled	JP30	Open
Power good signal detect from power supply	JP34	Pins 1 & 2 closed
Power good signal detect from board	JP34	Pins 2 & 3 closed
384KB high memory enabled	SW1/1	On
384KB high memory disabled	SW1/1	Off
í Factory configured - do not alter	SW1/6	On
Select color monitor type	SW1/7	On
Select monochrome monitor type	SW1/7	Off
80287 coprocessor installed	SW1/8	On
80287 coprocessor not installed	SW1/8	Off
Note: The setting of JP29 is only valid when the internal video board is enabled.		

UNIDENTIFIED
L A N O D E - 2 8 6

PROCESSOR TYPE SELECTION					
Setting	**JP7**	**JP9**	**JP12**	**JP20**	**JP44**
80286	Closed	Pins 2 & 3 closed	Open	Pins 1 & 2 closed	Open
P9	Open	Pins 1 & 2 closed	Closed	Pins 2 & 3 closed	Closed

IDE INTERFACE CONFIGURATION		
Setting	**JP31**	**JP41**
Enabled	Closed	Closed
Disabled	Open	Open

DRAM CONFIGURATION				
Size	**Bank 0**	**Bank 1**	**Bank 2**	**Bank 3**
512KB	(2) 256K x 9	None	None	None
640KB	(2) 256K x 9	(2) 64K x 9	None	None
1MB[1]	(2) 256K x 9	(2) 256K x 9	None	None
1MB[2]	(2) 256K x 9	(2) 256K x 9	None	None
1.5MB	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9	None
2MB[1]	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9
2MB[2]	(2) 1M x 9	None	None	None
2MB[3]	(2) 1M x 9	None	None	None
3MB	(2) 256K x 9	(2) 256K x 9	(2) 1M x 9	None
4MB[1]	(2) 1M x 9	(2) 1M x 9	None	None
4MB[2]	(2) 1M x 9	(2) 1M x 9	None	None
4.5MB	(2) 1M x 9	(2) 1M x 9	(2) 256K x 9	None
5MB[1]	(2) 256K x 9	(2) 256K x 9	(2) 1M x 9	(2) 1M x 9
5MB[2]	(2) 1M x 9	(2) 1M x 9	(2) 256K x 9	(2) 256K x 9
6MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	None
8MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9
Note: The exact location of each bank is unidentified.				

UNIDENTIFIED
L A N O D E - 2 8 6

DRAM SWITCH SELECTION				
Size	SW1/1	SW1/2	SW1/3	SW1/4
512KB	Off	Off	Off	Off
640KB	Off	On	Off	Off
1MB[1]	Off	Off	Off	On
1MB[2]	Off	On	Off	On
1.5MB	Off	Off	On	Off
2MB[1]	Off	Off	On	On
2MB[2]	On	Off	Off	Off
2MB[3]	On	On	Off	Off
3MB	Off	On	On	Off
4MB[1]	On	Off	Off	On
4MB[2]	On	On	Off	On
4.5MB	On	On	On	Off
5MB[1]	Off	On	On	On
5MB[2]	On	On	On	On
6MB	On	Off	On	Off
8MB	On	Off	On	On