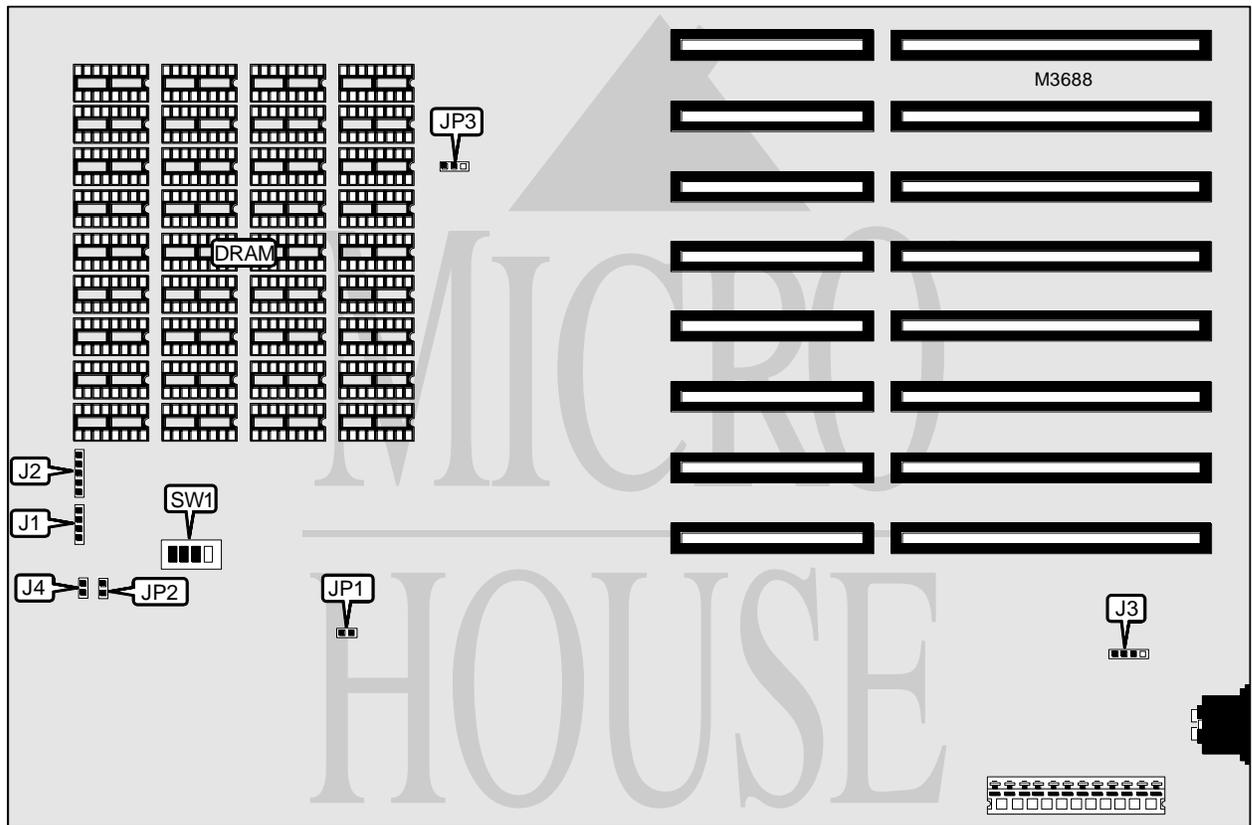


UNIDENTIFIED

80286-12

Processor	80286
Processor Speed	6/8/10/12MHz
Chip Set	Unidentified
Video Chip Set	None
Maximum Onboard Memory	1MB
Maximum Video Memory	None
Cache	None
BIOS	Unidentified
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	80287



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J1	Reset switch	J4
Power LED & keylock	J2	Turbo LED	JP1
External battery	J3	Turbo switch	JP2

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USER CONFIGURABLE SETTINGS		
Function	Label	Position
BIOS type select 27128	JP3	Pins 1 & 2 closed
BIOS type select 27256	JP3	Pins 2 & 3 closed
Monitor type select color	SW1/3	On
Monitor type select monochrome	SW1/3	Off

DRAM CONFIGURATION			
Size	Starting address	Bank 0	Bank 1
256KB	000000H - 03FFFFH	(18) 4164	(18) 4164
512KB	000000H - 07FFFFH	(18) 41256	None
640KB	000000H - 09FFFFH	(18) 41256	(18) 4164
1MB (A)	000000H - 07FFFFH	(18) 41256	(18) 41256
1MB (B)	100000H - 17FFFFH	(18) 41256	(18) 41256
1MB (C)	000000H - 09FFFFH	(18) 41256	(18) 41256
1MB (D)	100000H - 15FFFFH	(18) 41256	(18) 41256

Note: The location of banks 0 & 1 are unidentified.

DRAM SWITCH CONFIGURATION			
Size	SW1/1	SW1/2	SW1/4
256MB	On	On	Off
512MB	Off	On	Off
640MB	On	Off	Off
1MB (A)	Off	Off	Off
1MB (B)	Off	Off	Off
1MB (C)	On	Off	On
1MB (D)	On	Off	On