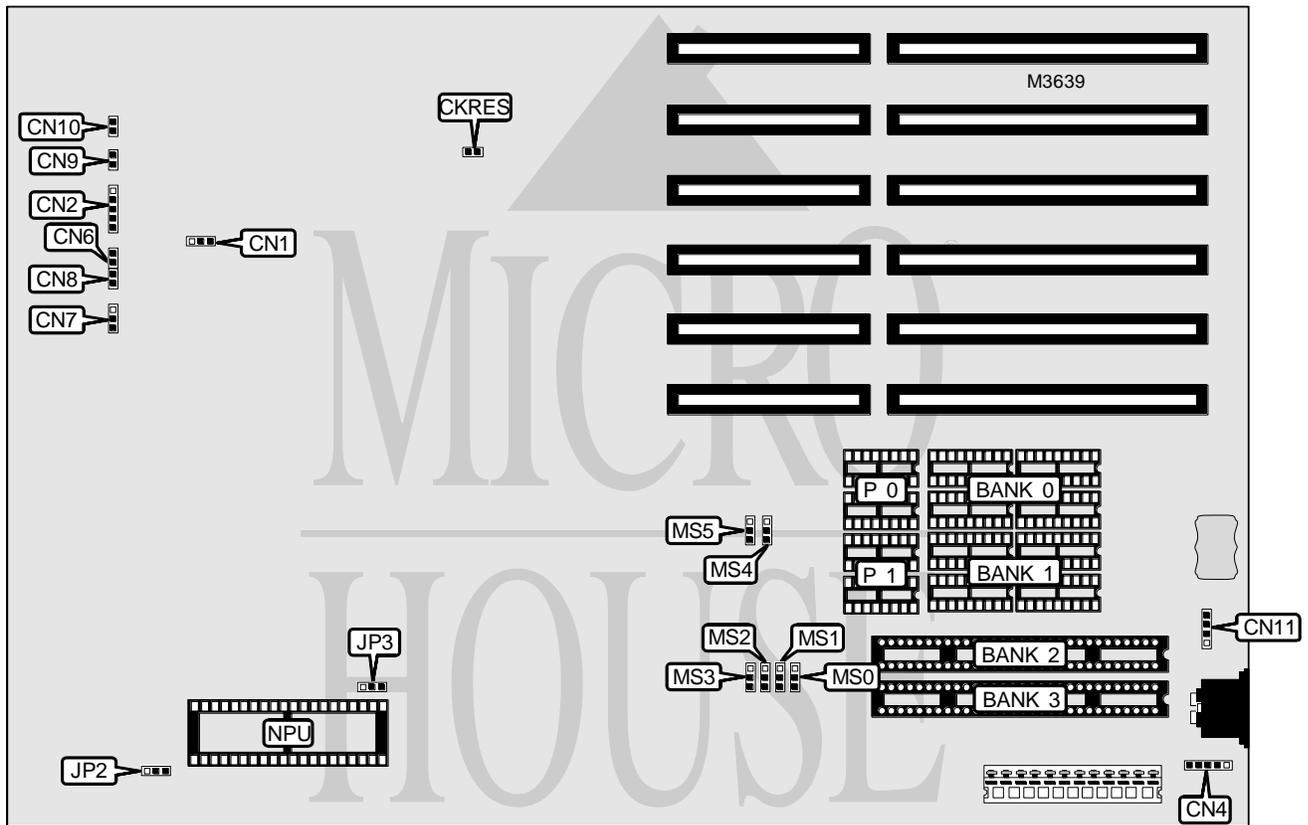


# UNIDENTIFIED

## AQ-216D (VER. 2.0)

<b>Processor</b>	80286
<b>Processor Speed</b>	16MHz
<b>Chip Set</b>	Unidentified
<b>Video Chip Set</b>	None
<b>Maximum Onboard Memory</b>	4MB
<b>Maximum Video Memory</b>	None
<b>Cache</b>	None
<b>BIOS</b>	AMI
<b>Dimensions</b>	330mm x 218mm
<b>I/O Options</b>	None
<b>NPU Options</b>	80287/80287XL



CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	CN2	Turbo switch	CN8
Auxiliary keyboard connector	CN4	Speaker	CN9
Turbo LED	CN6	Reset switch	CN10
7 segment display connector	CN7	External battery	CN11

*Continued on next page...*

# UNIDENTIFIED

## AQ-216D (VER. 2.0)

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USER CONFIGURABLE SETTINGS		
Function	Label	Position
Password enabled	CKRES	Open
Password disabled	CKRES	Closed
Monitor type select color	CN1	Pins 2 & 3 closed
Monitor type select monochrome	CN1	Pins 1 & 2 closed
NPU clock select external clock	JP2	Pins 1 & 2 closed
NPU clock select internal clock	JP2	Pins 2 & 3 closed
NPU clock frequency select CLK = frequency	JP3	Pins 1 & 2 closed
NPU clock frequency select CLK/3 (80287)	JP3	Pins 2 & 3 closed
NPU clock frequency select CLK/2 (80287XL)	JP3	Pins 2 & 3 closed

DRAM CONFIGURATION						
Size	Bank 0	P 0	Bank 1	P 1	Bank 2	Bank 3
512KB (A)	(4) 44256	(2) 41256	None	None	None	None
512KB (B)	None	None	None	None	(2) 256K x 9	None
1MB (A)	(4) 44256	(2) 41256	(4) 44256	(2) 41256	None	None
1MB (B)	None	None	None	None	(2) 256K x 9	(2) 256K x 9
1.5MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	(2) 256K x 9	None
2MB (A)	(4) 44256	(2) 41256	(4) 44256	(2) 41256	(2) 256K x 9	(2) 256K x 9
2MB (B)	None	None	None	None	(2) 1M x 9	None
4MB	None	None	None	None	(2) 1M x 9	(2) 1M x 9

DRAM CONFIGURATION						
Size	MS0	MS1	MS2	MS3	MS4	MS5
512KB (A)	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
512KB (B)	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
1MB (A)	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
1MB (B)	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
1.5MB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
2MB (A)	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
2MB (B)	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
4MB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position.