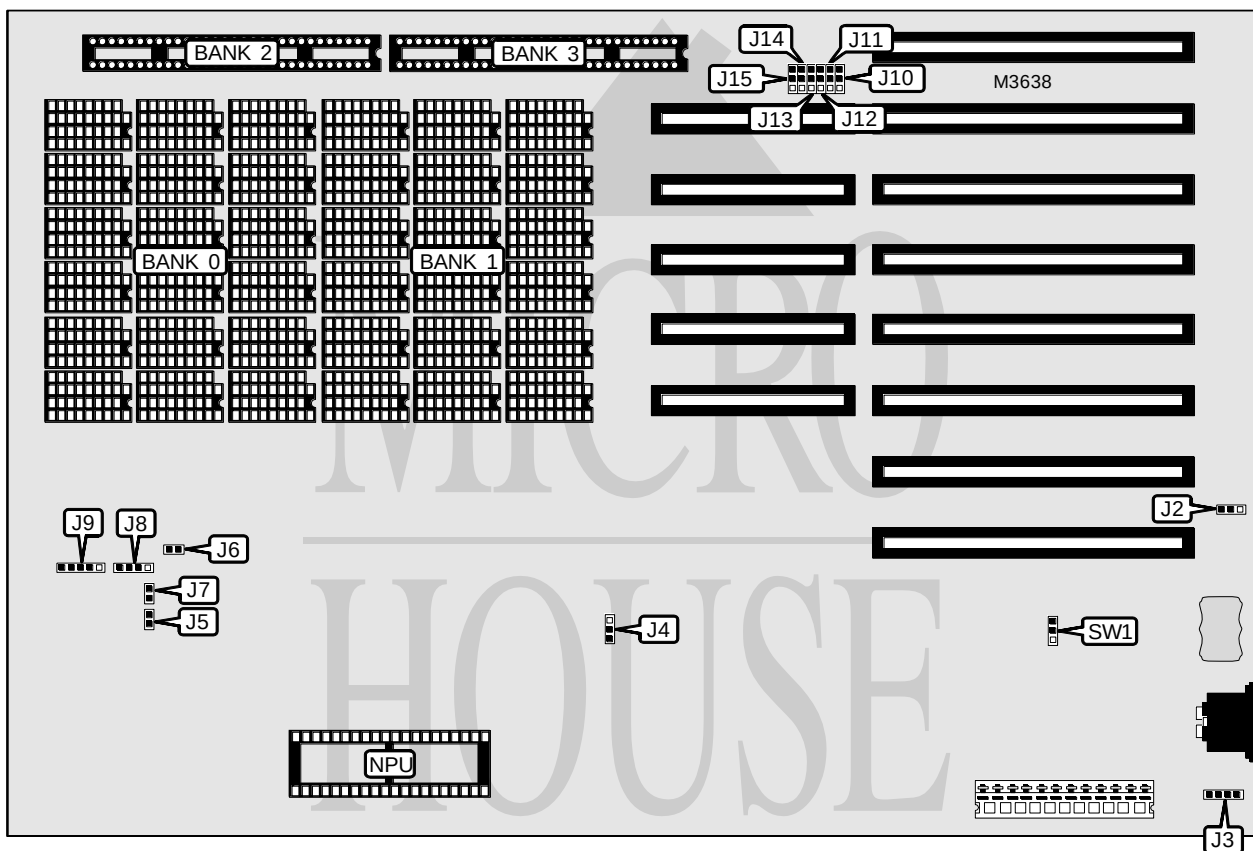


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286 NEAT (VER. 1.0B)

Processor	80286
Processor Speed	8/10/12/16MHz
Chip Set	C & T
Video Chip Set	None
Maximum Onboard Memory	8MB
Maximum Video Memory	None
Cache	None
BIOS	AMI
Dimensions	330mm x 220mm
I/O Options	None
NPU Options	80287



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J3	Turbo switch	J7
Turbo LED	J5	Speaker	J8
Reset switch	J6	Power LED & keylock	J9

Continued on next page. . .

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USER CONFIGURABLE SETTINGS		
Function	Label	Position
Battery type select internal	J2	Pins 1 & 2 closed
Battery type select external	J2	Pins 2 & 3 closed
í Factory configured - do not alter	J4	Pins 1 & 2 closed
Monitor type select color	SW1	Pins 1 & 2 closed
Monitor type select monochrome	SW1	Pins 2 & 3 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
512KB	(18) 41256	None	None	None
640KB	(18) 41256	(18) 4164	None	None
1MB	(18) 41256	(18) 41256	None	None
1.5MB	(18) 41256	(18) 41256	(2) 256K x 9	None
1.64MB	(18) 41256	(18) 4164	(2) 256K x 9	(2) 256K x 9
2MB	(18) 411000	None	None	None
2MB	(18) 41256	(18) 41256	(2) 256K x 9	(2) 256K x 9
3MB	(18) 41256	(18) 41256	(2) 1M x 9	None
4MB	(18) 411000	(18) 411000	None	None
4.64MB	(18) 41256	(18) 4164	(2) 1M x 9	(2) 1M x 9
5MB	(18) 41256	(18) 41256	(2) 1M x 9	(2) 1M x 9
6MB	(18) 411000	(18) 411000	(2) 1M x 9	None
8MB	(18) 411000	(18) 411000	(2) 1M x 9	(2) 1M x 9

DRAM JUMPER CONFIGURATION						
Banks used	J10	J11	J12	J13	J14	J15
2	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
4	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
Note: Pins designated should be in the closed position.						