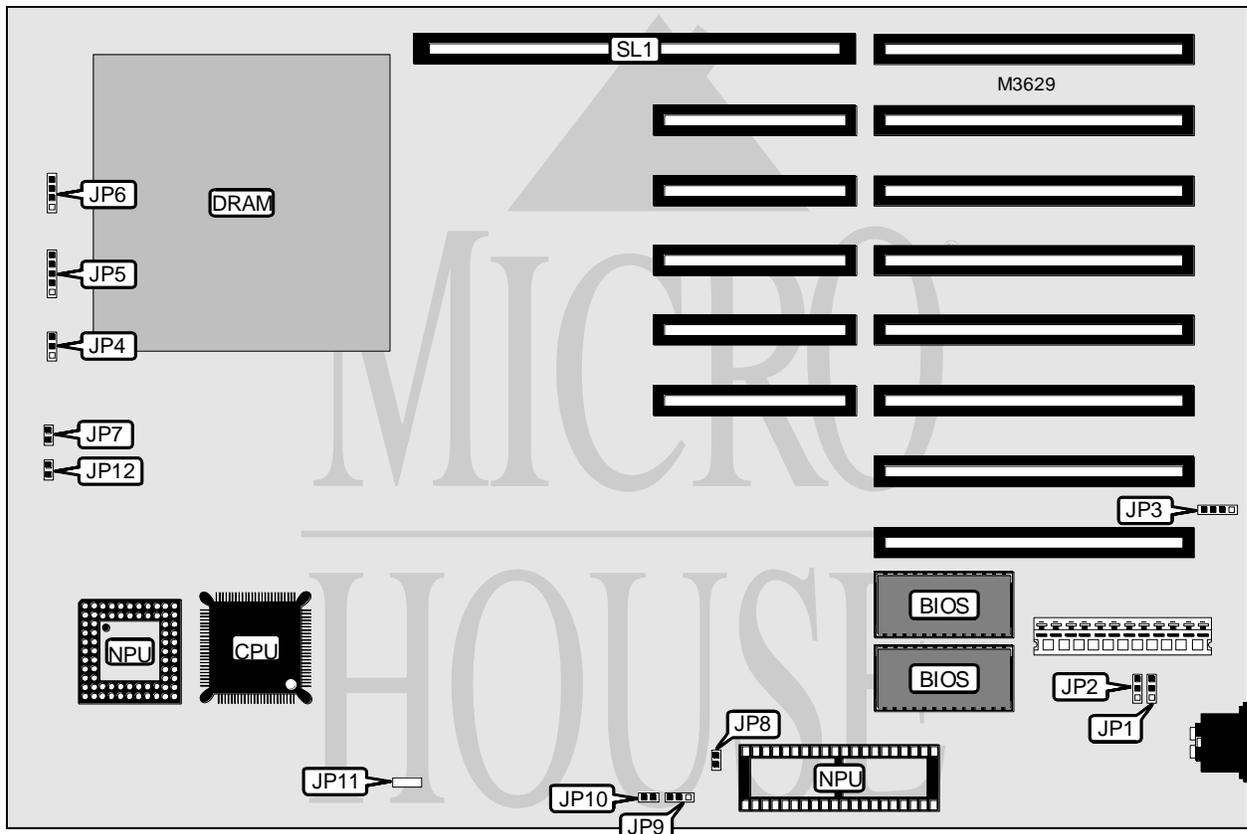


UNIDENTIFIED 80386 MOTHERBOARD (VER. 3.2)

Processor	80386DX
Processor Speed	16/20/25/30MHz
Chip Set	C & T
Video Chip Set	None
Maximum Onboard Memory	16MB (8MB on external memory card)
Maximum Video Memory	None
Cache	Unidentified
BIOS	AMI/Award/Phoenix
Dimensions	327mm x 218mm
I/O Options	External memory card, cache connector
NPU Options	80287/80387DX



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP3	Turbo LED	JP7
Turbo switch	JP4	Cache memory connector	JP11
Power LED & keylock	JP5	Reset switch	JP12
Speaker	JP6	External memory card	SL1

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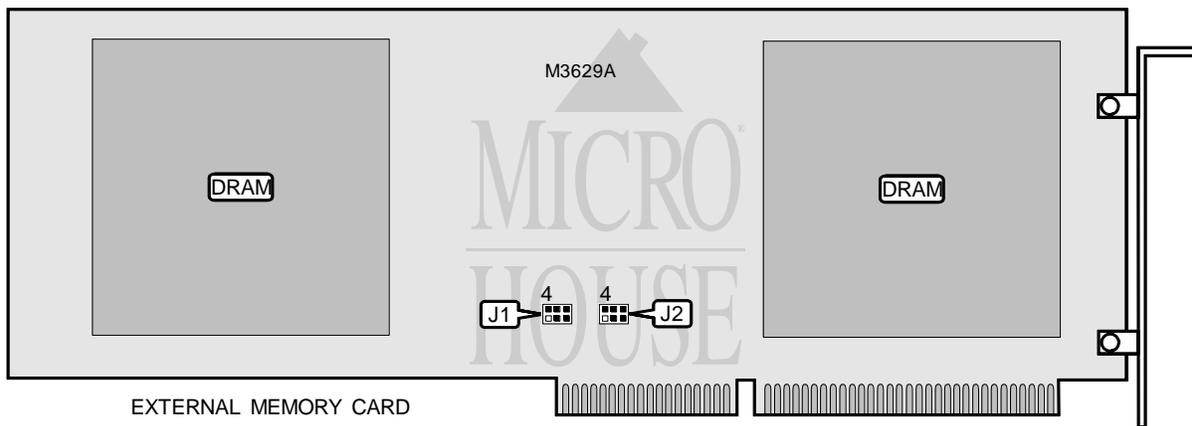
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USER CONFIGURABLE SETTINGS		
Function	Label	Position
í 8042 controller uses 23 pin control	JP1	Pins 1 & 2 closed
Circuit speed select by pressing CTRL, ALT + or -	JP1	Pins 2 & 3 closed
Monitor type select color/EGA/VGA	JP2	Pins 1 & 2 closed
Monitor type select monochrome	JP2	Pins 2 & 3 closed
Battery type select external	JP3	Closed
On board battery enabled	JP3	Pins 1 & 2 closed
On board battery disabled	JP3	Open
CMOS memory clear	JP3	Pins 3 & 4 closed
í Factory configured - do not alter	JP8	Closed

CACHE CONFIGURATION
Note: The size of the cache module is unidentified.

NPU SELECTION		
Type	JP9	JP10
None installed	Pins 1 & 2 closed	Open
80287	Pins 1 & 2 closed	Open
80387	Pins 2 & 3 closed	Closed



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DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
2MB	1MB	1MB	None	None
2MB	None	None	1MB	1MB
8MB	None	None	4MB	4MB
8MB	4MB	4MB	None	None
16MB	4MB	4MB	4MB	4MB

Note: The location of the banks are unidentified. The size of the chips are unidentified on the main board and memory card. Banks 1 & 2 are located on the main board. Banks 2 & 3 are located on the external memory card.

DRAM JUMPER CONFIGURATION		
Setting	J1	J2
No memory installed in main board	Pins 1 & 2, 4 & 5 closed	Pins 1 & 2, 4 & 5 closed
Memory installed on main board	Pins 2 & 3, 5 & 6 closed	Pins 2 & 3, 5 & 6 closed

Note: Either 2MB or 8MB will be installed on the memory card.