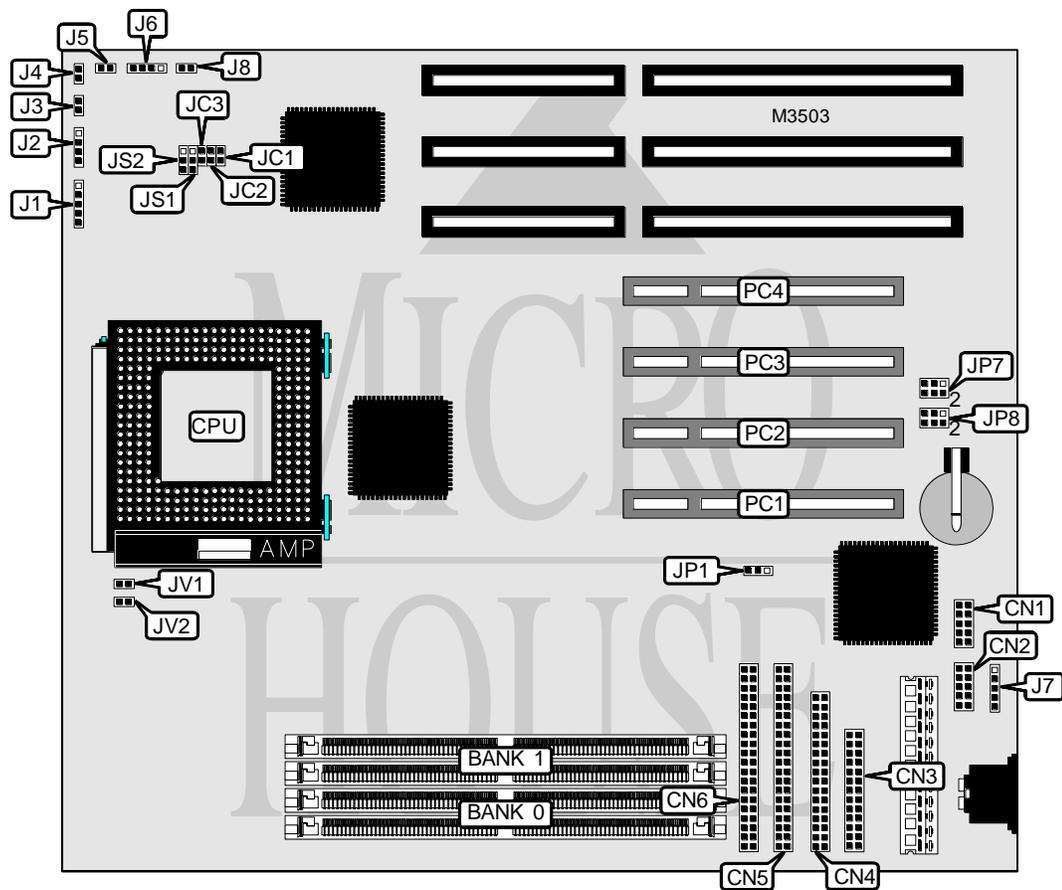


QDI COMPUTER, INC.

QDI-437FX-250

Processor	CX M1/Pentium
Processor Speed	75/90/100/120/133/150/166/180/200MHz
Chip Set	Intel
Video Chip Set	None
Maximum Onboard Memory	128MB
Maximum Video Memory	None
Cache	256KB
BIOS	Award
Dimensions	250mm x 220mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), IR connector
NPU Options	None



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QDI COMPUTER, INC.
QDI-437FX-250

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CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 2	CN1	Turbo LED	J3
Serial port 1	CN2	Reset switch	J4
Parallel port	CN3	IDE interface LED	J5
Floppy drive interface	CN4	IR connector	J6
IDE interface 2	CN5	PS/2 mouse interface	J7
IDE interface 1	CN6	Green PC connector	J8
Power LED & keylock	J1	32-bit PCI slots	PC1 - PC4
Speaker	J2		

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í CMOS memory normal operation	JP1	Pins 1 & 2 closed
CMOS memory clear	JP1	Pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 16M x 36	None
128MB	(2) 8M x 36	(2) 8M x 36

CACHE CONFIGURATION
Note: 256KB cache is factory installed and is not configurable. The location is unidentified.

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CPU SPEED SELECTION (CYRIX)							
CPU speed	Clock speed	Multiplier	JC1	JC2	JC3	JS1	JS2
100MHz	50MHz	2x	Closed	Closed	1 & 2	2 & 3	1 & 2
120MHz	60MHz	2x	Closed	Open	2 & 3	2 & 3	1 & 2

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL)							
CPU speed	Clock speed	Multiplier	JC1	JC2	JC3	JS1	JS2
75MHz	50MHz	1.5x	Closed	Closed	1 & 2	2 & 3	2 & 3
90MHz	60MHz	1.5x	Closed	Open	2 & 3	2 & 3	2 & 3
100MHz	66MHz	1.5x	Open	Closed	2 & 3	2 & 3	2 & 3
120MHz	60MHz	2x	Closed	Open	2 & 3	2 & 3	1 & 2
133MHz	66MHz	2x	Open	Closed	2 & 3	2 & 3	1 & 2
150MHz	60MHz	2.5x	Closed	Open	2 & 3	1 & 2	1 & 2
166MHz	66MHz	2.5x	Open	Closed	2 & 3	1 & 2	1 & 2
180MHz	60MHz	3x	Closed	Open	2 & 3	1 & 2	2 & 3
200MHz	66MHz	3x	Open	Closed	2 & 3	1 & 2	2 & 3

Note: Pins designated should be in the closed position. JC3 sets AT bus clock. Pins 1 & 2 are for PCICLK/3 & pins 2 & 3 are for PCICLK/4.

CPU TYPE SELECTION		
Type	JV1	JV2
P54C/P54CTB	Closed	Closed
P55C	Open	Open

DMA CHANNEL SELECTION		
Channel	JP7	JP8
0	Pins 1 & 2 closed	Pins 1 & 2 closed
1	Pins 3 & 4 closed	Pins 3 & 4 closed
3	Pins 5 & 6 closed	Pins 5 & 6 closed