EVEREX SYSTEMS, INC.
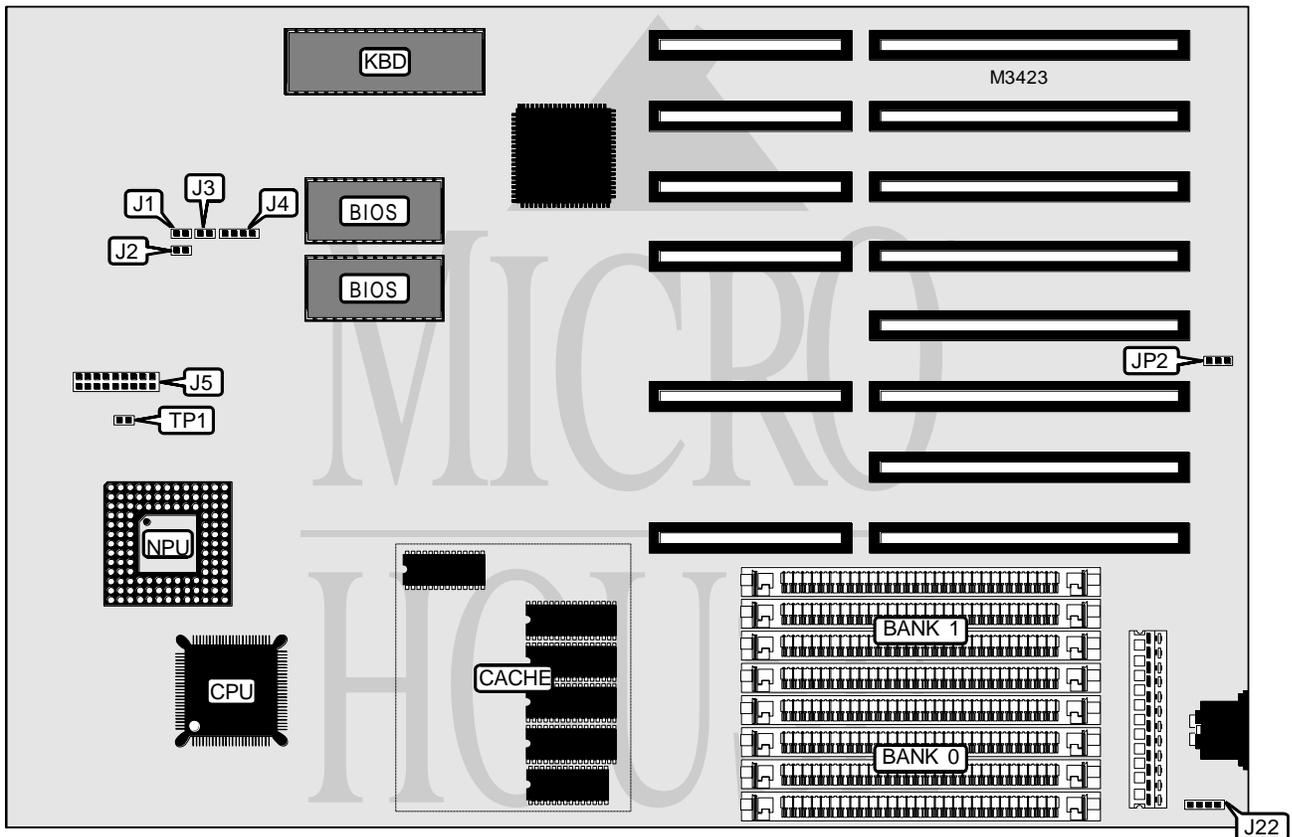# 386-25/33 (EV-18119)

Processor   80386DX
Processor Speed   25/33MHz
Chip Set   Unidentified
Video Chip Set   None
Maximum Onboard Memory   32MB
Maximum Video Memory   None
Cache   128KB
BIOS   Unidentified
Dimensions   330mm x 218mm
I/O Options   None
NPU Options   80387DX/3167

CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
Reset switch	J1	Speaker	J4
Turbo switch	J2	Front panel connector	J5
Turbo LED	J3	External battery	J22

Continued on next page. . .

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USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
EPROM size select - do not alter	JP2	Unidentified
Engineering test point - do not alter	TP1	Unidentified

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	None
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	None
5MB	(4) 256K x 9	(4) 1M x 9
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	None
17MB	(4) 256K x 9	(4) 4M x 9
17MB	(4) 4M x 9	(4) 256K x 9
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION
Note: 128KB cache is factory installed and is not configurable.