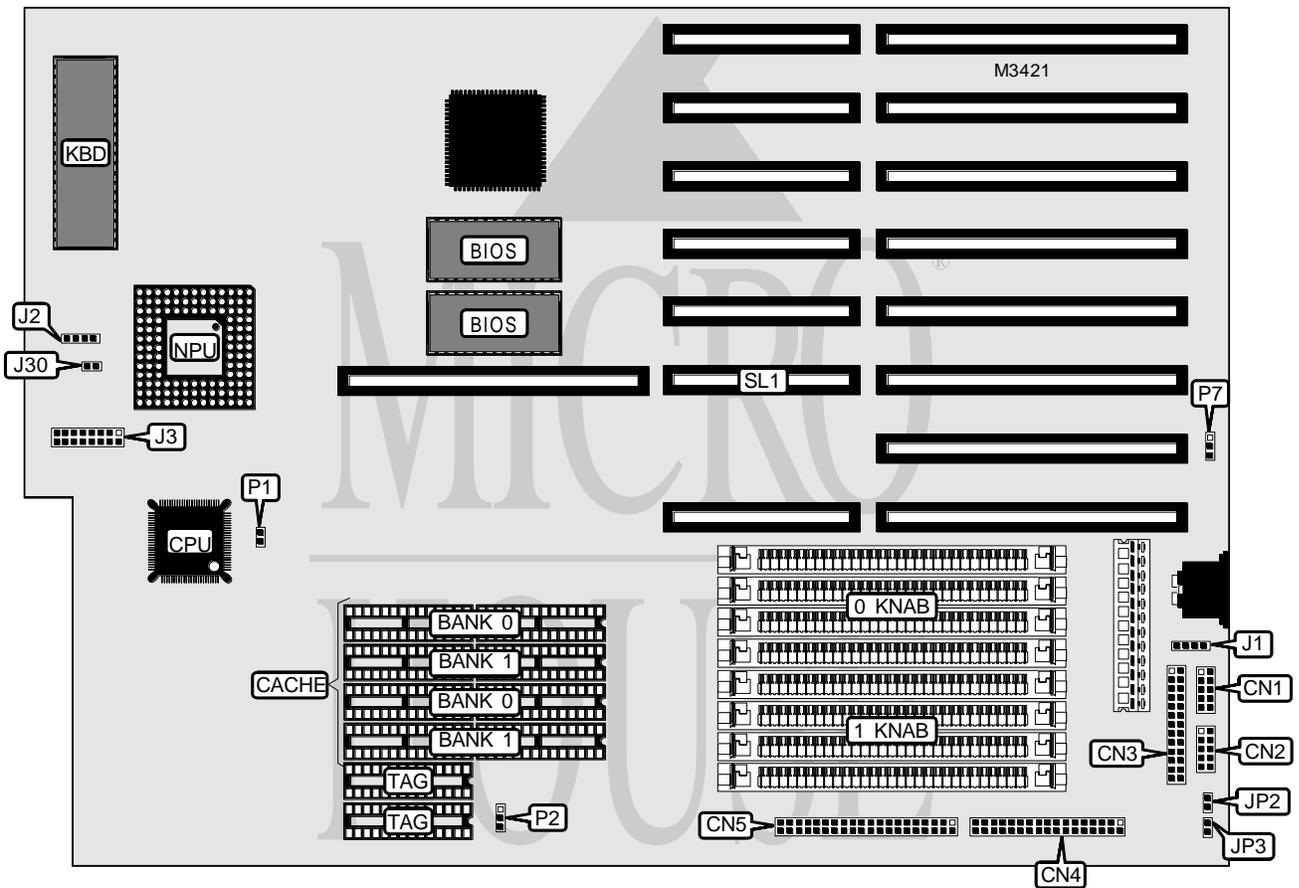


EVEREX SYSTEMS, INC.

AGI 3000R/S 386-25/33 (EV-18104)

<b>Processor</b>	80386
<b>Processor Speed</b>	25/33MHz
<b>Chip Set</b>	Unidentified
<b>Video Chip Set</b>	None
<b>Maximum Onboard Memory</b>	32MB
<b>Maximum Video Memory</b>	None
<b>Cache</b>	128/256KB
<b>BIOS</b>	Unidentified
<b>Dimensions</b>	330mm x 218mm
<b>I/O Options</b>	External memory card, floppy drive interface, IDE interface, parallel port, serial ports (2)
<b>NPU Options</b>	80387/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 2	CN1	External battery	J1
Serial port 1	CN2	Speaker	J2
Parallel port	CN3	Front panel connector	J3
Floppy drive interface	CN4	IDE interface LED	J30
IDE interface	CN5	External memory card	SL1

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**AGI 3000R/S 386-25/33 (EV-18104)**

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USER CONFIGURABLE SETTINGS		
Function	Label	Position
í Factory configured - do not alter	P1	Open
í Factory configured - do not alter	P7	Pins 2 & 3 closed
í Factory configured - do not alter	JP2	Open
í Factory configured - do not alter	JP3	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	None
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	None
5MB	(4) 256K x 9	(4) 1M x 9
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	None
17MB	(4) 256K x 9	(4) 4M x 9
17MB	(4) 4M x 9	(4) 256K x 9
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

Note: To increase memory, install an EV-18107B memory board at SL1.

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG 0/1
128KB	(4) 32K x 8	None	Unidentified
256KB	(4) 32K x 8	(4) 32K x 8	Unidentified

Note: The size and location of the TAG chip for 128KB is unidentified. The location of the 32K x 8 TAG chip for 256KB is unidentified.

CACHE JUMPER CONFIGURATION	
Size	P2
128KB	Pins 2 & 3 closed
256KB	Pins 1 & 2 closed