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Processor	CX486S/80486SX/80487SX/CX486M6(WB)/CX486M6(WT)/80486DX/80486DX2/P24T/P24T(WB)/P24T(WT)
Processor Speed	25/33/40/50(internal)/50/66(internal)MHz
Chip Set	UNI
Video Chip Set	None
Maximum Onboard Memory	128MB
Maximum Video Memory	None
Cache	32/64/128/256KB
BIOS	AMI
Dimensions	254mm x 218mm
I/O Options	32-bit VESA local bus slots (3)
NPU Options	None
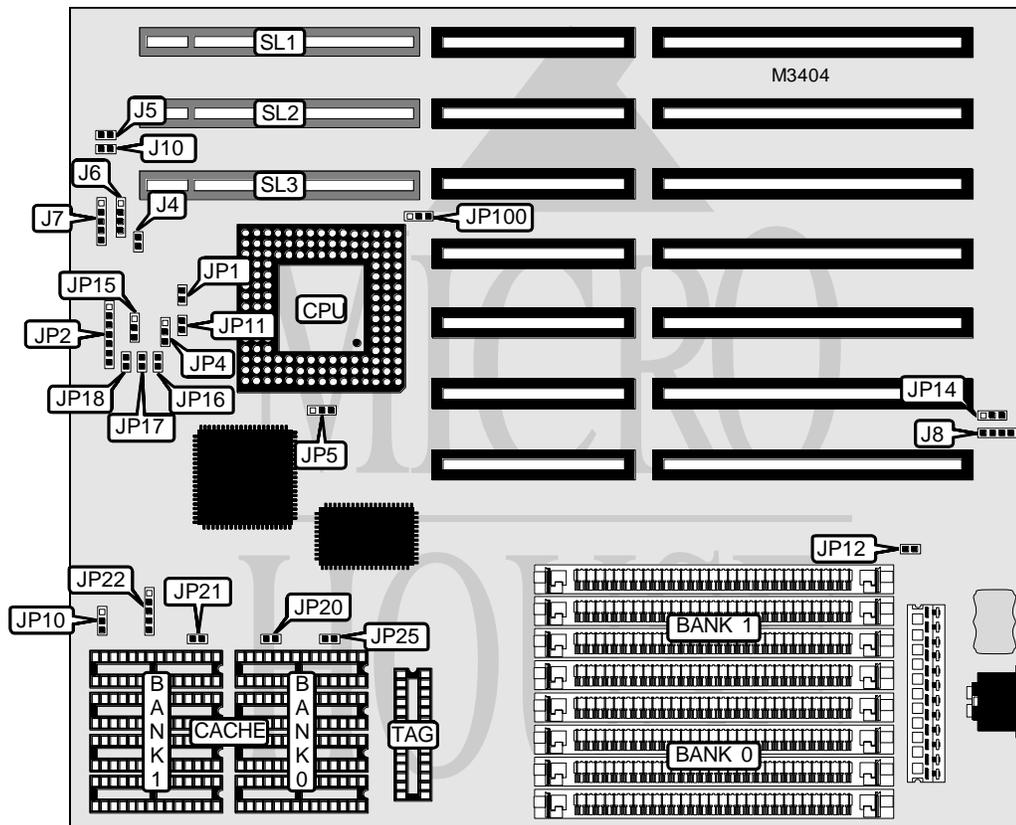
CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
Turbo switch	J4	External battery	J8
Turbo LED	J5	Reset switch	J10
Speaker	J6	32-bit VESA local bus slots	SL1 - SL3
Power LED & keylock	J7		

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USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
Monitor type select monochrome	JP12	Open
Monitor type select CGA	JP12	Closed
Monitor type select VGA/SVGA/EGA/XGA	JP12	Any setting
í CMOS memory normal operation	JP14	Pins 1 & 2 closed
CMOS memory clear	JP14	Pins 2 & 3 closed
í Factory configured - do not alter	JP100	Pins 1 & 2 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	None
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	None
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	None
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9
64MB	(4) 16M x 9	None
80MB	(4) 16M x 9	(4) 4M x 9
128MB	(4) 16M x 9	(4) 16M x 9

CACHE CONFIGURATION			
Size	**Bank 0**	**Bank 1**	**TAG**
32KB	(4) 8K x 8	None	(1) 8K x 8
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	None	(1) 8K/16K/32Kx 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 16K/32Kx 8

CACHE JUMPER CONFIGURATION				
Size	**JP20**	**JP21**	**JP22**	**JP25**
32KB	Open	Open	2 & 3	Open
64KB	Open	Closed	1 & 2	Open
128KB	Closed	Closed	1 & 2, 3 & 4	Open
256KB	Closed	Closed	1 & 2, 4 & 5	Closed
Note: Pins designated should be in the closed position.				

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CPU SPEED SELECTION			
Speed	**JP16**	**JP17**	**JP18**
25MHz	Closed	Closed	Open
33MHz	Closed	Open	Closed
40MHz	Open	Closed	Open
50iMHz	Closed	Closed	Open
50MHz	Open	Open	Closed
66iMHz	Closed	Open	Closed

CPU TYPE SELECTION	
Type	**JP2**
CX486S	Pins 5 & 6 closed
80486SX	Pins 5 & 6 closed
80487SX	Pins 1 & 2, 4 & 5, 6 & 7 closed
80486DX	Pins 2 & 3, 4 & 5, 6 & 7 closed
80486DX2	Pins 2 & 3, 4 & 5, 6 & 7 closed
P24T	Pins 1 & 2, 4 & 5, 6 & 7 closed

CPU TYPE SELECTION					
Type	**JP1**	**JP4**	**JP5**	**JP10**	**JP11**
CX486M6 (WB)	Open	1 & 2	2 & 3	2 & 3	Open
CX486M6 (WT)	Open	2 & 3	1 & 2	1 & 2	Open
80486DX	Open	2 & 3	1 & 2	1 & 2	Open
80486DX2	Open	2 & 3	1 & 2	1 & 2	Open
P24T (WB)	Closed	2 & 3	1 & 2	1 & 2	Closed
P24T (WT)	Open	2 & 3	1 & 2	1 & 2	Open
Note: Pins designated should be in the closed position.					

VL-BUS SPEED SELECTION	
Setting	**JP15**
<= 33MHz	Pins 1 & 2 closed
>33 MHz	Pins 2 & 3 closed