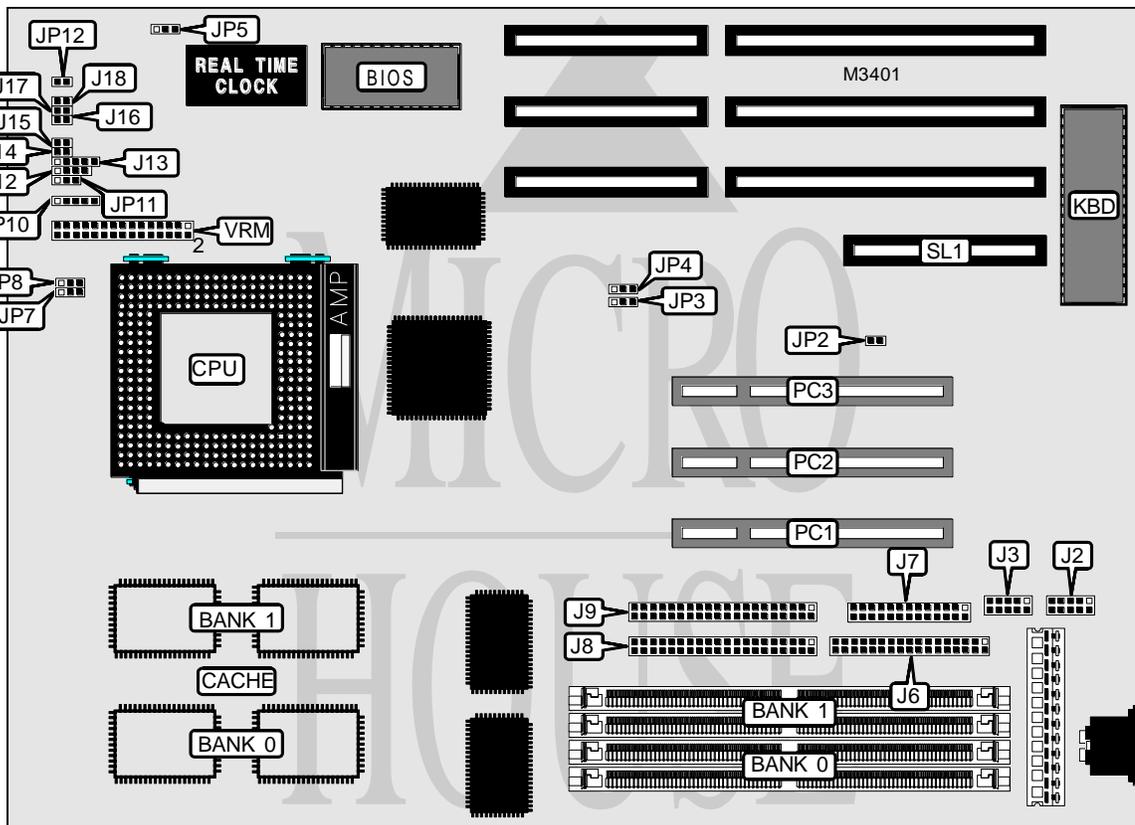


# EFA CORPORATION P55TPIO-B/P55TSPIO-B

|                               |  |
|-------------------------------|--|
| <b>Processor</b>              | CX M1/Pentium  |
| <b>Processor Speed</b>        | 75/90/100/120/133/150/166/200MHz   |
| <b>Chip Set</b>               | Intel  |
| <b>Video Chip Set</b>         | None   |
| <b>Maximum Onboard Memory</b> | 128MB  |
| <b>Maximum Video Memory</b>   | None   |
| <b>Cache</b>                  | 256/512KB  |
| <b>BIOS</b>                   | Award  |
| <b>Dimensions</b>             | 290mm x 220mm  |
| <b>I/O Options</b>            | 32-bit PCI slots (3), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, serial ports (2), audio bus slot, VRM connector |
| <b>NPU Options</b>            | None   |



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# EFA CORPORATION

## P55TPIO-B/P55TSPIO-B

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| CONNECTIONS            |          |                    |           |
|------------------------|----------|--------------------|-----------|
| Purpose                | Location | Purpose            | Location  |
| Serial port 2          | J2       | IDE interface LED  | J14       |
| Serial port 1          | J3       | Green PC connector | J15       |
| Floppy drive interface | J6       | Turbo LED          | J16       |
| Parallel port          | J7       | Turbo switch       | J17       |
| IDE interface 2        | J8       | Reset switch       | J18       |
| IDE interface 1        | J9       | 32-bit PCI slots   | PC1 - PC3 |
| Speaker                | J12      | Audio bus slot     | SL1       |
| Power LED & keylock    | J13      | VRM connector      | VRM       |

| USER CONFIGURABLE SETTINGS     |       |                   |
|--------------------------------|-------|-------------------|
| Function                       | Label | Position          |
| Sound interface enabled        | JP2   | Open              |
| Sound interface disabled       | JP2   | Closed            |
| í Flash BIOS voltage select 5v | JP5   | Pins 1 & 2 closed |
| Flash BIOS voltage select 12v  | JP5   | Pins 2 & 3 closed |
| í CMOS memory normal operation | JP12  | Open              |
| CMOS memory clear              | JP12  | Closed            |

| DRAM CONFIGURATION |               |               |
|--------------------|---------------|---------------|
| Size               | Bank 0        | Bank 1        |
| 4MB                | (2) 512K x 36 | None          |
| 8MB                | (2) 1M x 36   | None          |
| 8MB                | (2) 512K x 36 | (2) 512K x 36 |
| 12MB               | (2) 1M x 36   | (2) 512K x 36 |
| 12MB               | (2) 512K x 36 | (2) 1M x 36   |
| 16MB               | (2) 2M x 36   | None          |
| 16MB               | (2) 1M x 36   | (2) 1M x 36   |
| 20MB               | (2) 2M x 36   | (2) 512K x 36 |
| 20MB               | (2) 512K x 36 | (2) 2M x 36   |
| 24MB               | (2) 2M x 36   | (2) 1M x 36   |
| 24MB               | (2) 1M x 36   | (2) 2M x 36   |
| 32MB               | (2) 4M x 36   | None          |
| 32MB               | (2) 2M x 36   | (2) 2M x 36   |
| 36MB               | (2) 4M x 36   | (2) 512K x 36 |
| 36MB               | (2) 512K x 36 | (2) 4M x 36   |
| 40MB               | (2) 4M x 36   | (2) 1M x 36   |
| 40MB               | (2) 1M x 36   | (2) 4M x 36   |
| 48MB               | (2) 4M x 36   | (2) 2M x 36   |
| 48MB               | (2) 2M x 36   | (2) 4M x 36   |
| 64MB               | (2) 8M x 36   | None          |
| 64MB               | (2) 4M x 36   | (2) 4M x 36   |
| 68MB               | (2) 8M x 36   | (2) 512K x 36 |

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# EFA CORPORATION

## P55TPIO-B/P55TSPIO-B

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| DRAM CONFIGURATION (CON'T) |               |             |
|----------------------------|---------------|-------------|
| Size                       | Bank 0        | Bank 1      |
| 68MB                       | (2) 512K x 36 | (2) 8M x 36 |
| 72MB                       | (2) 8M x 36   | (2) 1M x 36 |
| 72MB                       | (2) 1M x 36   | (2) 8M x 36 |
| 80MB                       | (2) 8M x 36   | (2) 2M x 36 |
| 80MB                       | (2) 2M x 36   | (2) 8M x 36 |
| 96MB                       | (2) 8M x 36   | (2) 4M x 36 |
| 96MB                       | (2) 4M x 36   | (2) 8M x 36 |
| 128MB                      | (2) 8M x 36   | (2) 8M x 36 |

Note: Board also accepts x 32 SIMMs.

| CACHE CONFIGURATION |              |              |                 |
|---------------------|--------------|--------------|-----------------|
| Size                | Bank 0       | Bank 1       | TAG             |
| 256KB               | (2) 32K x 32 | None         | (1) 16K/32K x 8 |
| 512KB               | (2) 32K x 32 | (2) 32K x 32 | (1) 16K/32K x 8 |

Note: The location of the TAG is unidentified.

| CPU SPEED SELECTION |                   |                   |                   |
|---------------------|-------------------|-------------------|-------------------|
| Speed               | JP3               | JP4               | JP11              |
| 75MHz               | Pins 2 & 3 closed | Pins 2 & 3 closed | Pins 1 & 2 closed |
| 90MHz               | Pins 2 & 3 closed | Pins 1 & 2 closed | Pins 2 & 3 closed |
| 100MHz              | Pins 1 & 2 closed | Pins 2 & 3 closed | Pins 2 & 3 closed |
| 120MHz              | Pins 2 & 3 closed | Pins 1 & 2 closed | Pins 2 & 3 closed |
| 133MHz              | Pins 1 & 2 closed | Pins 2 & 3 closed | Pins 2 & 3 closed |
| 150MHz              | Pins 2 & 3 closed | Pins 2 & 3 closed | Pins 1 & 2 closed |
| 166MHz              | Pins 1 & 2 closed | Pins 2 & 3 closed | Pins 2 & 3 closed |
| 180MHz              | Pins 2 & 3 closed | Pins 1 & 2 closed | Pins 2 & 3 closed |
| 200MHz              | Pins 1 & 2 closed | Pins 2 & 3 closed | Pins 2 & 3 closed |

| CPU MULTIPLIER SELECTION (CYRIX) |                   |                   |
|----------------------------------|-------------------|-------------------|
| Multiplier                       | JP7               | JP8               |
| 1x                               | Pins 2 & 3 closed | Pins 2 & 3 closed |
| 2x                               | Pins 2 & 3 closed | Pins 1 & 2 closed |
| 3x                               | Pins 1 & 2 closed | Pins 2 & 3 closed |
| 4x                               | Pins 1 & 2 closed | Pins 1 & 2 closed |

| CPU MULTIPLIER SELECTION (INTEL) |                   |                   |
|----------------------------------|-------------------|-------------------|
| Multiplier                       | JP7               | JP8               |
| 1.5x                             | Pins 1 & 2 closed | Pins 1 & 2 closed |
| 2x                               | Pins 2 & 3 closed | Pins 1 & 2 closed |
| 2.5x                             | Pins 2 & 3 closed | Pins 2 & 3 closed |
| 3x                               | Pins 1 & 2 closed | Pins 2 & 3 closed |

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**EFA CORPORATION**  
**P55TPIO-B/P55TSPIO-B***... continued from previous page*

| <b>CPU VOLTAGE SELECTION</b> |                   |
|------------------------------|-------------------|
| <b>Voltage</b>               | <b>JP10</b>       |
| 3.3v (STD)                   | Pins 1 & 2 closed |
| 3.4v (VR)                    | Pins 2 & 3 closed |
| 3.5v (VRE)                   | Pins 4 & 5 closed |
| 3.3v/2.5v                    | VRM installed     |

| <b>VRM SELECTION</b> |  |
|----------------------|--|
| <b>Type</b>          | <b>VRM</b>                                     |
| All P54C             | Pins 17 & 19, 18 & 20, 21 & 23, 22 & 24 closed |
| P55C                 | VRM installed                                  |