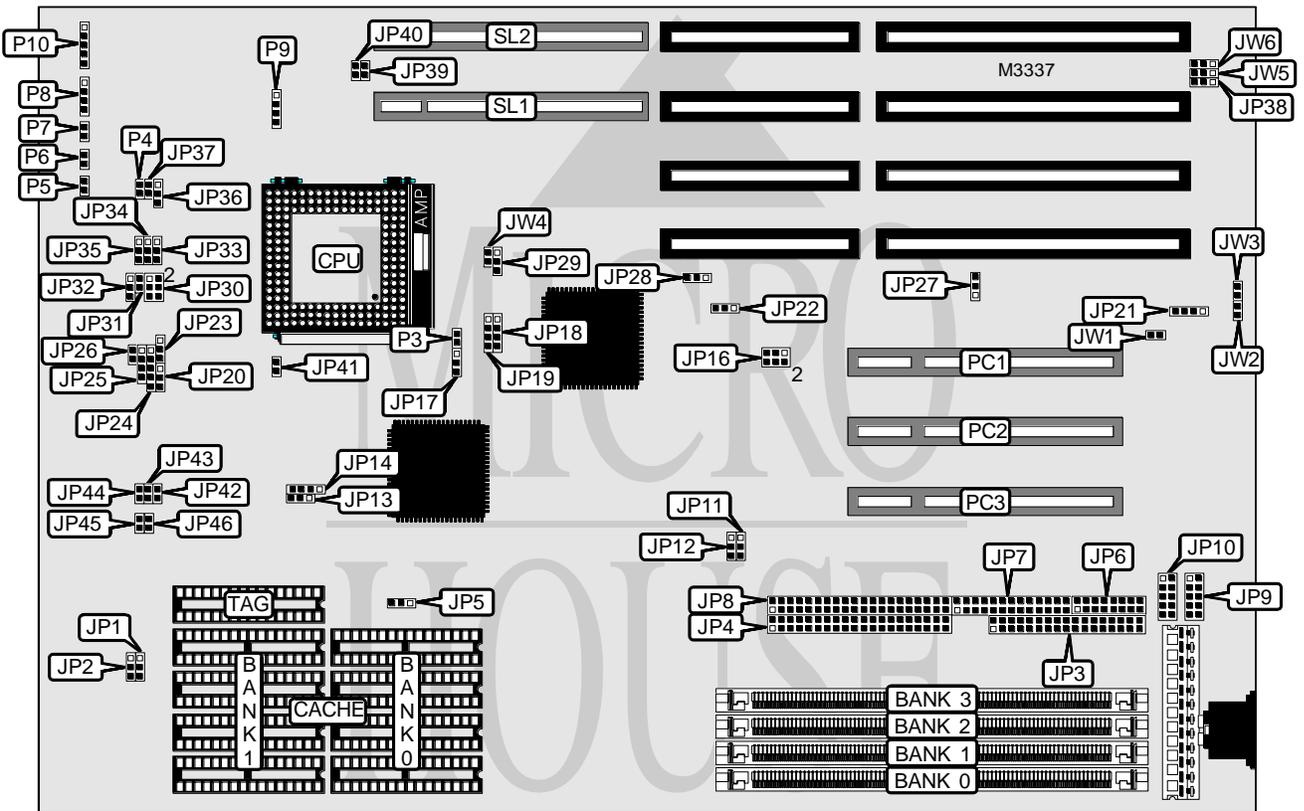


OCEAN INFORMATION SYSTEMS, INC.

HIPPO 12 486

Processor	CX486S/80486SX/SL80486SX/CX486D/UMCU5S/AM486DXL/ 80486DX/SL80486DX/UMCU5DS/AM486DX2/(SL)AM486DX2/ 80486DX2/SL80486DX2/AM486DX4/(SL)AM486DX4/80486DX4/P24D/P24T
Processor Speed	25/33/40/50(internal)/50/66(internal)/80(internal)/100(internal)MHz
Chip Set	Unidentified
Video Chip Set	None
Maximum Onboard Memory	256MB
Maximum Video Memory	None
Cache	128/256/512KB
BIOS	Unidentified
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slots (2), 32-bit PCI slots (3), floppy drive interface, game interface, IDE interfaces (2), parallel port, serial ports (2)
NPU Options	None



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CONNECTIONS			
Function	Label	Function	Label
Floppy drive interface	JP3	Turbo switch	P5
IDE interface 2	JP4	Turbo LED	P6
Game interface	JP6	Reset switch	P7
Parallel port	JP7	Speaker	P8
IDE interface 1	JP8	IDE interface LED	P9
Serial port 1	JP9	Power LED & keylock	P10
Serial port 2	JP10	32-bit PCI slots	PC1 - PC3
External battery	JP21	32-bit VESA local bus slots	SL1 & SL2

USER CONFIGURABLE SETTINGS		
Setting	Label	Position
IDE interface enabled	JP11	Pins 1 & 2 closed
IDE interface disabled	JP11	Pins 2 & 3 closed
í Factory configured - do not alter	JP12	Pins 2 & 3 closed
í Factory configured - do not alter	JP13	Pins 1 & 2 closed
í Factory configured - do not alter	JP17	Pins 1 & 2 closed
CMOS memory normal operation	JP21	Pins 2 & 3 closed
CMOS memory clear	JP21	Pins 3 & 4 closed
Battery type select external	JP21	Closed
í Factory configured - do not alter	JP22	Pins 1 & 2 closed
í Factory configured - do not alter	JP23	Pins 2 & 3 closed
í Factory configured - do not alter	JP27	Pins 2 & 3 closed
í Factory configured - do not alter	JP28	Open
í Factory configured - do not alter	JP29	Open
í Factory configured - do not alter	JP38	Pins 2 & 3 closed
í Factory configured - do not alter	JW1	Closed
í Factory configured - do not alter	JW2	Open
í Factory configured - do not alter	JW3	Open
í Factory configured - do not alter	JW4	Closed
í Factory configured - do not alter	JW5	Pins 1 & 2 closed
í Factory configured - do not alter	JW6	Pins 1 & 2 closed
í Factory configured - do not alter	P3	Open
Jumper information unavailable	P4	N/A

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DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
4MB	(1) 1M x 36	None	None	None
8MB	(1) 2M x 36	None	None	None
8MB	(1) 1M x 36	(1) 1M x 36	None	None
12MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	None
12MB	(1) 1M x 36	None	(1) 2M x 36	None
16MB	(1) 1M x 36			
16MB	(1) 1M x 36	(1) 1M x 36	(1) 2M x 36	None
16MB	(1) 2M x 36	(1) 2M x 36	None	None
16MB	(1) 4M x 36	None	None	None
20MB	(1) 1M x 36	None	(1) 4M x 36	None
20MB	(1) 2M x 36	(1) 2M x 36	(1) 1M x 36	None
24MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	None
24MB	(1) 2M x 36	None	(1) 4M x 36	None
24MB	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36	None
32MB	(1) 4M x 36	(1) 4M x 36	None	None
32MB	(1) 2M x 36			
36MB	(1) 4M x 36	(1) 4M x 36	(1) 1M x 36	None
40MB	(1) 4M x 36	(1) 4M x 36	(1) 2M x 36	None
40MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36
48MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	None
48MB	(1) 8M x 36	None	(1) 4M x 36	None
52MB	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
52MB	(1) 1M x 36	(1) 4M x 36	(1) 8M x 36	None
64MB	(1) 4M x 36			
64MB	(1) 4M x 36	(1) 4M x 36	(1) 8M x 36	None
64MB	(1) 8M x 36	(1) 8M x 36	None	None
80MB	(1) 8M x 36	(1) 8M x 36	(1) 4M x 36	None
96MB	(1) 4M x 36	(1) 4M x 36	(1) 16M x 36	None
96MB	(1) 8M x 36	None	(1) 16M x 36	None
112MB	(1) 4M x 36	(1) 8M x 36	(1) 16M x 36	None
128MB	(1) 8M x 36			
160MB	(1) 16M x 36	(1) 16M x 36	(1) 8M x 36	None
256MB	(1) 16M x 36			

Note: Board also accepts x 32 SIMMs.

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
128KB	(4) 32K x 8	None	(1) 32K x 8
256KB (A)	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8
256KB (B)	(4) 64K x 8	None	(1) 32K x 8
512KB	(4) 128K x 8	None	(1) 32K x 8

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CACHE JUMPER CONFIGURATION		
Size	JP5	JP14
128KB	Pins 2 & 3 closed	Open
256KB (A)	Pins 1 & 2 closed	Pins 1 & 2 closed
256KB (B)	Pins 2 & 3 closed	Pins 1 & 2 closed
512KB	Pins 2 & 3 closed	Pins 1 & 2, 3 & 4 closed

CPU SPEED SELECTION	
Speed	JP16
25MHz	Pins 5 & 6 closed
33MHz	Pins 1 & 2, 3 & 4, 5 & 6 closed
40MHz	Pins 3 & 4, 5 & 6 closed
50iMHz	Pins 5 & 6 closed
50MHz	Pins 1 & 2 closed
66iMHz	Pins 1 & 2, 3 & 4, 5 & 6 closed
80iMHz	Pins 3 & 4, 5 & 6 closed
100iMHz	Pins 1 & 2, 3 & 4, 5 & 6 closed

CPU TYPE SELECTION					
Type	JP1	JP2	JP18	JP19	JP20
CX486S	2 & 3	1 & 2	2 & 3	Open	Open
80486SX	1 & 2	1 & 2	2 & 3	Open	Open
SL80486SX	1 & 2	1 & 2	2 & 3	Open	Open
CX486D	2 & 3	1 & 2	1 & 2, 3 & 4	3 & 4	Open
UMC U5S	1 & 2	2 & 3	2 & 3	1 & 2, 3 & 4	Open
AM486DXL	1 & 2	2 & 3	1 & 2, 3 & 4	1 & 2, 3 & 4	Open
80486DX	1 & 2	1 & 2	1 & 2, 3 & 4	3 & 4	Open
SL80486DX	1 & 2	1 & 2	1 & 2, 3 & 4	3 & 4	Open
UMC U5DS	1 & 2	2 & 3	1 & 2, 3 & 4	1 & 2, 3 & 4	Open
AM486DX2	1 & 2	2 & 3	1 & 2, 3 & 4	1 & 2, 3 & 4	1 & 2
(SL)AM486DX2	1 & 2	1 & 2	1 & 2, 3 & 4	3 & 4	Open
80486DX2	1 & 2	1 & 2	1 & 2, 3 & 4	3 & 4	Open
SL80486DX2	1 & 2	1 & 2	1 & 2, 3 & 4	3 & 4	Open
AM486DX4	1 & 2	2 & 3	1 & 2, 3 & 4	1 & 2, 3 & 4	Open
(SL)AM486DX4	1 & 2	1 & 2	1 & 2, 3 & 4	3 & 4	Open
80486DX4	1 & 2	1 & 2	1 & 2, 3 & 4	3 & 4	Open
P24D	1 & 2	1 & 2	1 & 2, 3 & 4	3 & 4	2 & 3
P24T	1 & 2	1 & 2	1 & 2, 3 & 4	2 & 3	Open

Note: Pins designated should be in the closed position.

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CPU TYPE SELECTION (CON'T)					
Type	JP24	JP25	JP26	JP30	JP31
CX486S	1 & 2, 3 & 4	2 & 3	Open	3 & 4	Open
80486SX	Open	Open	Open	1 & 2	Open
SL80486SX	2 & 3, 4 & 5	Open	Open	3 & 4	Open
CX486D	1 & 2, 3 & 4	2 & 3	Open	3 & 4	Open
UMC U5S	Open	3 & 4	Open	1 & 2	Open
AM486DXL	Open	3 & 4	Open	1 & 2	Open
80486DX	Open	Open	Open	1 & 2	Open
SL80486DX	2 & 3, 4 & 5	Open	Open	3 & 4	Open
UMC U5DS	Open	3 & 4	Open	1 & 2	Open
AM486DX2	Open	3 & 4	Open	1 & 2	Open
(SL)AM486DX2	2 & 3, 4 & 5	Open	Open	3 & 4	Open
80486DX2	Open	Open	Open	1 & 2	Open
SL80486DX2	2 & 3, 4 & 5	Open	Open	3 & 4	Open
AM486DX4	Open	3 & 4	Open	1 & 2	Open
(SL)AM486DX4	2 & 3, 4 & 5	Open	Open	3 & 4	Open
80486DX4	2 & 3, 4 & 5	Open	Open	3 & 4	Open
P24D	2 & 3, 4 & 5	Open	1 & 2	3 & 4	1 & 2
P24T	2 & 3, 4 & 5	1 & 2	Open	3 & 4, 5 & 6	Open

Note: Pins designated should be in the closed position.

CPU TYPE SELECTION (CON'T)					
Type	JP32	JP34	JP35	JP36	JP37
CX486S	2 & 3	1 & 2	2 & 3	1 & 2	Open
80486SX	Open	Open	Open	Open	Open
SL80486SX	2 & 3	2 & 3	Open	Open	Open
CX486D	2 & 3	1 & 2	2 & 3	1 & 2	Open
UMC U5S	1 & 2	Open	Open	Open	Open
AM486DXL	1 & 2	Open	Open	Open	Open
80486DX	Open	Open	Open	Open	Open
SL80486DX	2 & 3	2 & 3	Open	Open	Open
UMC U5DS	1 & 2	Open	Open	Open	Open
AM486DX2	1 & 2	Open	Open	Open	Open
(SL)AM486DX2	2 & 3	2 & 3	Open	Open	Open
80486DX2	Open	Open	Open	Open	Open
SL80486DX2	2 & 3	2 & 3	Open	Open	Open
AM486DX4	1 & 2	Open	Open	Open	Open
(SL)AM486DX4	2 & 3	2 & 3	Open	Open	Open
80486DX4	2 & 3	2 & 3	Open	Open	Open
P24D	2 & 3	2 & 3	Open	Open	1 & 2
P24T	2 & 3	2 & 3	1 & 2	2 & 3	Open

Note: Pins designated should be in the closed position.

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CPU MULTIPLIER SELECTION (DX4 ONLY)	
Multiplier	JP33
2x	Pins 2 & 3 closed
2.5x	Pins 1 & 2 closed
3x	Open

CPU VOLTAGE SELECTION						
Voltage	JP41	JP42	JP43	JP44	JP45	JP46
3.45v/5v (auto)	Open	Open	Open	Open	Closed	Open
3.3v (fixed)	Closed	Open	Open	Open	Open	Open
3.45v (fixed)	Open	Open	Open	Open	Open	Open
3.6v (fixed)	Open	Closed	Open	Open	Open	Open
3.8v (fixed)	Open	Open	Closed	Open	Open	Open
4v (fixed)	Open	Open	Open	Closed	Open	Open
5v (fixed)	Open	Open	Open	Open	Open	Closed

VL BUS WAIT STATE SELECTION	
Setting	JP40
0	Open
1	Closed

VL-BUS SPEED SELECTION	
Setting	JP39
<= 33MHz	Open
>33 MHz	Closed