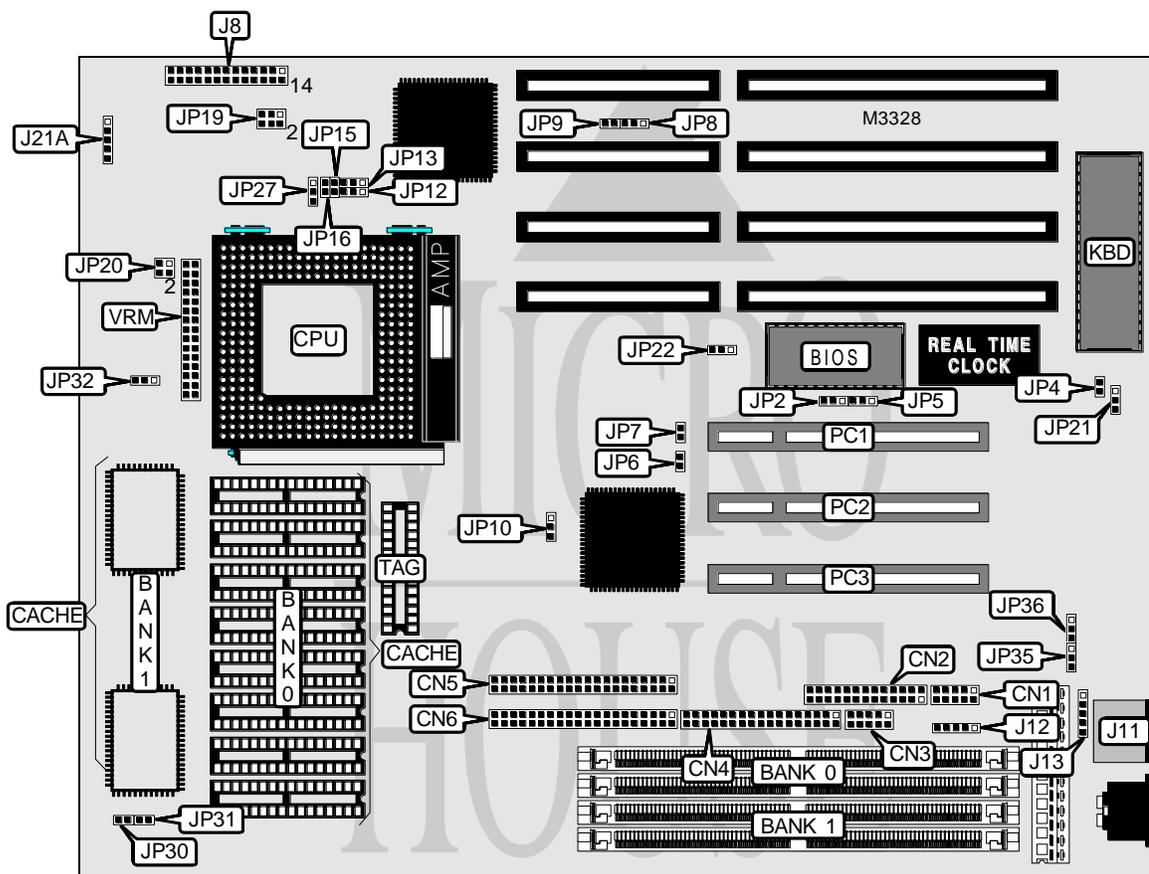


# BIOSTAR MICROTECH INTERNATIONAL CORPORATION

## MB-8500TAC

<b>Processor</b>	Pentium
<b>Processor Speed</b>	75/90/100/120/133/150/166/180MHz
<b>Chip Set</b>	Unidentified
<b>Maximum Onboard Memory</b>	128MB (EDO supported)
<b>Maximum Video Memory</b>	None
<b>Cache</b>	256/512KB
<b>BIOS</b>	AMI
<b>Dimensions</b>	275mm x 220mm
<b>I/O Options</b>	32-bit PCI slots (3), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), VRM connector
<b>NPU Options</b>	None



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## MB-8500TAC

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CONNECTIONS			
Function	Label	Function	Label
Serial port 1	CN1	Reset switch	J8 pins 12 & 13
Parallel port	CN2	Green PC connector	J8 pins 17 & 18
Serial port 2	CN3	IDE interface LED	J8 pins 20 & 21
Floppy drive interface	CN4	VCC ground connector	J8 pins 25 & 26
IDE interface 2	CN5	PS/2 mouse port	J11
IDE interface 1	CN6	Chassis fan power	JP19
Speaker	J8 pins 1 - 4	32-bit PCI slots	PC1 - PC3
Power LED & keylock	J8 pins 5 - 9	VRM connector	VRM
Turbo LED	J8 pins 10 & 11		

USER CONFIGURABLE SETTINGS		
Setting	Label	Position
í Jumper information unavailable	J12	N/A
í Jumper information unavailable	J13	N/A
í Jumper information unavailable	J21A	N/A
í Jumper information unavailable	JP2	N/A
í CMOS memory normal operation	JP4	Open
CMOS memory clear	JP4	Closed
Flash BIOS voltage select 5v	JP5	Pins 1 & 2 closed
Flash BIOS voltage select 12v	JP5	Pins 2 & 3 closed
EPROM installed	JP5	Open
Secondary IDE IRQ select IRQ15	JP8	Pins 1 & 2 closed
Secondary IDE IRQ select IRQ through PCI	JP8	Pins 2 & 3 closed
í Primary IDE IRQ14 enabled	JP9	Closed
Primary IDE IRQ14 disabled	JP9	Open
í Jumper information unavailable	JP12	N/A
í Jumper information unavailable	JP13	N/A
í Factory configured - do not alter	JP22	Pins 2 & 3 closed
í Jumper information unavailable	JP30	N/A
í Jumper information unavailable	JP31	N/A
í Jumper information unavailable	JP32	N/A

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
24MB	(2) 1M x 36	(2) 2M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36

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## MB-8500TAC

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DRAM CONFIGURATION (CON'T)		
Size	Bank 0	Bank 1
40MB	(2) 4M x 36	(2) 1M x 36
40MB	(2) 1M x 36	(2) 4M x 36
48MB	(2) 4M x 36	(2) 2M x 36
48MB	(2) 2M x 36	(2) 4M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
66MB	(2) 8M x 36	(2) 256K x 36
66MB	(2) 256K x 36	(2) 8M x 36
68MB	(2) 8M x 36	(2) 512K x 36
68MB	(2) 512K x 36	(2) 8M x 36
72MB	(2) 8M x 36	(2) 1M x 36
72MB	(2) 1M x 36	(2) 8M x 36
80MB	(2) 8M x 36	(2) 2M x 36
80MB	(2) 2M x 36	(2) 8M x 36
96MB	(2) 8M x 36	(2) 4M x 36
96MB	(2) 4M x 36	(2) 8M x 36
128MB	(2) 8M x 36	(2) 8M x 36

Note: Board accepts EDO memory.

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
256KB (A)	None	(2) 32K x 32	None
256KB (B)	(8) 32K x 8	None	(1) 8K/16K/32K x 8
512KB (A)	(8) 64K x 8	None	(1) 16K/32K x 8
512KB (B)	(8) 64K x 8	None	(1) 16K x 8

Note: Board will either have asynchronous or synchronous cache installed.

CACHE JUMPER CONFIGURATION	
Size	JP10
None	Open
256KB (B) (STD/Aster TAG)	Open
512KB (A) (STD TAG)	Pins 1 & 2 closed
512KB (B) (Aster TAG)	Pins 2 & 3 closed

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**MB-8500TAC**

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CPU SPEED SELECTION					
Speed	JP6	JP7	JP15	JP16	JP21
75MHz	Open	Open	Open	Open	1 & 2
90MHz	Open	Closed	Open	Open	2 & 3
100MHz	Closed	Closed	Open	Open	2 & 3
120MHz	Open	Closed	Closed	Open	2 & 3
133MHz	Closed	Closed	Closed	Open	2 & 3
150MHz	Open	Closed	Closed	Closed	2 & 3
166MHz	Closed	Closed	Closed	Closed	2 & 3
180MHz	Open	Closed	Open	Closed	2 & 3

Note: Pins designated should be in the closed position.

CPU TYPE SELECTION	
Type	JP20
P54C/CS/CQS/CT	Pins 1 & 2, 3 & 4 closed
P55C	Open

CPU VOLTAGE SELECTION	
Voltage	JP27
3.4v	Pins 1 & 2 closed
3.5v	Pins 2 & 3 closed

SERIAL PORT 2 SELECTION		
Setting	JP35	JP36
Used as COM2/4	Pins 1 & 2 closed	Pins 1 & 2 closed
Used as IR connector	Pins 2 & 3 closed	Pins 2 & 3 closed