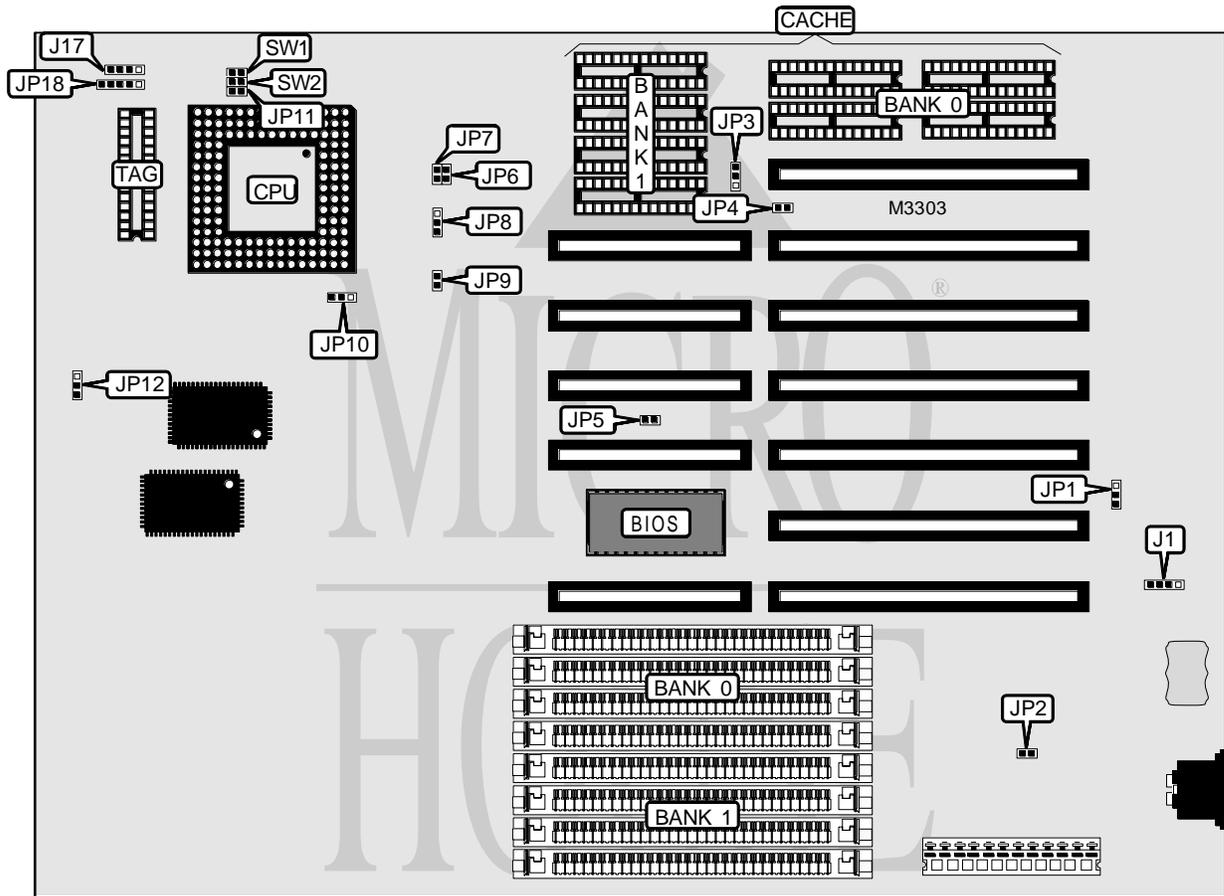


CHICONY, INC.

CH-486

Processor	80486SX/80487SX/80486DX
Processor Speed	20/25/33/50MHz
Chip Set	OPTI
Maximum Onboard Memory	32MB
Cache	64/128/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	None



CONNECTIONS			
Function	Label	Function	Label
External battery	J1	Power LED & keylock	JP18
Speaker	J17	Reset switch	SW1
Turbo LED	JP11	Turbo switch	SW2

Continued on next page. . .

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USER CONFIGURABLE SETTINGS		
Setting	Label	Position
í CMOS memory normal operation	JP1	Pins 2 & 3 closed
CMOS memory clear	JP1	Pins 1 & 2 closed
Monitor type select color	JP2	Closed
Monitor type select monochrome	JP2	Open
í Jumper information unavailable	JP5	N/A

DRAM		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	None
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	None
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	None
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE SIZE			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	None	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER				
Size	JP3	JP4	JP6	JP7
64KB	Pins 2 & 3 closed	Open	Open	Open
128KB	Pins 1 & 2 closed	Closed	Closed	Open
256KB	Pins 2 & 3 closed	Closed	Closed	Closed

CPU TYPE			
Setting	JP8	JP9	JP10
80486SX	Pins 1 & 2 closed	Closed	Pins 1 & 2 closed
80487SX	Pins 2 & 3 closed	Open	Open
80486DX	Pins 1 & 2 closed	Closed	Pins 2 & 3 closed

CPU SPEED	
Setting	JP12
20MHz	Pins 2 & 3 closed
25MHz	Pins 2 & 3 closed
33MHz	Pins 2 & 3 closed
50MHz	Pins 1 & 2 closed