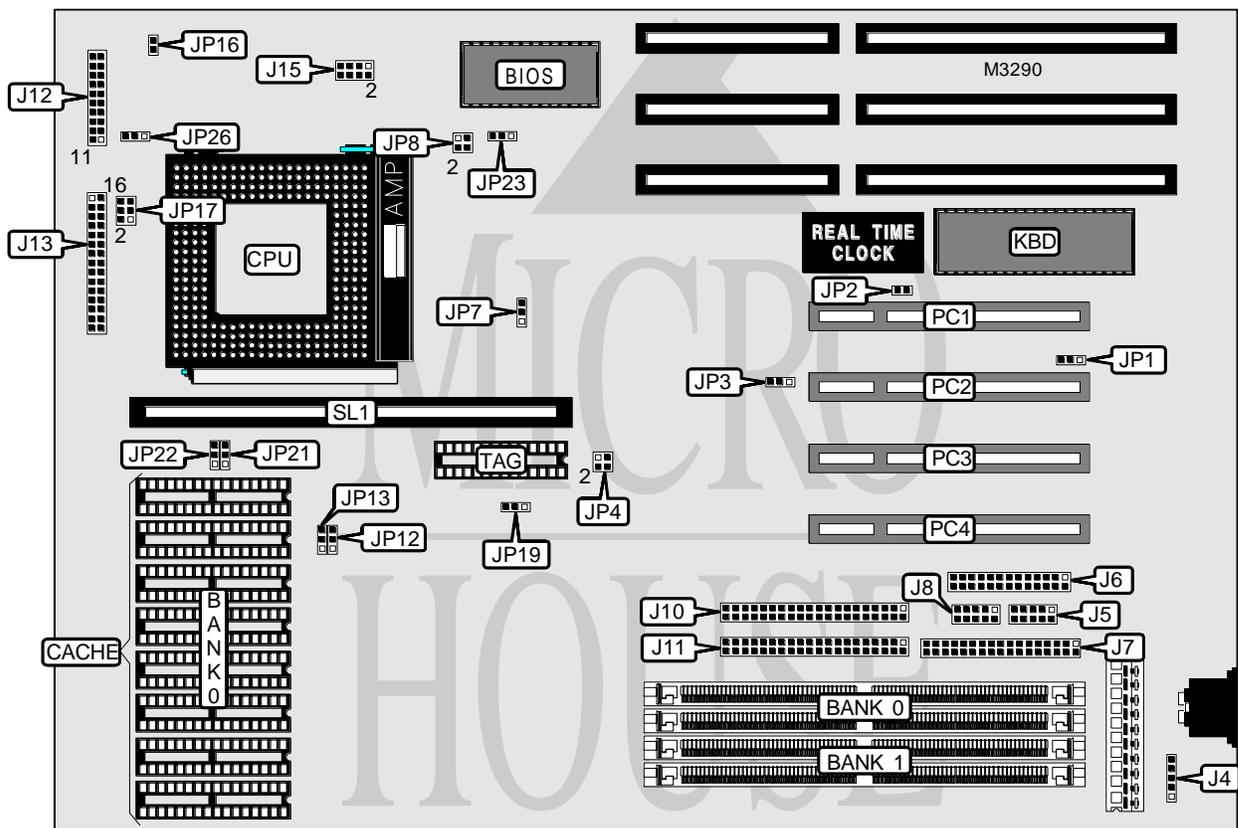


ELITEGROUP COMPUTER SYSTEMS, INC. TR5510 AIO (V1.3)

Processor	CX M1/Pentium
Processor Speed	75/90/100/120/133/150/166MHz
Chip Set	Intel
Maximum Onboard Memory	128MB
Cache	256/512KB
BIOS	Award
Dimensions	330mm x 218mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), VRM connector, cache slot, IR connector
NPU Options	None



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CONNECTIONS			
Function	Label	Function	Label
PS/2 mouse interface	J4	Reset switch	J12 pins 9 & 10
Serial port 1	J5	Power LED & keylock	J12 pins 11 - 15
Parallel port	J6	Speaker	J12 pins 17 - 20
Floppy drive interface	J7	VRM connector	J13
Serial port 2	J8	IR connector	J15
IDE interface 2	J10	IDE interface LED	JP16
IDE interface 1	J11	Chassis fan power	JP26
Turbo LED	J12 pins 2 & 3	32-bit PCI slots	PC1 - PC4
Green PC connector	J12 pins 4 & 5	Cache slot	SL1
Turbo switch	J12 pins 6 & 7		

USER CONFIGURABLE SETTINGS		
Setting	Label	Position
í On board I/O enabled	JP1	Pins 1 & 2 closed
On board I/O disabled	JP1	Pins 2 & 3 closed
í CMOS memory normal operation	JP2	Open
CMOS memory clear	JP2	Closed
í PCI clock speed select clock/4	JP3	Pins 2 & 3 closed
PCI clock speed select clock/3	JP3	Pins 1 & 2 closed
í Flash BIOS type select page mode flash	JP23	Open
Flash BIOS type select 5v	JP23	Pins 1 & 2 closed
Flash BIOS type select 12v	JP23	Pins 2 & 3 closed

DRAM		
Size	Bank 0	Bank 1
4MB	(2) 512K x 36	None
4MB	(2) 256K x 36	(2) 256K x 36
8MB	(2) 1M x 36	None
8MB	(2) 512K x 36	(2) 512K x 36
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
32MB	(2) 2M x 36	(2) 2M x 36
36MB	(2) 4M x 36	(2) 512K x 36
36MB	(2) 512K x 36	(2) 4M x 36
48MB	(2) 2M x 36	(2) 4M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36

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CACHE SIZE			
Size	Bank 0	TAG	SL1
256KB (A)	(8) 32K x 8	(1) 16K/32K x 8	Not installed
256KB (B)	None	(1) 32K x 8	Installed
512KB (A)	(8) 64K x 8	(1) 16K/32K x 8	Not installed
512KB (B)	None	(1) 32K x 8	Installed

CACHE JUMPER			
Size	JP7	JP21	JP22
Open	Open	Pins 2 & 3 closed	Pins 1 & 2 closed
256KB (B)	Open	Open	Open
512KB (A)	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
512KB (B)	Open	Open	Open

CACHE VOLTAGE		
Setting	JP12	JP13
Mixed voltage	Pins 1 & 2 closed	Pins 1 & 2 closed
3.3v	Pins 2 & 3 closed	Pins 2 & 3 closed

TAG JUMPER	
Setting	JP19
Installed	Pins 1 & 2 closed
Not installed	Pins 2 & 3 closed

Note: TAG must be installed if using pipeline burst cache.

CPU SPEED (CYRIX)		
Setting	JP4	JP8
120MHz	Open	Pins 1 & 2 closed
133MHz	Pins 1 & 2 closed	Pins 1 & 2 closed
150MHz	Pins 3 & 4 closed	Pins 1 & 2 closed
166MHz	Pins 1 & 2, 3 & 4 closed	Pins 1 & 2 closed

CPU SPEED (PENTIUM)		
Setting	JP4	JP8
75MHz	Open	Open
90MHz	Pins 3 & 4 closed	Open
100MHz	Pins 1 & 2, 3 & 4 closed	Open
120MHz	Pins 3 & 4 closed	Pins 1 & 2 closed
133MHz	Pins 1 & 2, 3 & 4 closed	Pins 1 & 2 closed
150MHz	Pins 3 & 4 closed	Pins 1, & 2, 3 & 4 closed
166MHz	Pins 1 & 2, 3 & 4 closed	Pins 1, & 2, 3 & 4 closed

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CPU VOLTAGE		
Setting	J13	JP17
3.3v	Pins 6 & 7,21 & 22 closed	Pins 5 & 6 closed
í 3.38v	Pins 6 & 7,21 & 22 closed	Pins 3 & 4 closed
3.52v	Pins 6 & 7,21 & 22 closed	Pins 1 & 2 closed