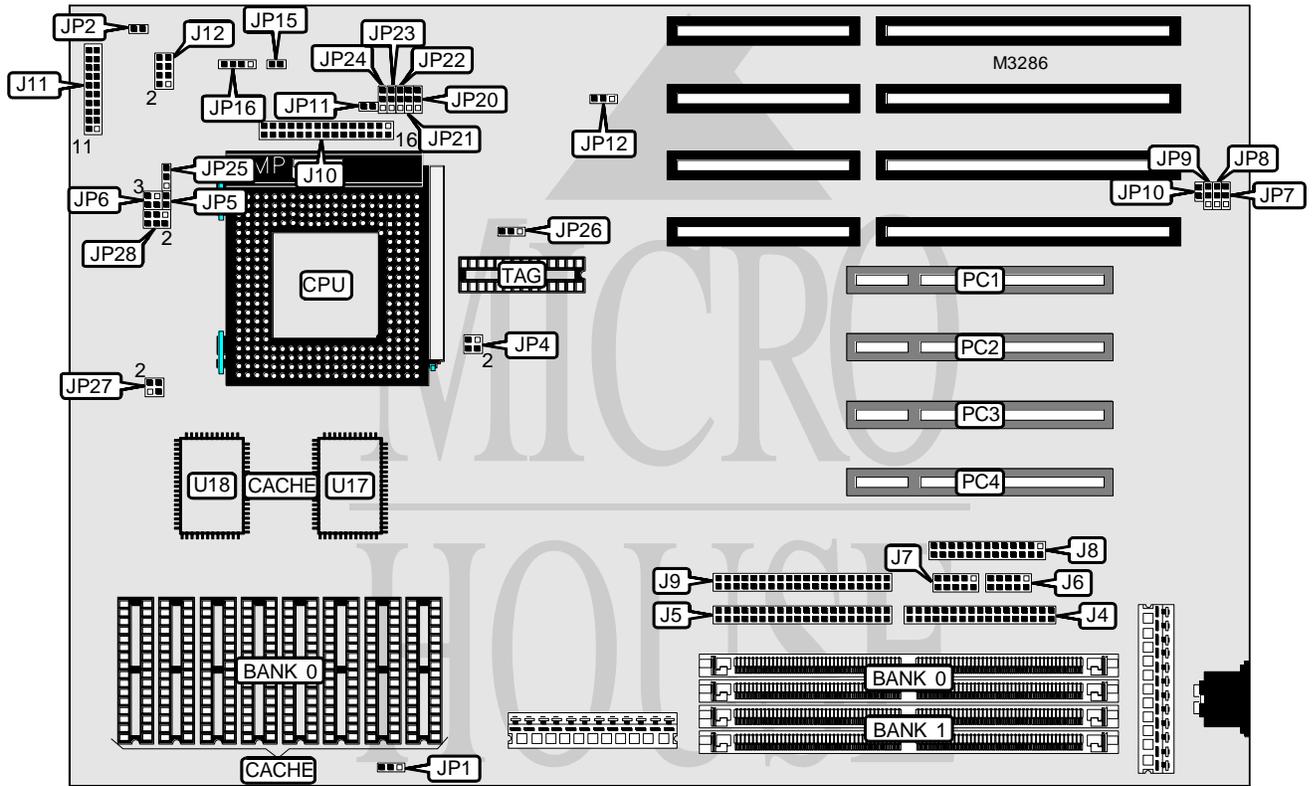


ELITEGROUP COMPUTER SYSTEMS, INC.

TS54P AIO (V1.0)

Processor	Pentium
Processor Speed	75/90/100/120/133/150MHz
Chip Set	Intel
Maximum Onboard Memory	128MB
Cache	256/512KB
BIOS	Phoenix
Dimensions	330mm x 218mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, serial ports (2), VRM connector, IR connector
NPU Options	None



CONNECTIONS			
Function	Label	Function	Label
Floppy drive interface	J4	Green PC connector	J11 pins 4 & 5
IDE interface 2	J5	Reset switch	J11 pins 9 & 10
Serial port 1	J6	Power LED & keylock	J11 pins 11 - 15
Serial port 2	J7	Speaker	J11 pins 17 - 20
Parallel port	J8	IR connector	J12
IDE interface 1	J9	IDE interface LED	JP2
VRM connector	J10	External battery	JP16
Turbo LED	J11 pins 2 & 3	32-bit PCI slots	PC1 - PC4

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USER CONFIGURABLE SETTINGS		
Setting	Label	Position
Normal COM2/4	J12	Pins 5 & 6, 7 & 8 closed
IR function connector	J12	Open
37C665GT enabled	JP7	Pins 1 & 2 closed
37C665GT disabled	JP7	Pins 2 & 3 closed
í ECP/EPP mode enabled	JP10	Closed
ECP/EPP mode disabled	JP10	Open
í Factory configured - do not alter	JP12	Pins 2 & 3 closed
í CMOS memory normal operation	JP15	Open
CMOS memory clear	JP15	Closed
Cache type select write back	JP25	Pins 1 & 2 closed
Cache type select write through	JP25	Pins 2 & 3 closed
PCI clock select PCICLK/4	JP26	Pins 1 & 2 closed
PCI clock select PCICLK/3	JP26	Pins 2 & 3 closed

DRAM		
Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
8MB	(2) 512K x 36	(2) 512K x 36
12MB	(2) 1M x 36	(2) 512K x 36
12MB	(2) 512K x 36	(2) 1M x 36
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
20MB	(2) 2M x 36	(2) 512K x 36
20MB	(2) 512K x 36	(2) 2M x 36
24MB	(2) 2M x 36	(2) 1M x 36
24MB	(2) 1M x 36	(2) 2M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
36MB	(2) 4M x 36	(2) 512K x 36
36MB	(2) 512K x 36	(2) 4M x 36
40MB	(2) 4M x 36	(2) 1M x 36
40MB	(2) 1M x 36	(2) 4M x 36
48MB	(2) 4M x 36	(2) 2M x 36
48MB	(2) 2M x 36	(2) 4M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
68MB	(2) 8M x 36	(2) 512K x 36
68MB	(2) 512K x 36	(2) 8M x 36
72MB	(2) 8M x 36	(2) 1M x 36
72MB	(2) 1M x 36	(2) 8M x 36
80MB	(2) 8M x 36	(2) 2M x 36

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DRAM (CON'T)		
Size	Bank 0	Bank 1
80MB	(2) 2M x 36	(2) 8M x 36
96MB	(2) 8M x 36	(2) 4M x 36
96MB	(2) 4M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None
128MB	(2) 8M x 36	(2) 8M x 36

Note: Banks 0/1 are interchangeable.

CACHE SIZE			
Size	Bank 0	U17 & U18	TAG
256KB (A) (pipeline burst)	None	(2) 32K x 32	(1) 8K x 8
256KB (B) (pipeline burst)	None	(2) 32K x 32	(1) 32K x 8
256KB (C) (burst)	None	(2) 32K x 32	(1) 8K x 8
256KB (D) (burst)	None	(2) 32K x 32	(1) 32K x 8
256KB (E)	(8) 32K x 8	None	(1) 8K x 8
256KB (F)	(8) 32K x 8	None	(1) 32K x 8
512KB	(8) 64K x 8	None	(1) 32K x 8

CACHE JUMPER					
Size	JP20	JP21	JP22	JP23	JP24
256KB (A) (pipeline burst)	2 & 3	1 & 2	1 & 2	2 & 3	1 & 2
256KB (B) (pipeline burst)	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2
256KB (C) (burst)	2 & 3	2 & 3	1 & 2	2 & 3	1 & 2
256KB (D) (burst)	1 & 2	2 & 3	1 & 2	2 & 3	1 & 2
256KB (E)	2 & 3	1 & 2	2 & 3	2 & 3	1 & 2
256KB (F)	1 & 2	1 & 2	2 & 3	2 & 3	1 & 2
512KB	2 & 3	1 & 2	2 & 3	1 & 2	2 & 3

Note: Pins designated should be in the closed position.

CACHE VOLTAGE	
Setting	JP1
Mixed	Pins 2 & 3 closed
3.3v	Pins 1 & 2 closed

CPU SPEED		
Setting	J10	JP11
75/90/100/120/133MHz	Pins 4 & 5, 6 & 7, 19 & 20, 21 & 22 closed	Open
150MHz	Closed	Closed

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CPU SPEED		
Setting	JP4	JP5
75MHz	Open	Open
90MHz	Pins 3 & 4 closed	Open
100MHz	Pins 1 & 2, 3 & 4 closed	Open
120MHz	Pins 3 & 4 closed	Closed
133MHz	Pins 1 & 2, 3 & 4 closed	Closed

CPU VOLTAGE (75MHZ - 133MHZ ONLY)		
Setting	JP6	JP27
3.3v from power supply	Open	Open
3.3v from VRM	Pins 1 & 2, 3 & 4 closed	Pins 1 & 2, 3 & 4 closed

CPU VOLTAGE (75MHZ - 133MHZ ONLY)	
Setting	JP28
3.14v - 3.46v	Pins 1 & 2 closed
3.3v - 3.46v	Pins 3 & 4 closed
3.45v - 3.6v	Pins 5 & 6 closed

DMA CHANNEL		
Setting	JP8	JP9
DMA1	Pins 1 & 2 closed	Pins 1 & 2 closed
DMA3	Pins 2 & 3 closed	Pins 2 & 3 closed