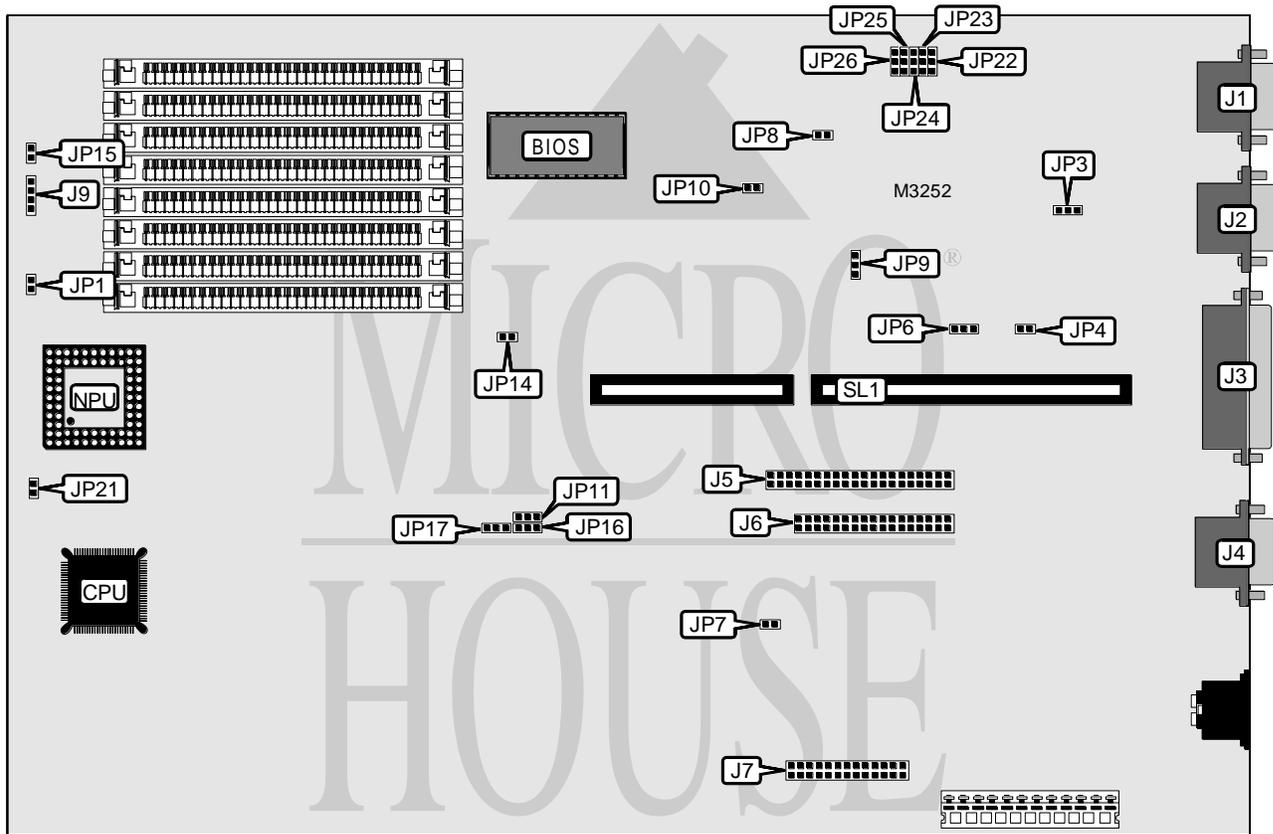


# IPC CORPORATION, LTD.

## IPC DYNASTY LE (MB386LE&A)

<b>Processor</b>	80386SX
<b>Processor Speed</b>	33MHz
<b>Chip Set</b>	Headland
<b>Max. Onboard DRAM</b>	16MB
<b>Cache</b>	None
<b>BIOS</b>	AMI
<b>Dimensions</b>	330mm x 218mm
<b>I/O Options</b>	Floppy drive interface, IDE interface, parallel port, serial ports (2), VGA feature connector, VGA port, riser slot
<b>NPU Options</b>	80387SX



CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 2	J1	VGA feature connector	J7
Serial port 1	J2	Speaker	J9
Parallel port	J3	IDE interface LED	JP1
VGA port	J4	Reset switch	JP15
IDE interface	J5	Power on LED	JP21
Floppy drive interface	J6	Riser slot	SL1

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IPC DYNASTY LE (MB386LE&A)

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í IDE interface enabled	JP4	Closed
IDE interface disabled	JP4	Open
í On board video enabled	JP6	Pins 1 & 2 closed
On board video disabled	JP6	Pins 2 & 3 closed
í VGA 0 wait state enabled	JP7	Closed
VGA 0 wait state disabled	JP7	Open
í Floppy drive pre comp select normal comp	JP8	Open
Floppy drive pre comp select alternate comp	JP8	Closed
í Floppy drive interface enabled	JP9	Pins 2 & 3 closed
Floppy drive interface disabled	JP9	Pins 1 & 2 closed
í Floppy drive spindle speed select single speed	JP10	Open
Floppy drive spindle speed select dual speed	JP10	Closed
í CMOS memory normal operation	JP11	Open
CMOS memory clear	JP11	Closed
í Monitor type select color/EGA/VGA	JP14	Closed
Monitor type select monochrome	JP14	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	None
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	None
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	None

Note: The orientation of Banks 0 & 1 are unidentified.

PARALLEL PORT CONFIGURATION			
LPT	JP3	JP23	JP25
Disabled	Open	Pins 2 & 3 closed	Pins 2 & 3 closed
LPT1	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
í LPT2	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
LPT3	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 1 & 2 closed

SERIAL PORT CONFIGURATION				
COM1	COM2	JP22	JP24	JP26
Disabled	Disabled	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
í Enabled	Enabled	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
Enabled	Disabled	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
Disabled	Enabled	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 1 & 2 closed

VGA MEMORY CONFIGURATION		
Read/write	JP16	JP17
í MEMR/MEMW	Pins 1 & 2 closed	Pins 1 & 2 closed
SMEMR/SMEMW	Pins 2 & 3 closed	Pins 2 & 3 closed

