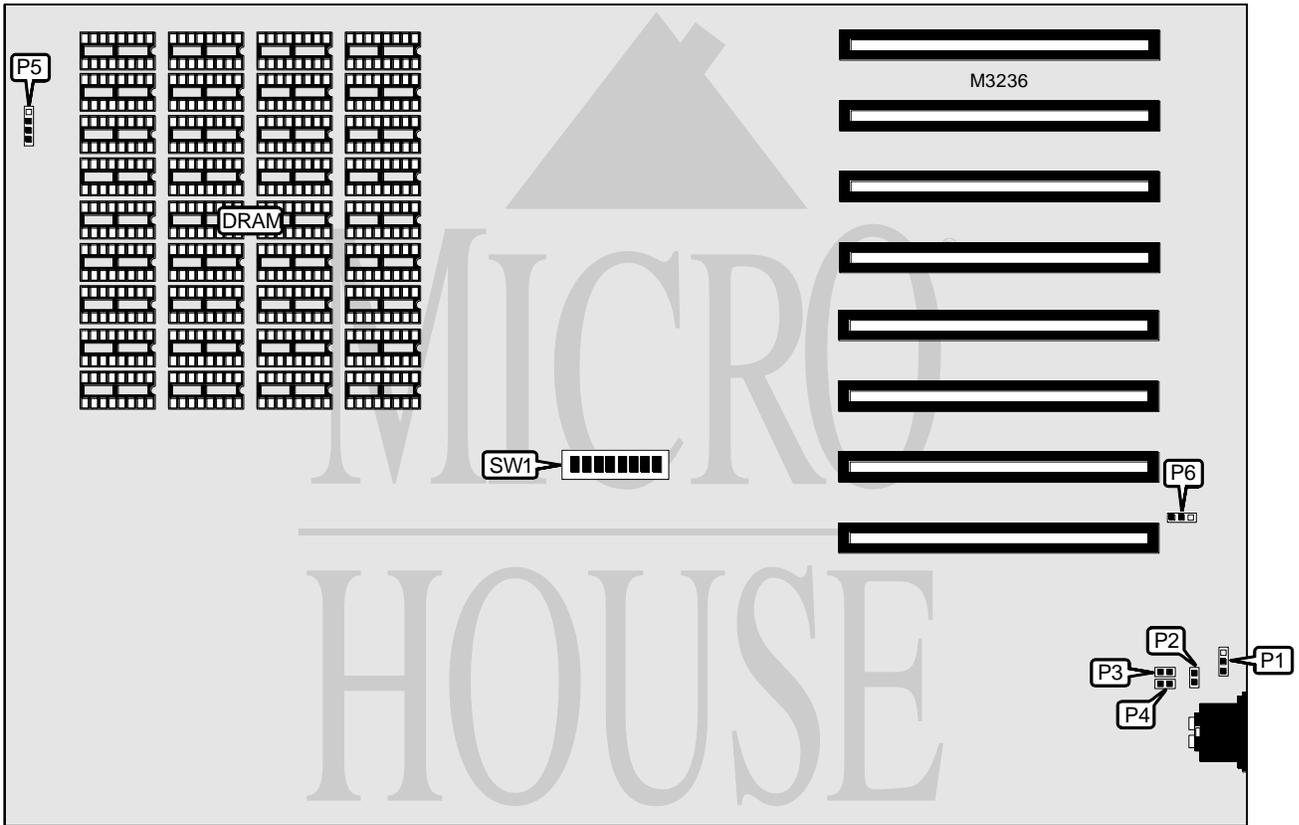


UNIDENTIFIED

FT-640 MB V5.0

Processor	8088 (exact location unidentified)
Processor Speed	4.77/8MHz
Chip Set	Unidentified
Max. Onboard DRAM	640KB
Cache	None
BIOS	MCT
Dimensions	330mm x 212mm
I/O Options	None
NPU Options	8087 (exact location unidentified)



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	P2	Speaker	P5 pins 1 & 2
Reset switch	P3	Power LED	P5 pins 3 & 4

Continued on next page. . .

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
CPU speed select 4.77MHz	P1	Pins 1 & 2 closed
CPU speed select 8MHz	P1	Pins 2 & 3 closed
MCT turbo BIOS	P1	Open
Keyboard enabled	P4	Closed
Keyboard disabled	P4	Open
Power good signal detect from power supply	P6	Pins 1 & 2 closed
Power good signal detect from board	P6	Pins 2 & 3 closed
í Factory configured - do not alter	SW1/1	Off
í Factory configured - do not alter	SW1/2	On
í Factory configured - do not alter	SW1/3	Off
í Factory configured - do not alter	SW1/4	Off
1 floppy drive installed	SW1/7	On
2 floppy drives installed	SW1/7	Off
í Factory configured - do not alter	SW1/8	On

VIDEO CONFIGURATION		
Setting	SW1/5	SW1/6
Monochrome	Off	Off
Color	On	Off
EGA	On	On

DRAM CONFIGURATION
Note: DRAM configuration is unidentified. One bank uses 41256 chips and the other uses 4164 chips.