ALARIS, INC.
L E O P A R D   L X   4 8 6 S L C 2

Processor	486SLC2
Processor Speed	50(internal)/66(internal)MHz
Chip Set	Unidentified
Max. Onboard DRAM	16MB
Cache	None
BIOS	AMI
Dimensions	228mm x 220mm
I/O Options	None
NPU Options	80387SX
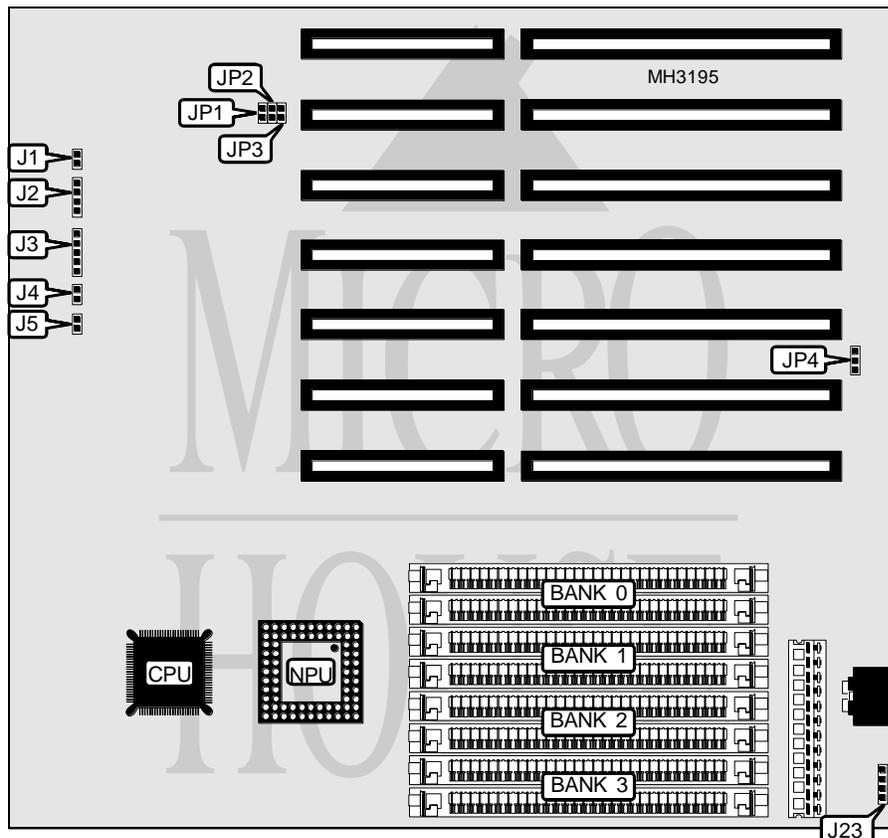
CONNECTIONS			
Purpose	Location	Purpose	Location
Reset switch	J1	Turbo switch	J4
Speaker	J2	Turbo LED	J5
Power LED & keylock	J3	External battery	J23

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP1	N/A
í Factory configured - do not alter	JP2	N/A
í Factory configured - do not alter	JP3	N/A
í CMOS memory normal operation	JP4	pins 2 & 3 closed
CMOS memory clear	JP4	pins 1 & 2 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(2) 256K x 9	(2) 256K x 9	NONE	NONE
2MB	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9
2MB	(2) 1M x 9	NONE	NONE	NONE
3MB	(2) 256K x 9	(2) 256K x 9	(2) 1M x 9	NONE
4MB	(2) 1M x 9	(2) 1M x 9	NONE	NONE
5MB	(2) 256K x 9	(2) 256K x 9	(2) 1M x 9	(2) 1M x 9
8MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9
9MB	(2) 256K x 9	(2) 256K x 9	(2) 4M x 9	NONE
10MB	(2) 1M x 9	(2) 4M x 9	NONE	NONE
16MB	(2) 4M x 9	(2) 4M x 9	NONE	NONE

MISCELLANEOUS TECHNICAL NOTE
Note: The location of pin 1 is unidentified.