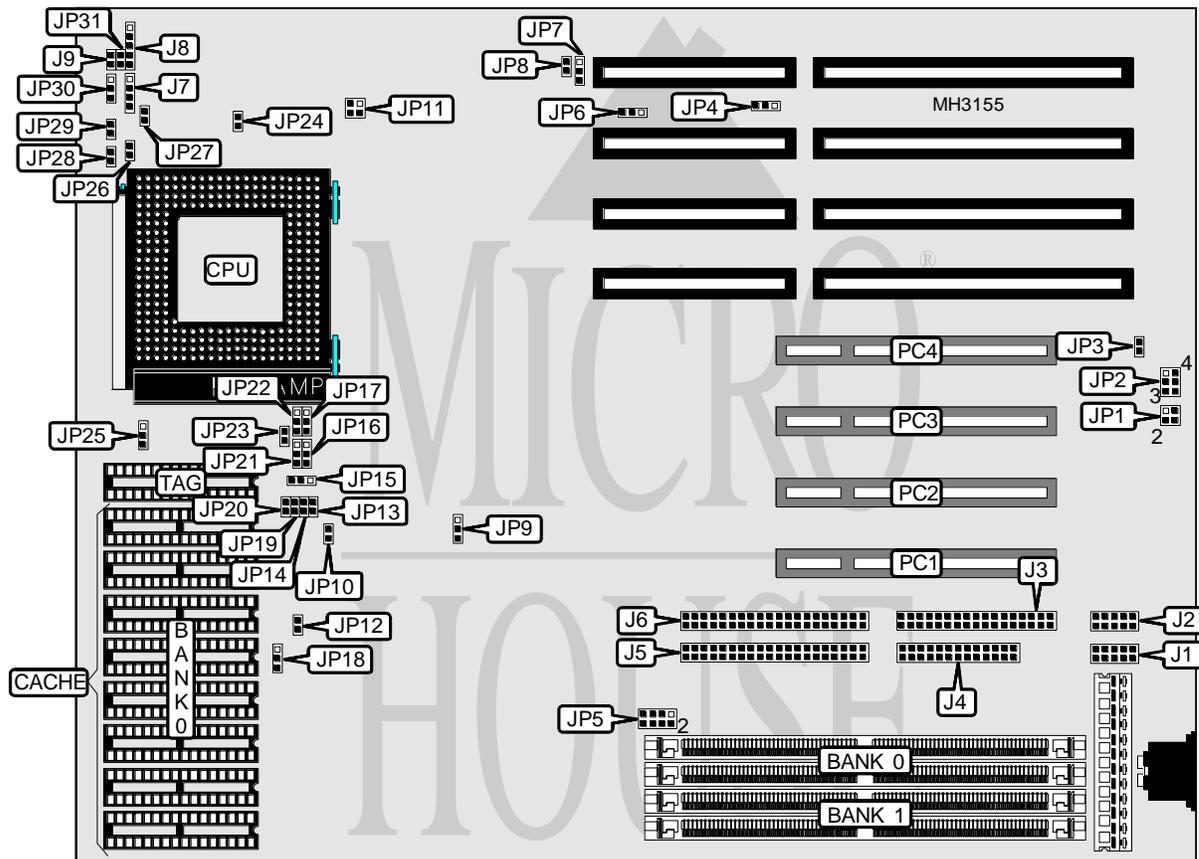


# ATC/UNITRON COMPUTER & COMPUTER PARTS

## U 6 9 7 8

<b>Processor</b>	AMD K5/Pentium
<b>Processor Speed</b>	75/90/100MHz
<b>Chip Set</b>	Unidentified
<b>Max. Onboard DRAM</b>	128MB
<b>Cache</b>	64/256/512/1024KB
<b>BIOS</b>	Award
<b>Dimensions</b>	330mm x 218mm
<b>I/O Options</b>	32-bit PCI slots (4), green PC connector, floppy drive interface, IDE interfaces (2), parallel port, serial ports (2)
<b>NPU Options</b>	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 1	J1	Reset switch	J9
Serial port 2	J2	IDE interface LED	JP26
Floppy drive interface	J3	Green PC connector	JP28
Parallel port	J4	Turbo LED	JP29
IDE interface 1	J5	Turbo switch	JP30
IDE interface 2	J6	Green PC connector	JP31
Speaker	J7	32-bit PCI slots	PC1 - PC4
Power LED & keylock	J8		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í ECP/EPP signal select DREQ1	JP1	pins 1 & 3 closed
ECP/EPP signal select DREQ3	JP1	pins 2 & 4 closed
í ECP/EPP signal select DACK1	JP2	pins 1 & 4 closed
ECP/EPP signal select DACK3	JP2	pins 2 & 5 closed
í Monitor type select color	JP3	Closed
Monitor type select monochrome	JP3	Open
í Factory configured - do not alter	JP4	N/A
í BIOS type select EPROM	JP6	Open
BIOS type select 5v flash	JP6	pins 2 & 3 closed
BIOS type select 12v flash	JP6	pins 1 & 2 closed
í Factory configured - do not alter	JP7	N/A
í CMOS memory normal operation	JP8	Open
CMOS memory clear	JP8	Closed
í Factory configured - do not alter	JP11	N/A
í L1 invalidate cycle select master write cycles force invalidation	JP16	pins 2 & 3 closed
L1 invalidate cycle select every cycle with W/R# force invalidation	JP16	pins 1 & 2 closed
í Pipeline disabled	JP17	pins 2 & 3 closed
Pipeline enabled	JP17	pins 1 & 2 closed
í Cache type select write back	JP21	pins 1 & 2 closed
Cache type select write through	JP21	pins 2 & 3 closed
í Turbo enabled	JP30	pins 1 & 2 closed
Turbo disabled	JP30	pins 2 & 3 closed

DRAM CONFIGURATION 1		
Size	Bank 0	Bank 1
2MB	(2) 256K x 36	NONE
4MB	(2) 512K x 36	NONE
8MB	(2) 1M x 36	NONE
16MB	(2) 2M x 36	NONE
32MB	(2) 4M x 36	NONE
64MB	(2) 8M x 36	NONE
128MB	(2) 16M x 36	NONE

DRAM CONFIGURATION 2		
Size	Bank 0	Bank 1
4MB	(2) 256K x 36	(2) 256K x 36
16MB	(2) 1M x 36	(2) 1M x 36
64MB	(2) 4M x 36	(2) 4M x 36

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DRAM CONFIGURATION 3		
Size	Bank 0	Bank 1
12MB	(2) 512K x 36	(2) 1M x 36
36MB	(2) 512K x 36	(2) 4M x 36
8MB	(2) 512K x 36	(2) 512K x 36
20MB	(2) 512K x 36	(2) 2M x 36
48MB	(2) 2M x 36	(2) 4M x 36
32MB	(2) 2M x 36	(2) 2M x 36
80MB	(2) 2M x 36	(2) 8M x 36
128MB	(2) 8M x 36	(2) 8M x 36

DRAM JUMPER CONFIGURATION	
Configuration	JP9
1	pins 1 & 2 OR 2 & 3 closed
2	pins 1 & 2 closed
3	pins 2 & 3 closed

CACHE CONFIGURATION		
Size	Bank 0	TAG
64KB	(8) 8K x 8	(1) 8K x 8
256KB	(8) 32K x 8	(1) 8K x 8
512KB	(8) 64K x 8	(1) 16K x 8
1MB	(8) 128K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION				
Size	JP13	JP14	JP19	JP20
64KB	Open	Open	Open	Open
256KB	Open	Open	Closed	Closed
512KB	Open	Closed	Closed	Closed
1MB	Closed	Closed	Closed	Closed

CACHE VOLTAGE CONFIGURATION			
Voltage	JP15	JP18	JP25
Mixed voltage	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
3.3v	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed

CPU TYPE CONFIGURATION			
Type	JP23	JP24	JP27
AMD K5	Open	Open	Open
Pentium	Closed	Closed	Closed

CPU SPEED CONFIGURATION		
Speed	JP10	JP12
75MHz	Open	Open
90MHz	Open	Closed
100MHz	Closed	Closed

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CPU SPEED CONFIGURATION (AMD K5)	
Speed	JP22
1x	pins 2 & 3 closed
1.5x/2x/3x	pins 1 & 2 closed

CPU SPEED CONFIGURATION (PENTIUM)	
Speed	JP22
1.5x	pins 1 & 2 closed
2x	pins 2 & 3 closed

PCI IDE CONFIGURATION	
Base address	JP5
í Enabled	pins 1 & 2 open
Disabled	pins 1 & 2 closed

PCI IDE CONFIGURATION	
1 <sup>st</sup> IDE channel	JP5
í Enabled	pins 3 & 4 open
Disabled	pins 3 & 4 closed

PCI IDE CONFIGURATION	
2 <sup>nd</sup> IDE channel	JP5
í Enabled	pins 5 & 6 closed
Disabled	pins 5 & 6 open

PCI IDE CONFIGURATION	
External D/FF	JP5
í Needed	pins 9 & 10 open
Not needed	pins 9 & 10 closed