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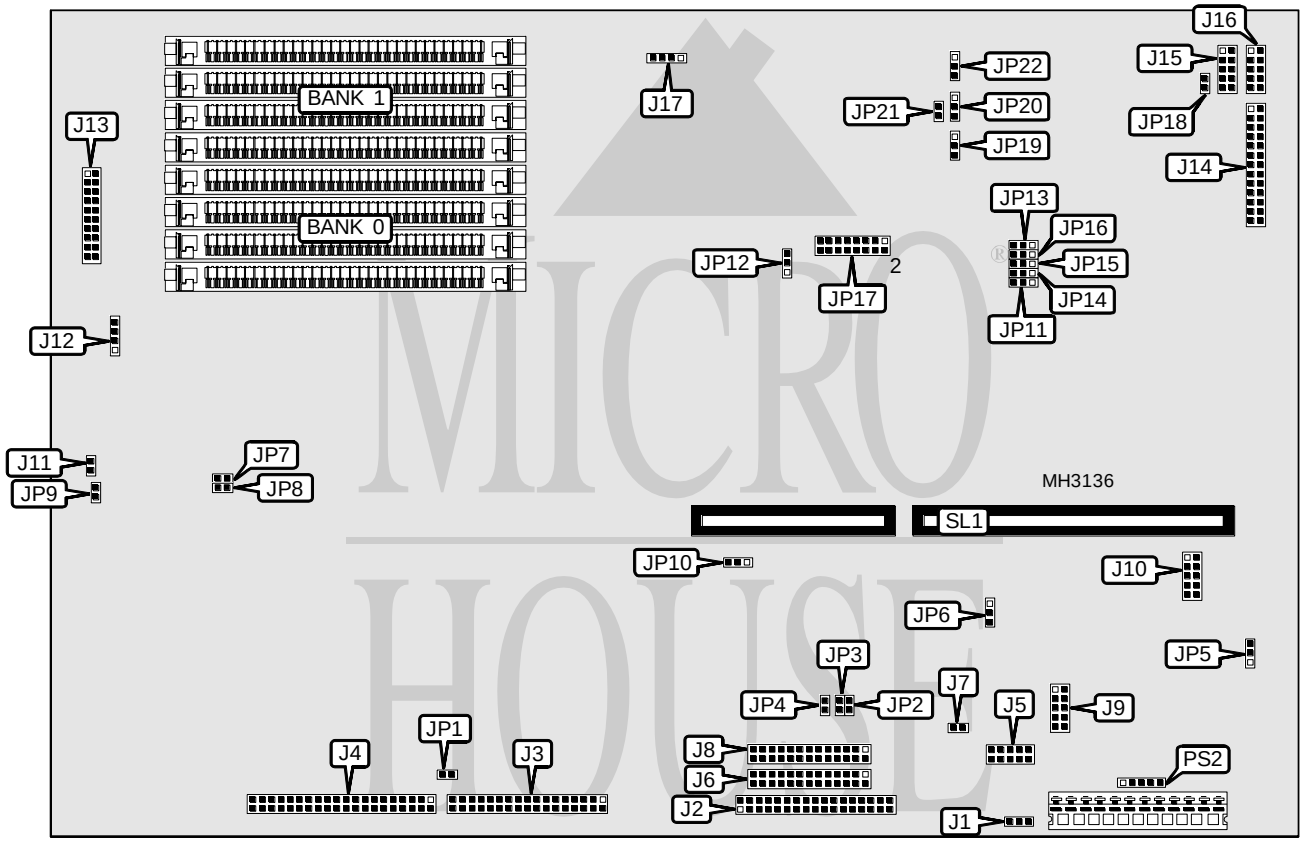
Processor	80486SX/80486DX (exact location unidentified)
Processor Speed	25MHz
Chip Set	Unidentified
Max. Onboard DRAM	32MB
Cache	None
BIOS	Unidentified
Dimensions	330mm x 218mm
I/O Options	Floppy drive interfaces (2), IDE interface, parallel port, serial ports (2), VGA port, riser slot, numeric display connector, numeric display power connector, internal numeric display connector, external numeric display connector, printer power connectors (2), drawer port connector, external keyboard connectors (2), scanner port, keyboard controller
NPU Options	None



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CONNECTIONS			
Purpose	Location	Purpose	Location
Numeric display power connector	J1	External keyboard connector	J12
Serial port 2/external floppy drive	J2	Keyboard controller	J13
Floppy drive interface	J3	Parallel port	J14
IDE interface	J4	Serial port 1	J15
Numeric display connector	J5	Scanner port	J16
Internal numeric display connector	J6	External keyboard connector	J17
External numeric display connector	J8	Reset switch	JP9
Printer power connector	J9	Printer power connector	PS2
Drawer port connector	J10	Riser slot	SL1
Speaker	J11		

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Floppy drive set as drive B	J7	Open
Floppy drive set as drive A	J7	Closed
Monitor type select monochrome	JP1	Open
Monitor type select color/EGA/VGA	JP1	Closed
í 5v power to external floppy drive interface disabled	JP2	Open
5v power to external floppy drive interface enabled	JP2	Closed
í 12v power to external floppy drive interface disabled	JP3	Open
12v power to external floppy drive interface enabled	JP3	Closed
í Factory configured - do not alter	JP4	N/A
í Drawer port voltage select 12v	JP5	pins 1 & 2 closed
Drawer port voltage select 25v	JP5	pins 2 & 3 closed
í Factory configured - do not alter	JP10	N/A
í Keylock level I to control floppy access disabled	JP12	pins 2 & 3 closed
Keylock level I to control floppy access enabled	JP12	pins 1 & 2 closed
í 12v power to scanner port disabled	JP18	Open
12v power to scanner port enabled	JP18	Closed
í Keylock level I to control write access to NVR disabled	JP19	pins 1 & 2 closed
Keylock level I to control write access to NVR enabled	JP19	pins 2 & 3 closed
í Non volatile RAM select 128KB (DS1245Y)	JP21	Open
Non volatile RAM select 8KB or 32KB (DS1225Y/DS1235Y)	JP21	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
32MB	(4) 4M x 9	(4) 4M x 9

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CPU TYPE CONFIGURATION		
Type	JP7	JP8
80486SX	Open	Open
80486DX	Closed	Closed

I/O CONTROLLER INDEX CONFIGURATION		
Index address	Data address	JP6
í 398H	399H	pins 1 & 2 closed
26EH	26FH	pins 2 & 3 closed

I/O ADDRESS RANGE CONFIGURATION	
Port	JP17
í 100H	pins 1 & 2 closed
110H	pins 3 & 4 closed
120H	pins 5 & 6 closed
130H	pins 7 & 8 closed
140H	pins 9 & 10 closed
150H	pins 11 & 12 closed
160H	pins 13 & 14 closed
170H	pins 15 & 16 closed

KEYLOCK LEVEL 1 OR 5 TO CONTROL READ ACCESS TO NVR CONFIGURATION		
Setting	JP20	JP22
í Disabled	pins 1 & 2 closed	pins 1 & 2 closed
Enabled	pins 2 & 3 closed	pins 2 & 3 closed

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I/O CONFIGURATION									
Floppy	IDE	Serial 1	Serial 2	Parallel	JP11	JP13	JP14	JP15	JP16
í PRI	PRI	COM1	COM2	LPT2	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
PRI	PRI	COM1	COM2	LPT1	2 & 3	1 & 2	2 & 3	2 & 3	2 & 3
PRI	SEC	COM1	COM2	LPT1	2 & 3	2 & 3	2 & 3	2 & 3	1 & 2
PRI	PRI	COM3	COM4	LPT1	2 & 3	1 & 2	2 & 3	2 & 3	1 & 2
PRI	PRI	COM2	COM3	LPT2	2 & 3	2 & 3	2 & 3	1 & 2	2 & 3
PRI	SEC	COM3	COM4	LPT2	2 & 3	1 & 2	2 & 3	1 & 2	2 & 3
PRI	PRI	COM1	N/A	LPT2	2 & 3	2 & 3	2 & 3	1 & 2	1 & 2
PRI	PRI	COM1	N/A	LPT1	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2
PRI	SEC	COM1	N/A	LPT1	2 & 3	2 & 3	1 & 2	2 & 3	2 & 3
PRI	PRI	COM3	N/A	LPT1	2 & 3	1 & 2	1 & 2	2 & 3	2 & 3
PRI	PRI	COM3	N/A	LPT2	2 & 3	2 & 3	1 & 2	2 & 3	1 & 2
PRI	SEC	COM3	N/A	LPT2	2 & 3	1 & 2	1 & 2	2 & 3	1 & 2
PRI	N/A	COM1	COM2	LPT2	2 & 3	2 & 3	1 & 2	1 & 2	2 & 3
PRI	N/A	COM1	COM2	LPT1	2 & 3	1 & 2	1 & 2	1 & 2	2 & 3
PRI	N/A	COM3	COM4	LPT1	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2
PRI	N/A	COM2	COM3	LPT2	2 & 3	1 & 2	1 & 2	1 & 2	1 & 2
PRI	PRI	N/A	N/A	LPT2	1 & 2	2 & 3	2 & 3	2 & 3	2 & 3
PRI	PRI	N/A	N/A	LPT1	1 & 2	1 & 2	2 & 3	2 & 3	2 & 3
PRI	SEC	N/A	N/A	LPT1	1 & 2	2 & 3	2 & 3	2 & 3	1 & 2
PRI	SEC	N/A	N/A	LPT2	1 & 2	1 & 2	2 & 3	2 & 3	1 & 2
N/A	N/A	COM1	COM2	LPT2	1 & 2	2 & 3	2 & 3	1 & 2	2 & 3
N/A	N/A	COM1	COM2	LPT1	1 & 2	1 & 2	2 & 3	1 & 2	1 & 2
N/A	N/A	COM3	COM4	LPT1	1 & 2	2 & 3	2 & 3	1 & 2	1 & 2
N/A	N/A	COM2	COM3	LPT2	1 & 2	1 & 2	2 & 3	1 & 2	1 & 2
PRI	N/A	COM1	COM2	LPT2	1 & 2	2 & 3	1 & 2	2 & 3	2 & 3
PRI	N/A	COM1	COM2	LPT2	1 & 2	2 & 3	1 & 2	2 & 3	2 & 3
SEC	N/A	COM1	COM2	LPT1	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3
PRI	N/A	COM3	COM4	LPT1	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2
PRI	N/A	COM2	COM3	LPT2	1 & 2	2 & 3	1 & 2	1 & 2	2 & 3
SEC	N/A	COM3	COM4	LPT2	1 & 2	1 & 2	1 & 2	1 & 2	2 & 3
PRI	N/A	N/A	N/A	N/A	1 & 2	2 & 3	1 & 2	1 & 2	1 & 2
NONE	NONE	NONE	NONE	NONE	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
Note: Pins designated should be in the closed position. PRI = primary and SEC = secondary.									