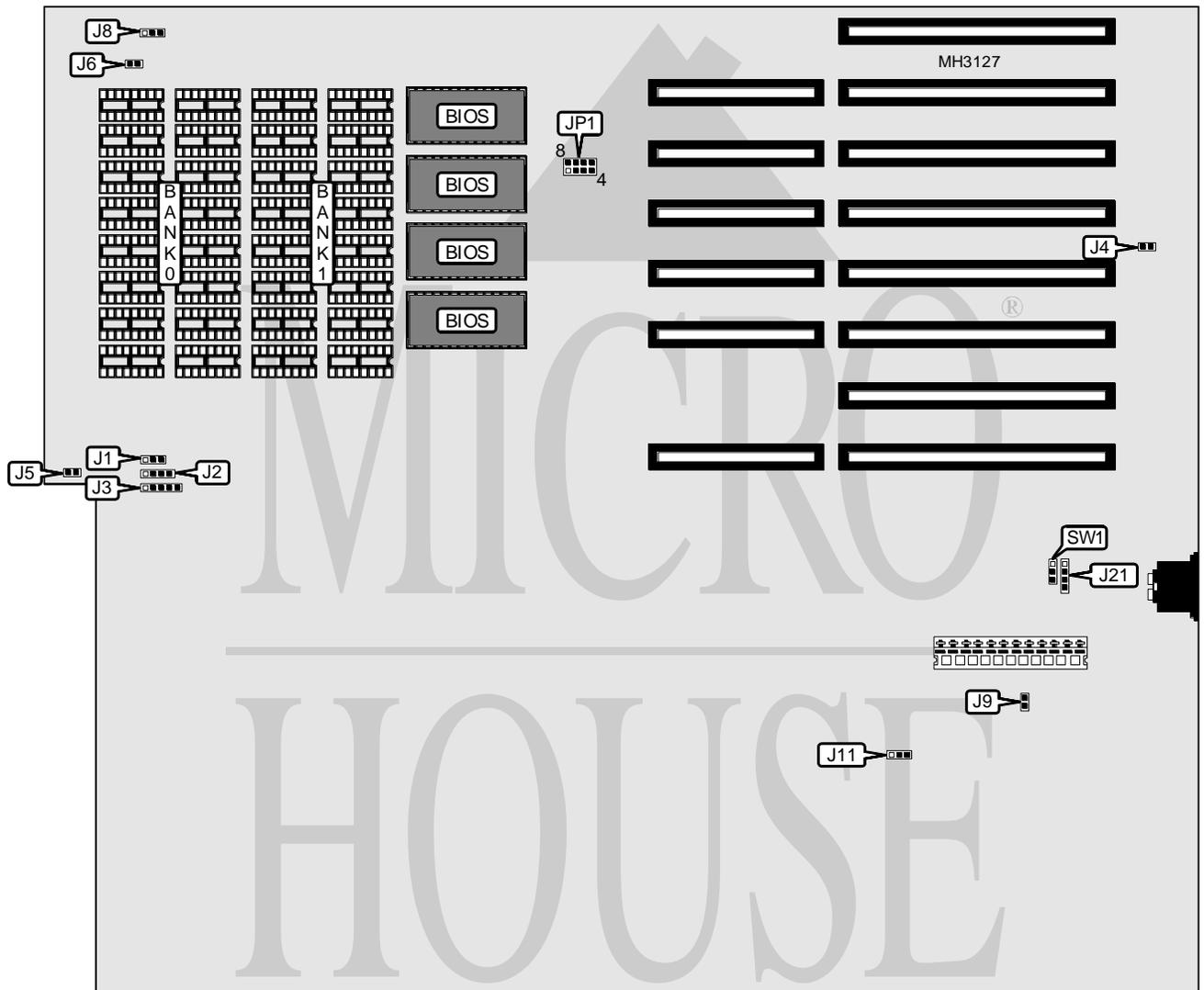


UNIDENTIFIED SUPER-286Z

Processor	80286
Processor Speed	6/10/12MHz
Chip Set	Unidentified
Max. Onboard DRAM	1MB
Cache	None
BIOS	AMI/Award/Erso/Phoenix
Dimensions	350mm x 304mm
I/O Options	None
NPU Options	80287



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J2	Reset switch	J9
Power LED & keylock	J3	External battery	J21
Turbo LED	J6		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
I/O wait state enabled	J4	Closed
I/O wait state disabled	J4	Open
Wait state select 1 wait state	J5	Closed
Wait state select 0 wait states	J5	Open
CPU speed select high speed	J8	pins 2 & 3 closed
CPU speed select keyboard selectable	J8	pins 1 & 2 closed
Monitor type select monochrome	SW1	pins 1 & 2 closed
Monitor type select color	SW1	pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
512KB	(16) 41256	NONE
640KB	(16) 41256	(16) 4164
1MB	(16) 41256	(16) 41256

DRAM JUMPER CONFIGURATION	
Size	J1
512KB	pins 2 & 3 closed
640KB	pins 2 & 3 closed
1MB	pins 1 & 2 closed

CPU SPEED CONFIGURATION	
Speed	J11
6/10MHz	pins 2 & 3 closed
6/12MHz	pins 1 & 2 closed

BIOS CONFIGURATION	
Size	JP1
27128	pins 1 & 8, 2 & 7 closed
27128/27256	pins 3 & 6, 4 & 5 closed