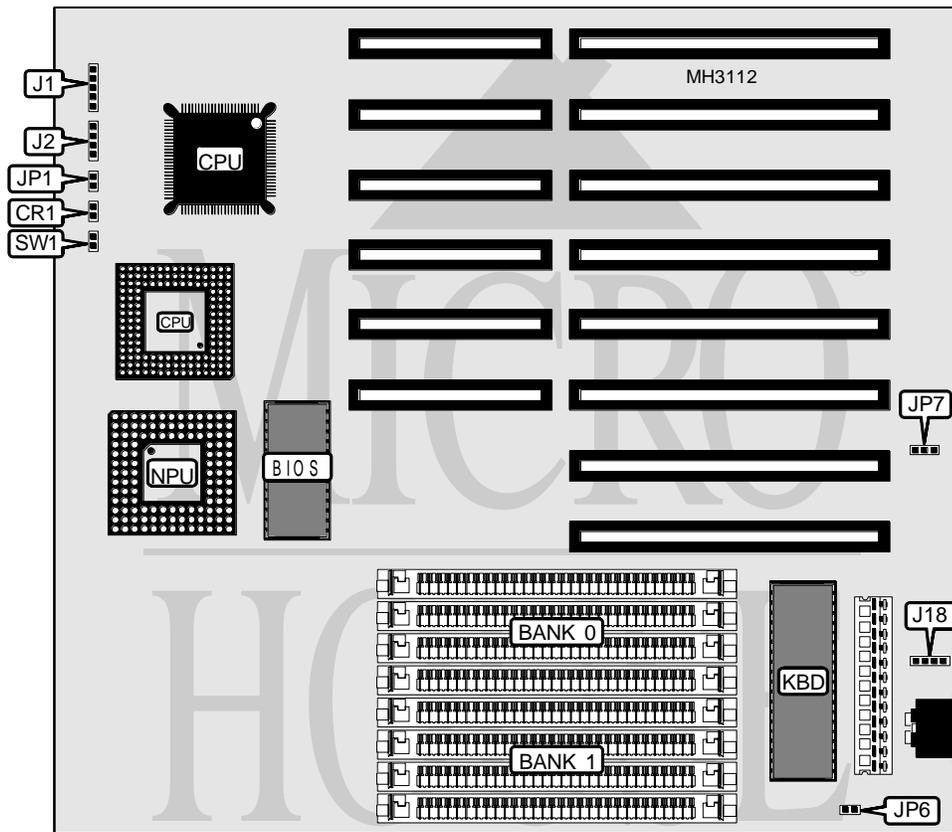


# UNIDENTIFIED OPTI396 MODEL 2

<b>Processor</b>	AM386DXL/80386DX
<b>Processor Speed</b>	25MHz
<b>Chip Set</b>	OPTI
<b>Max. Onboard DRAM</b>	32MB
<b>Cache</b>	None
<b>BIOS</b>	AMI
<b>Dimensions</b>	230mm x 220mm
<b>I/O Options</b>	None
<b>NPU Options</b>	80387DX/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	CR1	External battery	J18
Power LED & keylock	J1	Turbo switch	JP1
Speaker	J2	Reset switch	SW1

Continued on next page. . .

# UNIDENTIFIED OPTI396 MODEL 2

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP3	N/A
í Factory configured - do not alter	JP4	pins 2 & 3 closed
í Factory configured - do not alter	JP5	pins 2 & 3 closed
Monitor type select monochrome	JP6	Open
Monitor type select color	JP6	Closed
í CMOS memory normal operation	JP7	pins 2 & 3 closed
CMOS memory clear	JP7	pins 1 & 2 closed
Note: The location of jumpers JP3, JP4 and JP5 are unidentified.		

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
32MB	(4) 4M x 9	(4) 4M x 9

MISCELLANEOUS TECHNICAL NOTE
Note: The location of pin 1 is unidentified.