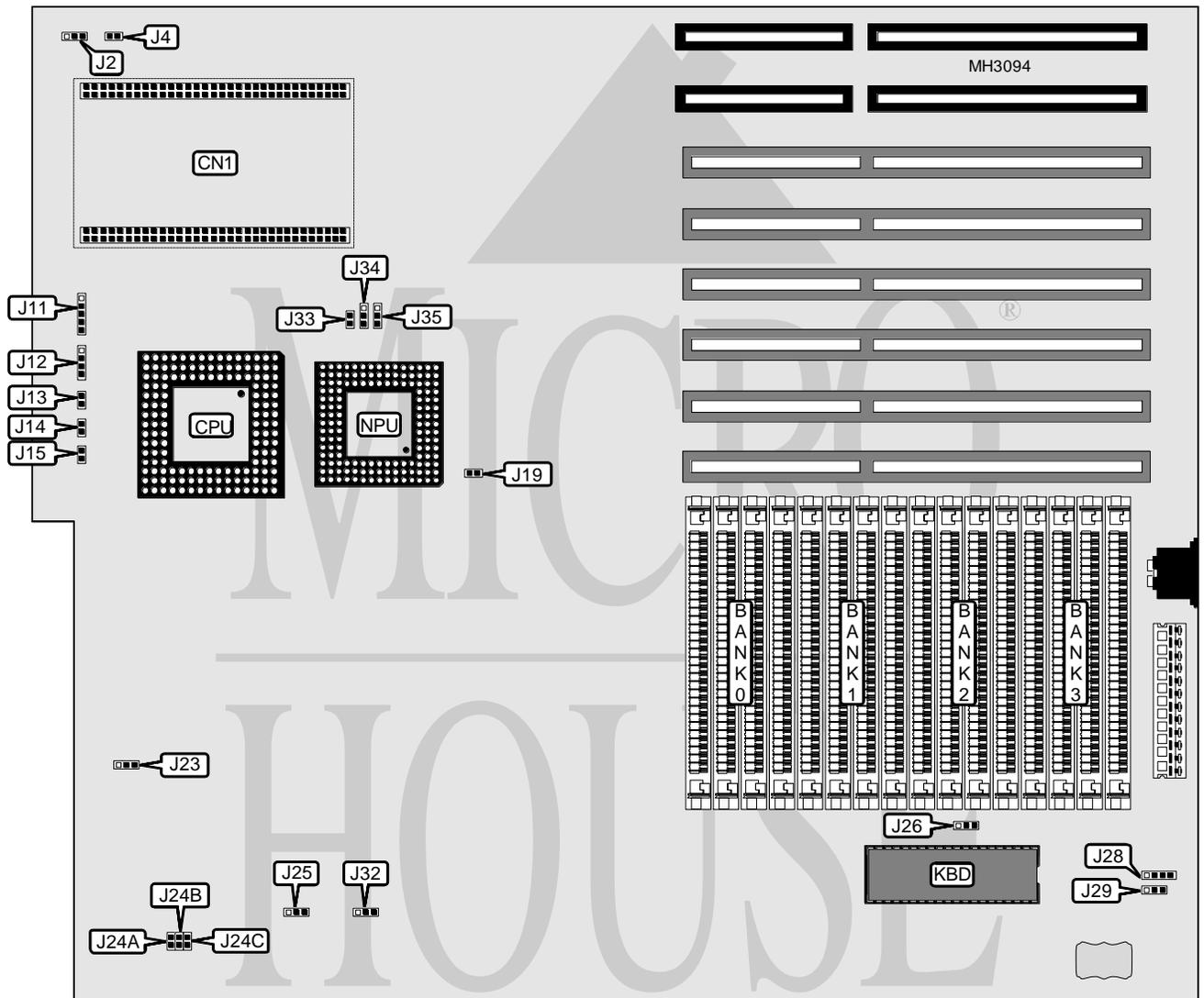


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Processor	80486SX/80487SX
Processor Speed	25/33MHz
Chip Set	Unidentified
Max. Onboard DRAM	64MB
Cache	Intel Turbocache486 module
BIOS	DTK
Dimensions	355mm x 305mm
I/O Options	Turbocache486 connector
NPU Options	4167



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CONNECTIONS			
Purpose	Location	Purpose	Location
Turbocache486 connector	CN1	Turbo LED	J13
Chassis fan power	J4	Reset switch	J14
Power LED & keylock	J11	Turbo switch	J15
Speaker	J12	External battery	J28

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select monochrome	J26	pins 1 & 2 closed
Monitor type select color	J26	pins 2 & 3 closed
í Battery type select external	J29	pins 1 & 2 closed
Battery type select internal	J29	pins 2 & 3 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
4MB	(4) 256K x 9			
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE
10MB	(4) 1M x 9	(4) 1M x 9	(4) 256K x 9	(4) 256K x 9
16MB	(4) 1M x 9			
32MB	(4) 4M x 9	(4) 4M x 9	NONE	NONE
40MB	(4) 4M x 9	(4) 4M x 9	(4) 1M x 9	(4) 1M x 9
64MB	(4) 4M x 9			

DRAM JUMPER CONFIGURATION							
Size	J2	J23	J24A	J24B	J24C	J25	J32
2MB	1 & 2	1 & 2	Closed	Closed	Closed	1 & 2	1 & 2
4MB	1 & 2	1 & 2	Closed	Closed	Open	1 & 2	1 & 2
8MB	2 & 3	2 & 3	Closed	Open	Closed	2 & 3	2 & 3
10MB	1 & 2	1 & 2	Closed	Open	Open	1 & 2	1 & 2
16MB	2 & 3	2 & 3	Open	Closed	Closed	2 & 3	2 & 3
32MB	2 & 3	2 & 3	Open	Closed	Open	2 & 3	2 & 3
40MB	2 & 3	2 & 3	Open	Open	Closed	2 & 3	2 & 3
64MB	2 & 3	2 & 3	Open	Open	Open	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

CACHE CONFIGURATION
Note: Cache is installed at CN1. The size is unidentified. Intel part #:82485MA or 82485MB.

CPU TYPE CONFIGURATION			
Type	J33	J34	J35
80486SX	Open	pins 2 & 3 closed	Open
80487SX	Closed	pins 1 & 2 closed	pins 1 & 2 closed

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CPU SPEED CONFIGURATION	
Speed	J19
25MHz	Closed
33MHz	Open