UNIDENTIFIED
4 8 6 L

Processor	80486SX/80487SX/80486DX
Processor Speed	20/25/33/50MHz
Chip Set	OPTI
Max. Onboard DRAM	32MB
Cache	64/128/256KB
BIOS	AMI
Dimensions	330mm x 220mm
I/O Options	Local bus slot
NPU Options	None
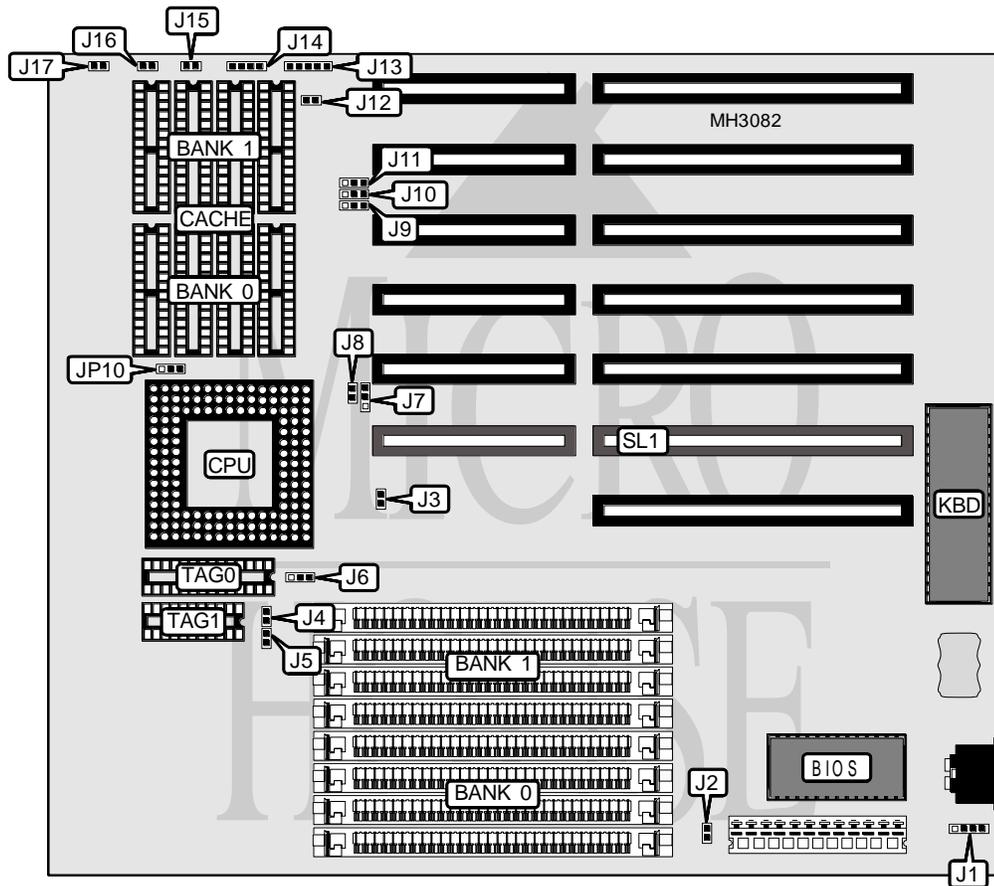
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Reset switch	J16
Power LED & keylock	J13	Turbo LED	J17
Speaker	J14	Local bus slot	SL1
Turbo switch	J15		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Battery type select internal	J1	pins 1 & 2 closed
Battery type select external	J1	Closed
í Monitor type select color	J2	Closed
Monitor type select monochrome	J2	Open
í ATCLK select CLKI/6	J3	Closed
ATCLK select CLKI/4	J3	Open
Phase frequency select single phase	J6	pins 1 & 2 closed
Phase frequency select double phase	J6	pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG 0	TAG 1
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8	(1) 16K x 4
128KB	(4) 32K x 8	NONE	(1) 8K x 8	(1) 16K x 4
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8	(1) 16K x 4

CACHE JUMPER CONFIGURATION				
Size	J4	J5	J10	J12
64KB	Open	Open	pins 2 & 3 closed	Open
128KB	Open	Closed	pins 1 & 2 closed	Closed
256KB	Closed	Closed	pins 2 & 3 closed	Closed

CPU TYPE CONFIGURATION			
Type	J7	J8	JP10
80486SX	pins 2 & 3 closed	Open	Open
80487SX	pins 1 & 2 closed	Closed	Open
80486DX	pins 1 & 2 closed	Closed	pins 2 & 3 closed

CHIPSET CONFIGURATION		
Type	J9	J11
í 82C495	pins 1 & 2 closed	pins 1 & 2 closed
82C493	pins 2 & 3 closed	pins 2 & 3 closed