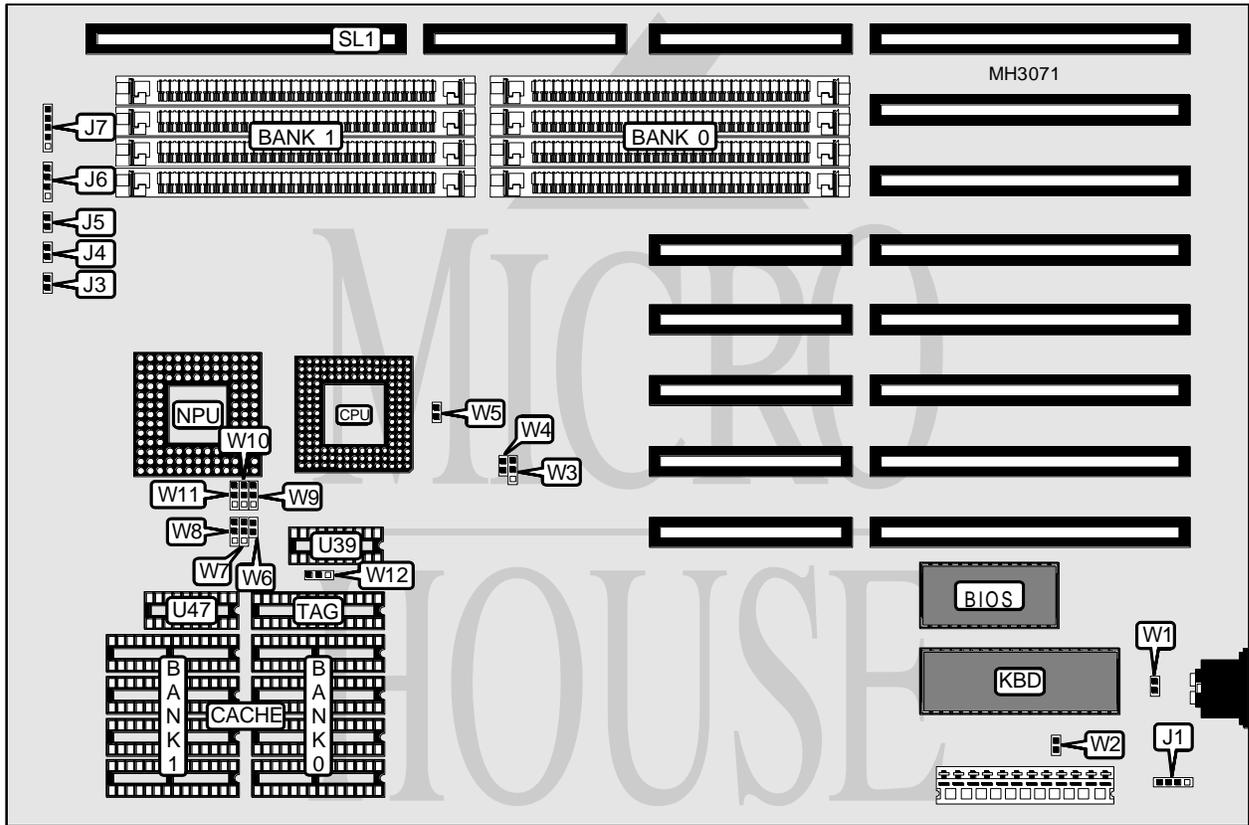


UNIDENTIFIED M317C

Processor	80386DX
Processor Speed	25/33/40MHz
Chip Set	PC Chips
Max. Onboard DRAM	32MB
Cache	32/64/128/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	Proprietary slot
NPU Options	80387DX/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Speaker	J6
Reset switch	J3	Power LED & keylock	J7
Turbo LED	J4	Proprietary slot	SL1
Turbo switch	J5		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Battery type select internal	W1	Closed
Battery type select none installed	W1	Open
í Monitor type select monochrome	W2	Open
Monitor type select color	W2	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
32KB	(4) 8K x 8	NONE	(1) 8K x 8
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

Note: To configure cache in write back mode, install (1) 16K x 4 chip in U47.

CACHE CONFIGURATION	
Size	W6
<= 16MB	Closed
> 16MB	Open

Note: To configure cache main memory over 16MB, install (1) 16K x 4 chip in U39.

CACHE JUMPER CONFIGURATION						
Size	W7	W8	W9	W10	W11	W12
32KB	2 & 3	N/A	1 & 2	1 & 2	1 & 2	2 & 3
64KB	1 & 2	N/A	1 & 2	1 & 2	2 & 3	1 & 2
128KB	2 & 3	1 & 2	1 & 2	2 & 3	2 & 3	1 & 2
256KB	1 & 2	2 & 3	2 & 3	2 & 3	2 & 3	1 & 2

Note: Pins designated should be in the closed position.

NPU CONFIGURATION			
Setting	W3	W4	W5
Not installed	pins 2 & 3 closed	Open	Open
Installed	pins 1 & 2 closed	Closed	Closed