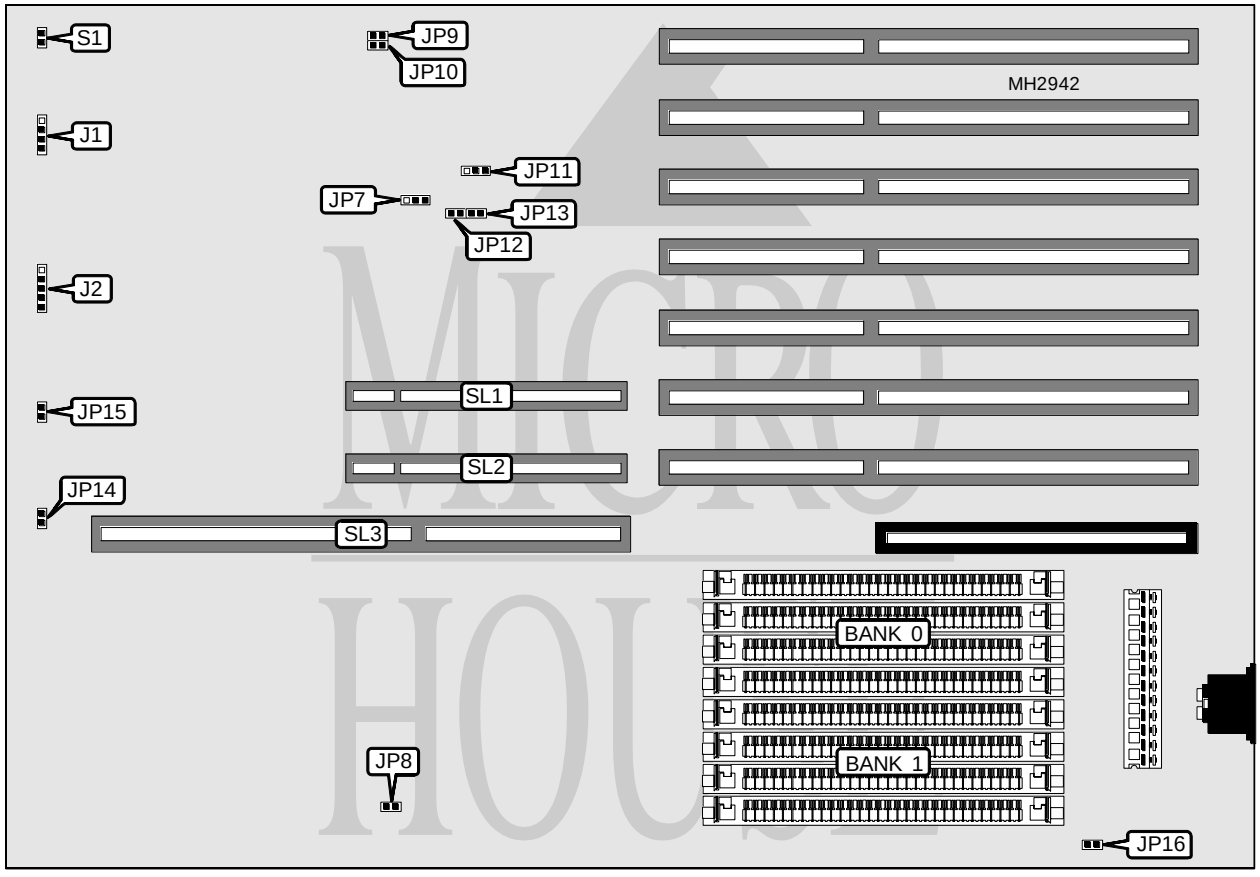
WYSE TECHNOLOGY, INC.
WYSE SERIES 6000I (MODEL 650)

Processor	80486SX/80487SX/80486DX/80486DX2
Processor Speed	16/20/25/33/50/50(internal)/66(internal)MHz
Chip Set	Unidentified
Max. Onboard DRAM	128MB
Cache	128/256/512KB
BIOS	Unidentified
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slots (2), CPU slot
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J1	32-bit VESA local bus slots	SL1 & SL2
Power LED & keylock	J2	CPU slot	SL3
Reset switch	S1		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP7	pins 2 & 3 closed
í Monitor type select monochrome	JP8	Open
Monitor type select color	JP8	Closed
í Factory configured - do not alter	JP12	Closed
í Factory configured - do not alter	JP13	Closed
í Factory configured - do not alter	JP14	Open
í Factory configured - do not alter	JP15	Open
í Factory configured - do not alter	JP16	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9
64MB	(4) 16M x 9	NONE
128MB	(4) 16M x 9	(4) 16M x 9

CPU SPEED CONFIGURATION	
Speed	JP11
16MHz	pins 2 & 3 closed
20MHz	pins 2 & 3 closed
25MHz	pins 1 & 2 closed
33MHz	pins 1 & 2 closed
50iMHz	pins 1 & 2 closed
50MHz	pins 1 & 2 closed
66iMHz	pins 1 & 2 closed

BUS SPEED CONFIGURATION		
CPU speed	JP9	JP10
<= 33MHz	Open	Open
> 33MHz	Closed	Closed

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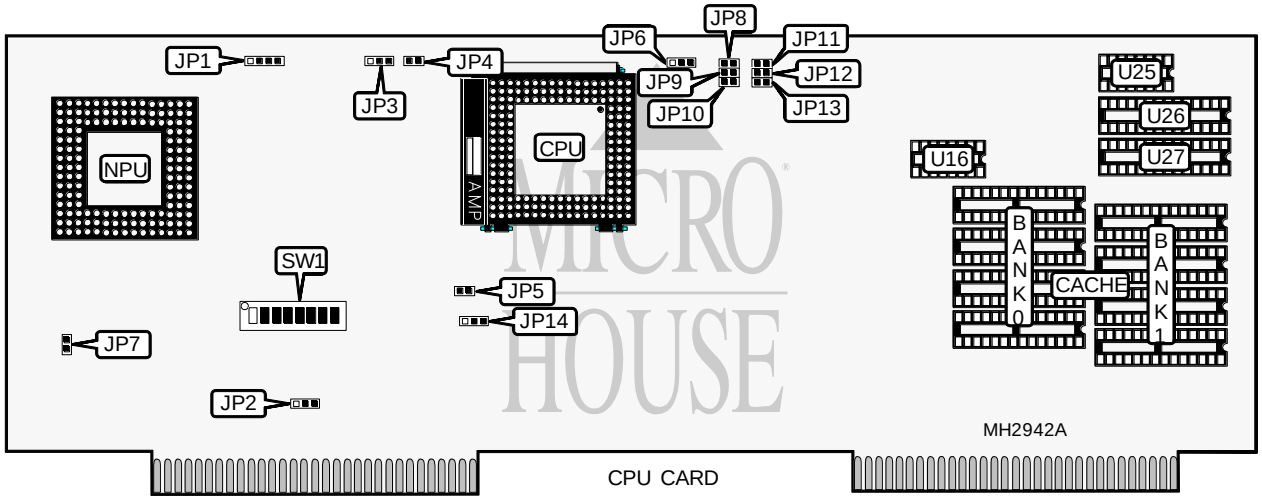

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP4	Open
í Factory configured - do not alter	JP5	Closed

CACHE CONFIGURATION		
Size	Bank 0	Bank 1
128KB	(4) 32K x 8	NONE
256KB	(4) 32K x 8	(4) 32K x 8
512MB	(4) 128K x 8	NONE

CACHE TAG CONFIGURATION				
Size	U16	U25	U26	U27
128KB	(1) 16K or (1) 64K x 1	(1) 64K x 1	NONE	(1) 8K x 1
256KB	(1) 16K or (1) 64K x 1	(1) 64K x 1	(1) 8K x 1	(1) 8K x 1
512MB	(1) 64K x 1	(1) 64K x 1	NONE	(1) 32K x 8

CACHE JUMPER CONFIGURATION							
Size	JP6	JP8	JP9	JP10	JP11	JP12	JP13
128KB	1 & 2	Closed	Open	Open	Closed	Open	Open
256KB	2 & 3	Closed	Closed	Open	Closed	Closed	Open
512MB	1 & 2	Closed	Closed	Closed	Closed	Closed	Closed
Note: Pins designated should be in the closed position.							

CPU TYPE CONFIGURATION			
Type	JP1	JP2	JP3
80486SX	pins 2 & 3 closed	pins 2 & 3 closed	Open
80487SX	pins 1 & 2, 3 & 4 closed	pins 2 & 3 closed	pins 1 & 2 closed
80486DX	pins 1 & 2, 3 & 4 closed	pins 2 & 3 closed	pins 2 & 3 closed
80486DX2	pins 1 & 2, 3 & 4 closed	pins 2 & 3 closed	pins 2 & 3 closed

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CPU SPEED CONFIGURATION									
Size	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7	SW1/8	JP14
16MHz	Off	On	Off	On	On	Off	On	Off	1 & 2
20MHz	Off	Off	Off	On	On	Off	On	Off	1 & 2
25MHz	Off	On	On	Off	On	Off	On	Off	1 & 2
33MHz	Off	Off	On	Off	On	Off	On	Off	1 & 2
50iMHz	Off	On	On	Off	On	Off	On	Off	1 & 2
66iMHz	Off	Off	On	Off	On	Off	On	Off	1 & 2
Note: Pins designated should be in the closed position.									

CPU SPEED CONFIGURATION (CON'T)	
Speed	JP7
16MHz	Closed
20MHz	Closed
25MHz	Open
33MHz	Open
50iMHz	Open
50MHz	Open
66iMHz	Open