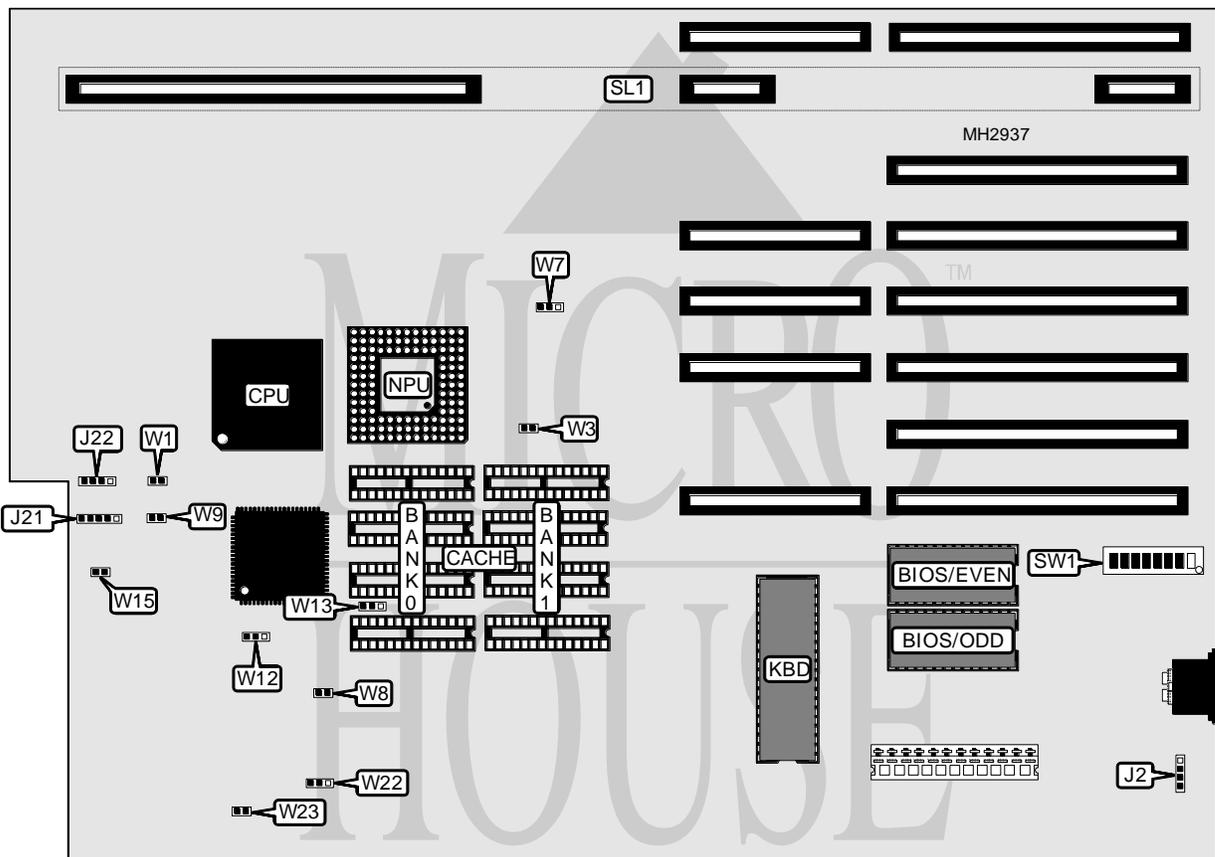


MICRONICS COMPUTERS, INC. MICRONICS 386 (VER. 5.0)

Processor 80386SX
Processor Speed 20/25/33MHz
Chip Set Unidentified
Max. Onboard DRAM 16MB (on external memory card)
Cache 32/64KB
BIOS Phoenix
Dimensions 355mm x 305mm
I/O Options External memory card
NPU Options 80387SX/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J2	Turbo LED	W9
Power LED & keylock	J21	Turbo switch	W15
Speaker	J22	External memory card	SL1
Reset switch	W1		

Continued on next page. . .

MICRONICS COMPUTERS, INC.

MICRONICS 386 (VER. 5.0)

... continued from previous page

USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Factory configured - do not alter	SW1/1	On
í NPU disabled	SW1/2	Off
NPU enabled	SW1/2	On
í Factory configured - do not alter	SW1/3	Off
í EGA/BIOS relocated	SW1/4	On
EGA/BIOS not relocated	SW1/4	Off
í Factory configured - do not alter	SW1/5	Off
í Monitor type select CGA/EGA/VGA	SW1/6	On
Monitor type select monochrome	SW1/6	Off
í Cache enabled	SW1/7	On
Cache disabled	SW1/7	Off
í Bus speed select low	SW1/8	Off
Bus speed select high	SW1/8	On
í Factory configured - do not alter	W3	Closed
í Factory configured - do not alter	W4	Open
í Factory configured - do not alter	W5	Open
í Factory configured - do not alter	W6	Open
í Factory configured - do not alter	W10	Open
í Non cacheable region at E0000 to EFFFF	W22	Closed
Non cacheable region at D0000 to DFFFF	W22	Open
í Factory configured - do not alter	W23	N/A

Note: The location of jumpers W4, W5, W6, & W10 are unidentified.

CACHE CONFIGURATION		
Size	Bank 0	Bank 1
32KB	(4) 8K x 8	NONE
64KB	(4) 8K x 8	(4) 8K x 8

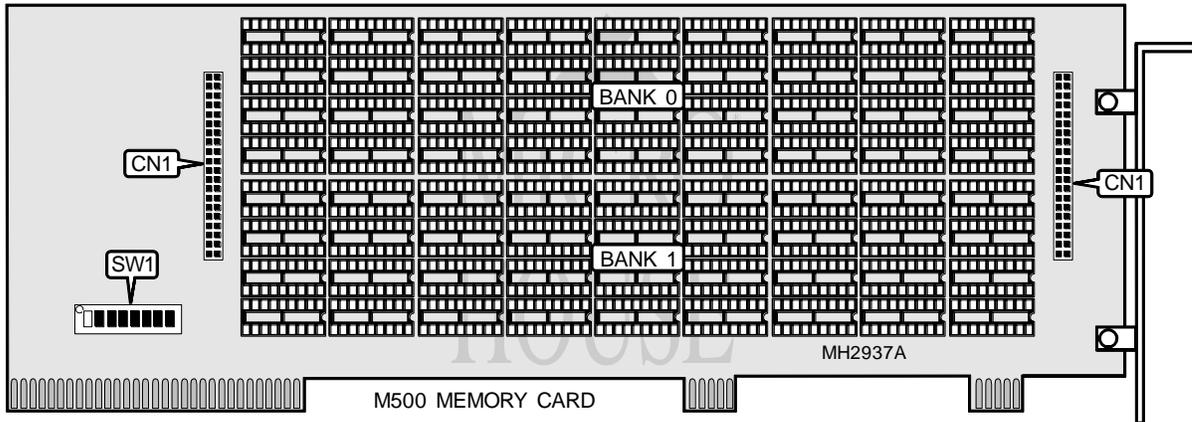
CACHE JUMPER CONFIGURATION		
Setting	W12	W13
Direct map	Closed	pins 1 & 2 closed
Two way set	Open	pins 2 & 3 closed

NPU CONFIGURATION		
Setting	W7	W8
Asynchronous	pins 1 & 2 closed	Closed
Synchronous	pins 2 & 3 closed	Open

Continued on next page. . .

MICRONICS COMPUTERS, INC.
 MICRONICS 386 (VER. 5.0)

... continued from previous page



CONNECTIONS	
Purpose	Location
Proprietary memory expansion connector	CN1

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(36) 41256	NONE
2MB	(36) 41256	(36) 41256
4MB	(36) 411000	NONE
5MB	(36) 41256	(36) 411000
8MB	(36) 411000	(36) 411000

Note: Expansion card has a maximum of 8MB. To install 12MB or 16MB, install memory card at CN1.

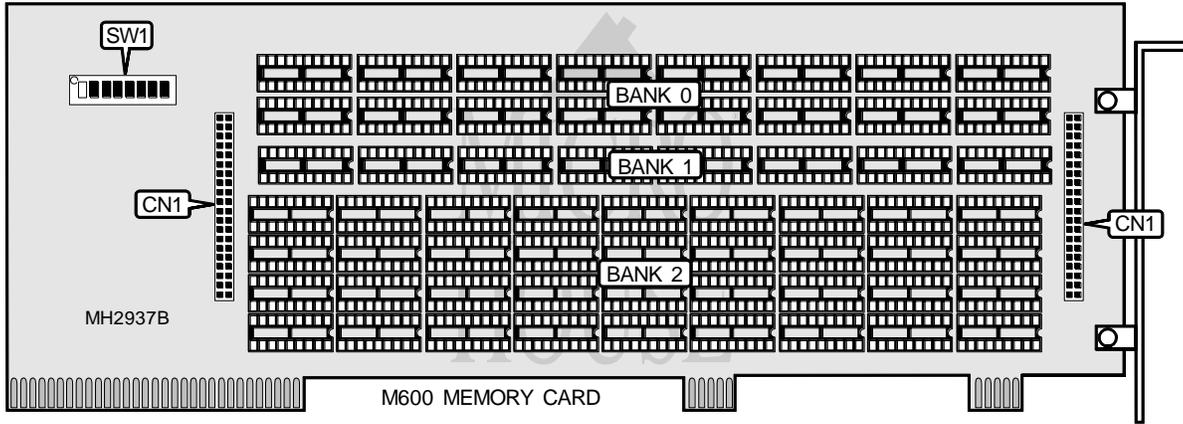
DRAM SWITCH CONFIGURATION								
Size	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7	SW1/8
1MB	On	Off	On	On	On	Off	Off	Off
2MB	Off	On	On	On	On	Off	Off	Off
4MB	On	On	Off	Off	On	Off	Off	Off
5MB	Off	Off	Off	On	On	Off	Off	Off
8MB	Off	On	Off	Off	On	Off	Off	Off
12MB	Off	Off	Off	Off	On	Off	Off	Off
16MB	Off	Off	Off	Off	On	Off	Off	Off

Note: These chips are board specific. Refer to manual for proper chip sizes.

Continued on next page...

MICRONICS COMPUTERS, INC.
 MICRONICS 386 (VER. 5.0)

... continued from previous page



CONNECTIONS

Purpose	Location
Proprietary memory expansion connector	CN1

DRAM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2
1MB	(16) 44256	NONE	NONE
2MB	(16) 44256	(8) 44256	NONE
5MB	(16) 44256	NONE	(36) 411100
6MB	(16) 44256	(8) 44256	(36) 411100

Note: Expansion card has a maximum of 6MB. To install 16MB, install memory card at CN1.

DRAM SWITCH CONFIGURATION

Size	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7	SW1/8
1MB	Off	Off	Off	Off	On	On	On	On
2MB	Off	Off	Off	On	On	On	On	On
5MB	Off	On	On	Off	On	Off	On	On
6MB	Off	On	Off	On	On	On	On	On
16MB	On							

Note: These chips are board specific. Refer to manual for proper chip sizes.