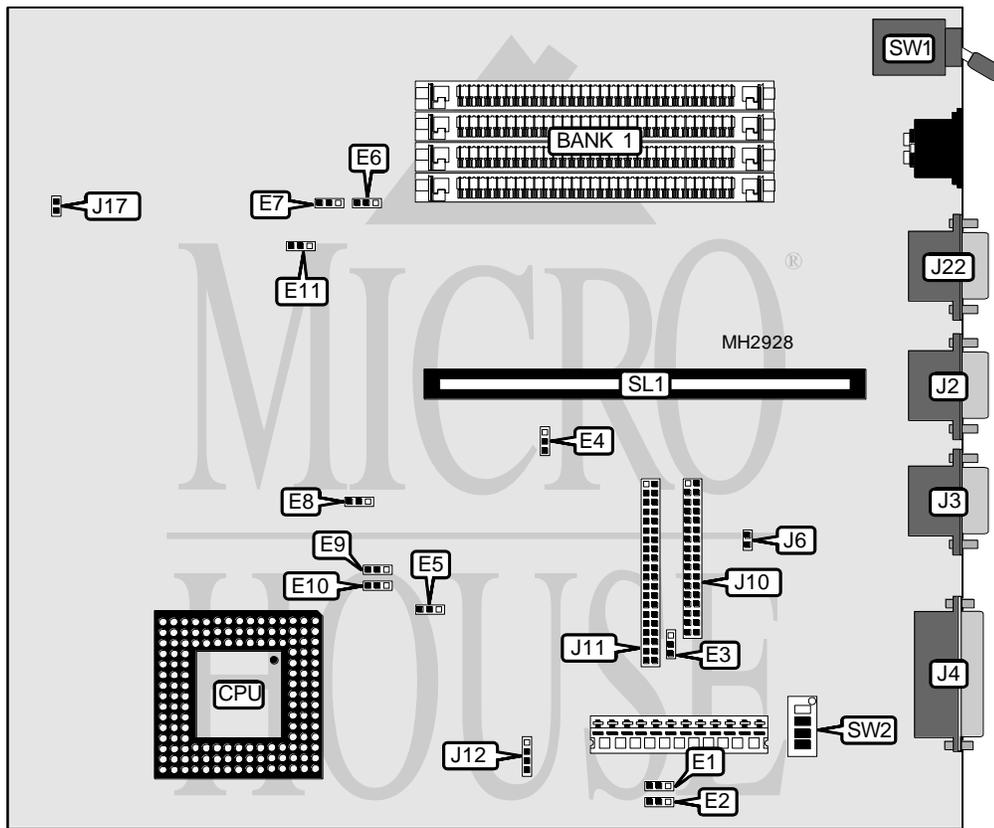


POSITIVE CORPORATION P C 4 2 5

Processor	80486SX
Processor Speed	25MHz
Chip Set	Unidentified
Max. Onboard DRAM	5MB
Cache	None
BIOS	Unidentified
Dimensions	254mm x 218mm
I/O Options	Parallel port, serial ports (2), VGA port, floppy drive interface, IDE interface, riser slot
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 2	J2	External battery	J12
Serial port 1	J3	Power/speed indicator	J17
Parallel port	J4	VGA port	J22
Speaker	J6	Riser Card	SL1
Floppy drive interface	J10	Reset switch	SW1
IDE interface	J11		

Continued on next page. . .

POSITIVE CORPORATION

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Factory configured - do not alter	E1	pins 2 & 3 closed
í IDE interface interrupt enabled	E2	pins 1 & 2 closed
IDE interface interrupt disabled	E2	Open
í Factory configured - do not alter	E3	pins 2 & 3 closed
í Factory configured - do not alter	E4	pins 2 & 3 closed
í Factory configured - do not alter	E5	pins 2 & 3 closed
í Factory configured - do not alter	E8	pins 1 & 2 closed
í Factory configured - do not alter	E9	pins 2 & 3 closed
í Parity disabled	E10	pins 2 & 3 closed
Parity enabled	E10	pins 1 & 2 closed
í Factory configured - do not alter	E11	pins 2 & 3 closed
í Onboard RAM 256KB to 512KB enabled	SW2/1	Off
Onboard RAM 256KB to 512KB disabled	SW2/1	On
í Onboard RAM 512KB to 640KB enabled	SW2/2	Off
Onboard RAM 512KB to 640KB disabled	SW2/2	On
í Security enabled	SW2/3	Off
Security disabled	SW2/3	On
í Factory configured - do not alter	SW2/4	Off

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	1MB	NONE
5MB	1MB	(4) 1M x 9
Note: Bank 0 is factory installed and is not configurable. The location is unidentified.		

IDE CONFIGURATION		
Setting	E6	E7
Primary	pins 2 & 3 closed	pins 2 & 3 closed
Secondary	pins 1 & 2 closed	pins 1 & 2 closed