UNIDENTIFIED
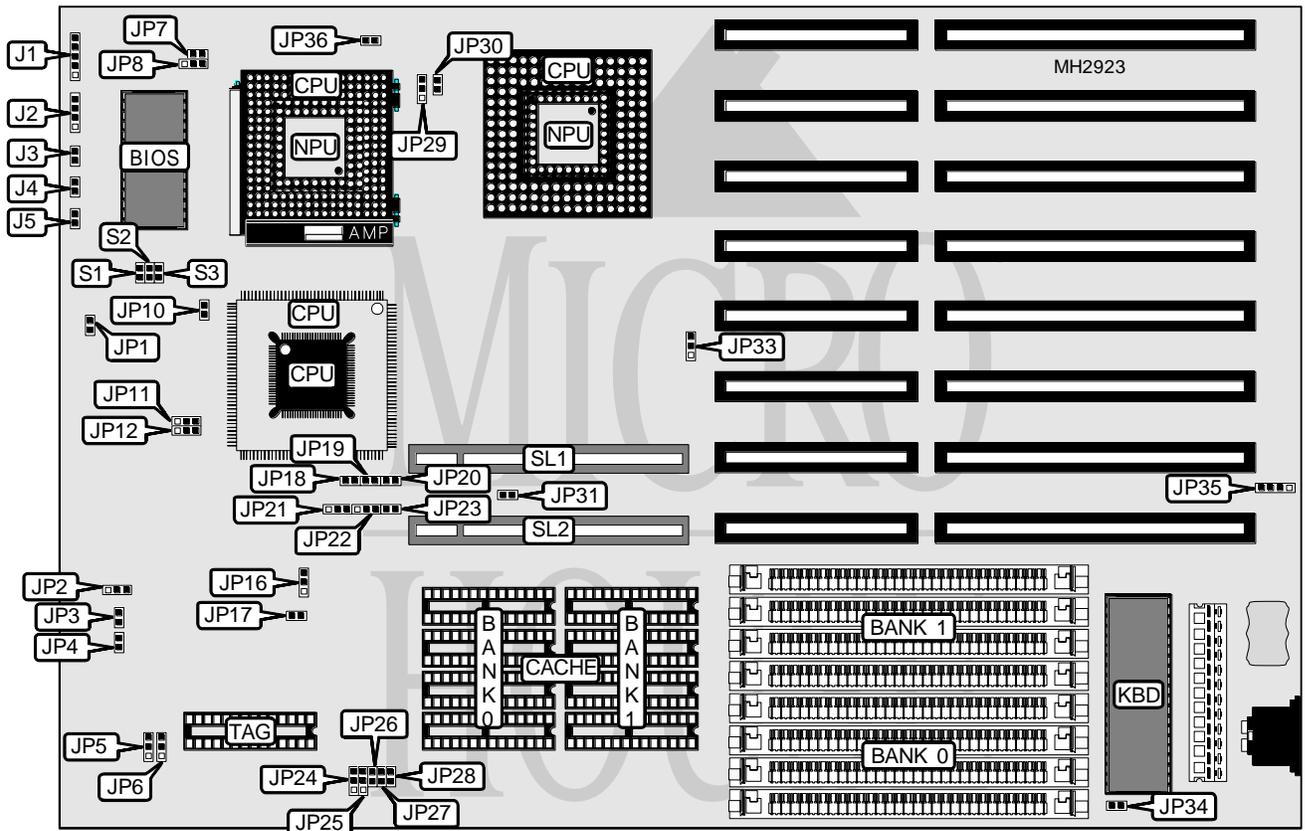# OPTI-495SX 3/486WB CACHE

Processor	80386DX/CX486DLC/80486SX/80487SX/80486DX/80486DX2
Processor Speed	25/33/40/50(internal)/50/66(internal)MHz
Chip Set	OPTI
Max. Onboard DRAM	32MB
Cache	64/128/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slots (2)
NPU Options	80387DX/CX487DLC

CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	J1	Reset switch	J5
Speaker	J2	External battery	JP35
Turbo switch	J3	32-bit VESA local bus slots	SL1 & SL2
Turbo LED	J4		

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UNIDENTIFIED
OPTI-495SX 3/486WB CACHE

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP2	N/A
í Factory configured - do not alter	JP3	N/A
í Factory configured - do not alter	JP4	N/A
í Factory configured - do not alter	JP5	N/A
í Factory configured - do not alter	JP6	N/A
í Factory configured - do not alter	JP17	N/A
í Factory configured - do not alter	JP18	N/A
í Factory configured - do not alter	JP19	N/A
í Factory configured - do not alter	JP20	N/A
í Factory configured - do not alter	JP21	N/A
í Factory configured - do not alter	JP22	N/A
í Factory configured - do not alter	JP23	N/A
í Factory configured - do not alter	JP24	N/A
í Factory configured - do not alter	JP31	N/A
í Monitor type select monochrome	JP34	Open
Monitor type select color	JP34	Closed
í CMOS memory normal operation	JP35	pins 2 & 3 closed
CMOS memory clear	JP35	pins 3 & 4 closed
Battery type select external	JP35	Closed
í NPU type select 80387 PQFP	JP36	Open
NPU type select 80387 PGA	JP36	Closed
í Factory configured - do not alter	S1	N/A
í Factory configured - do not alter	S2	N/A
í Factory configured - do not alter	S3	N/A

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	(4) 4M x 9
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

UNIDENTIFIED
OPTI-495SX 3/486WB CACHE

CACHE JUMPER CONFIGURATION				
Size	JP25	JP26	JP27	JP28
64KB	pins 2 & 3 closed	Open	Open	Open
128KB	pins 1 & 2 closed	Open	Closed	Closed
256KB	pins 2 & 3 closed	Closed	Closed	Closed

CPU TYPE CONFIGURATION	
Type	JP7
80386DX (PQFP)	Open
80386DX (PGA)	Closed
CX486DLC	Closed
80486 (PQFP)	Open
80486 (PGA)	Closed

CPU TYPE CONFIGURATION (80486 PGA)			
Type	JP8	JP29	JP30
80486SX	Open	pins 2 & 3 closed	Open
80487SX	pins 2 & 3 closed	pins 1 & 2 closed	Closed
80486DX	pins 1 & 2 closed	pins 1 & 2 closed	Closed
80486DX2	pins 1 & 2 closed	pins 1 & 2 closed	Closed

CPU SPEED CONFIGURATION (80386/CX486DLC)						
Speed	JP1	JP10	JP11	JP12	JP16	JP33
25MHz	Open	Closed	1 & 2	2 & 3	2 & 3	1 & 2
33MHz	Closed	Open	1 & 2	2 & 3	2 & 3	1 & 2
40MHz	Open	Open	1 & 2	2 & 3	2 & 3	1 & 2
Note: Pins designated should be in the closed position.						

CPU SPEED CONFIGURATION (80486)						
Speed	JP1	JP10	JP11	JP12	JP16	JP33
25MHz	Open	Closed	2 & 3	1 & 2	1 & 2	2 & 3
33MHz	Closed	Open	2 & 3	1 & 2	1 & 2	2 & 3
50iMHz	Open	Closed	2 & 3	1 & 2	1 & 2	2 & 3
50MHz	Open	Closed	1 & 2	1 & 2	1 & 2	2 & 3
66iMHz	Closed	Open	2 & 3	1 & 2	1 & 2	2 & 3
Note: Pins designated should be in the closed position.						