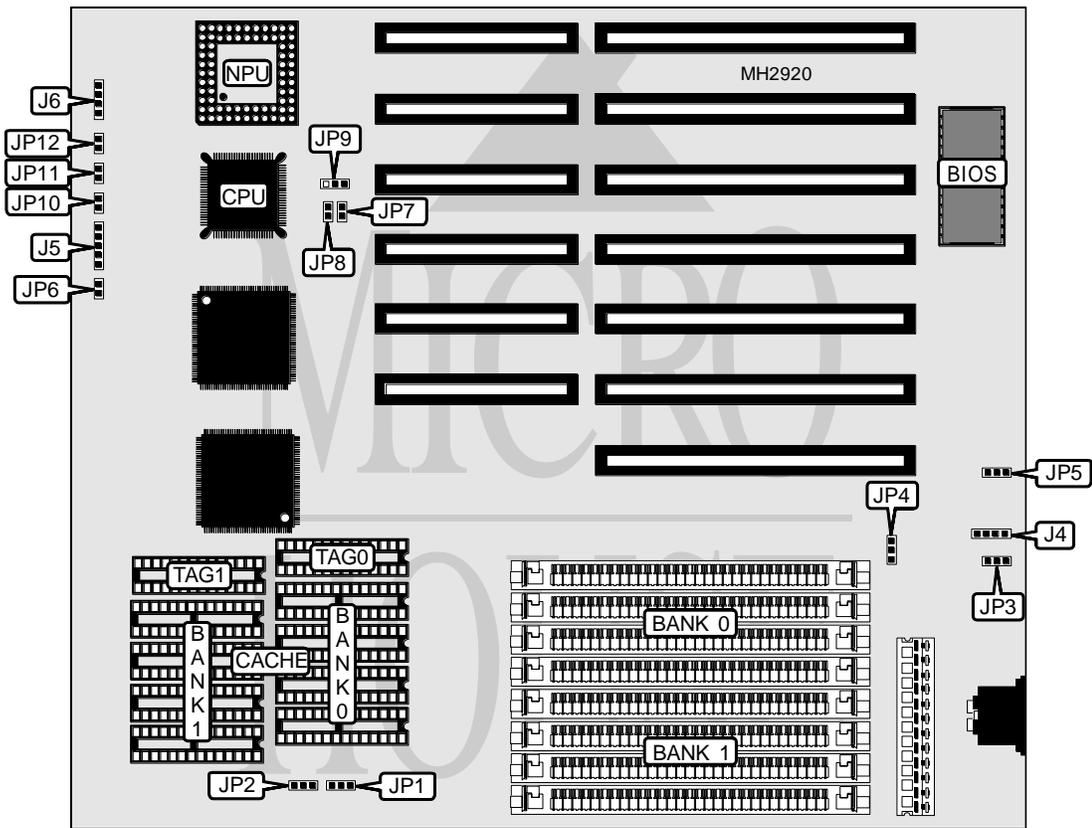


EDOM INTERNATIONAL CORPORATION

A386-33/40C (MA012)

Processor	80386DX
Processor Speed	33/40MHz
Chip Set	Unidentified
Max. Onboard DRAM	64MB
Cache	64/128/256KB
BIOS	AMI
Dimensions	260mm x 220mm
I/O Options	None
NPU Options	80387DX



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery connector	J4	Turbo LED	JP10
Power LED & keylock	J5	Reset switch	JP11
Speaker	J6	Turbo switch	JP12

Continued on next page. . .

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A386-33/40C (MA012)

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Battery type select internal	JP3	pins 1 & 2 closed
Battery type select external	JP3	pins 2 & 3 closed
í Power good signal detect from power supply	JP4	pins 2 & 3 closed
Power good signal detect from board	JP4	pins 1 & 2 closed
í Monitor type select color	JP6	Closed
Monitor type select monochrome	JP6	Open
í Clock generator	JP9	pins 1 & 2 closed
OSC	JP9	pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
32MB	(4) 4M x 9	(4) 4M x 9
64MB	(4) 16M x 9	NONE

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG0	TAG1
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8	NONE
128KB	(4) 32K x 8	NONE	(1) 32K x 8	NONE
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION		
Size	JP1	JP2
64KB	pins 1 & 2 closed	pins 1 & 2 closed
128KB	pins 2 & 3 closed	pins 1 & 2 closed
256KB	pins 2 & 3 closed	pins 2 & 3 closed

CPU SPEED CONFIGURATION		
Speed	JP7	JP8
25Mhz	Open	Closed
33Mhz	Closed	Open
40Mhz	Open	Open

VOLTAGE CONFIGURATION	
Voltage	JP5
< 5v	pins 1 & 2 closed
> 5v	pins 2 & 3 closed

TECHNICAL NOTE
Note: The location of Pin 1 is unidentified.