UNIDENTIFIED
E S - 8 1 6 3 / 3 8 6 - C S

Processor	80386SX
Processor Speed	20MHz
Chip Set	Unidentified
Max. Onboard DRAM	16MB
Cache	None
BIOS	AMI
Dimensions	220mm x 220mm
I/O Options	None
NPU Options	80387SX
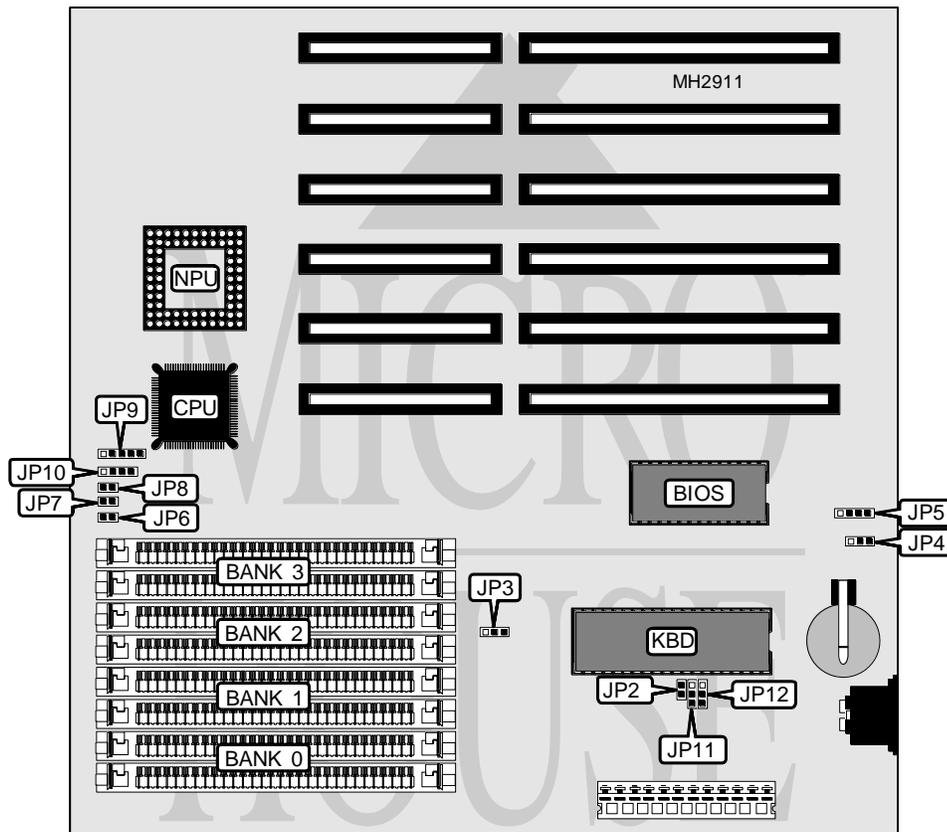
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP5	Turbo LED	JP8
Reset switch	JP6	Power LED & keylock	JP9
Turbo switch	JP7	Speaker	JP10

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ES-8163/386-CS

USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Factory configured - do not alter	JP2	N/A
í CMOS memory normal operation	JP4	pins 1 & 2 closed
CMOS memory clear	JP4	pins 2 & 3 closed
í Factory configured - do not alter	JP11	N/A
í Factory configured - do not alter	JP12	N/A

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
512KB	(2) 256K x 9	NONE	NONE	NONE
1MB	(2) 256K x 9	(2) 256K x 9	NONE	NONE
1.5MB	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9	NONE
2MB	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9
2MB	(2) 1M x 9	NONE	NONE	NONE
2.5MB	(2) 256K x 9	(2) 1M x 9	NONE	NONE
3MB	(2) 256K x 9	(2) 256K x 9	(2) 1M x 9	NONE
4MB	(2) 1M x 9	(2) 1M x 9	None	NONE
4.5MB	(2) 256K x 9	(2) 1M x 9	(2) 1M x 9	NONE
5MB	(2) 256K x 9	(2) 256K x 9	(2) 1M x 9	(2) 1M x 9
6MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	None
6.5MB	(2) 256K x 9	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9
8MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9
8MB	(2) 4M x 9	NONE	NONE	NONE
16MB	(2) 4M x 9	(2) 4M x 9	NONE	NONE

DRAM JUMPER CONFIGURATION	
Select	JP3
256KB	pins 1 & 2 closed
1MB	pins 1 & 2 closed
4MB	pins 2 & 3 closed