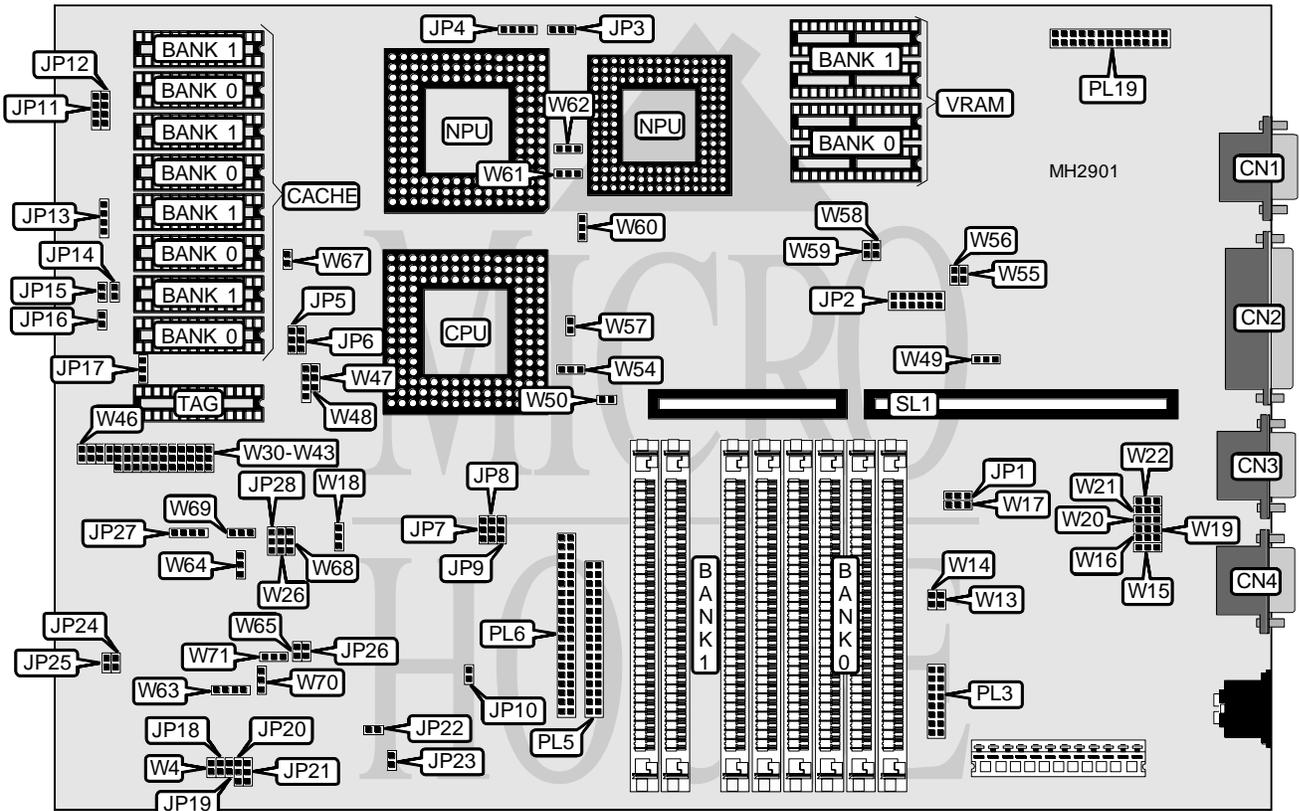


PC BRAND VALUE SERIES 486

Processor 80386DX/80486SX/80487SX/80486DX/80486DX2
Processor Speed 25/33/40/50(internal)MHz
Chip Set Unidentified
Max. Onboard DRAM 32MB
Cache 64/128/256/1024KB
BIOS Unidentified
Dimensions 330mm x 218mm
I/O Options Parallel port, serial ports (2), VGA port, VGA feature connector, floppy drive interface, IDE interface, game interface, riser slot
NPU Options 80387DX/3167/4167



CONNECTIONS			
Purpose	Location	Purpose	Location
VGA port	CN1	IDE interface	PL6
Parallel port	CN2	Reset switch	PL9
Serial port 1	CN3	Turbo LED	PL12
Serial port 2	CN4	IDE interface LED	PL14
Game interface	PL3	VGA feature connector	PL19
Floppy drive interface	PL5	Riser Card	SL1

Note: The location of jumpers PL9, PL12, PL14 are unidentified.

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP1	N/A
í Factory configured - do not alter	JP2	N/A
í Factory configured - do not alter	JP3	N/A
í Factory configured - do not alter	JP3	N/A
í Factory configured - do not alter	JP4	N/A
í Factory configured - do not alter	JP5	N/A
í Factory configured - do not alter	JP6	N/A
í Factory configured - do not alter	JP7	N/A
í Factory configured - do not alter	JP8	N/A
í Factory configured - do not alter	JP9	N/A
í Factory configured - do not alter	JP10	N/A
í Factory configured - do not alter	JP11	N/A
í Factory configured - do not alter	JP12	N/A
í Factory configured - do not alter	JP13	N/A
í Factory configured - do not alter	JP14	N/A
í Factory configured - do not alter	JP15	N/A
í Factory configured - do not alter	JP16	N/A
í Factory configured - do not alter	JP17	N/A
í Factory configured - do not alter	JP18	N/A
í Factory configured - do not alter	JP19	N/A
í Factory configured - do not alter	JP20	N/A
í Factory configured - do not alter	JP21	N/A
í Factory configured - do not alter	JP22	N/A
í Factory configured - do not alter	JP23	N/A
í Factory configured - do not alter	JP24	N/A
í Factory configured - do not alter	JP25	N/A
í Factory configured - do not alter	JP26	N/A
í Factory configured - do not alter	JP27	N/A
í Factory configured - do not alter	JP28	N/A
í External speaker enabled	PL20	pins 2 & 3 closed
External speaker disabled	PL20	pins 1 & 2 closed
í System security enabled	W4	Closed
System security disabled	W4	Open
í Game port enabled	W13	Open
Game port disabled	W13	Closed
í Floppy drive interface enabled	W19	pins 1 & 2 closed
Floppy drive interface disabled	W19	pins 2 & 3 closed
í IDE interface enabled	W20	pins 1 & 2 closed
IDE interface disabled	W20	pins 2 & 3 closed
í External speaker enabled	W28	pins 2 & 3 closed
External speaker disabled	W28	pins 1 & 2 closed

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USER CONFIGURABLE SETTINGS (CON'T)		
Function	Jumper	Position
í 80486 burst mode select 2-1-1-1	W44	Open
80486 burst mode select 3-2-2-2	W44	Closed
í Cache wait state select 0 wait states	W45	Closed
Cache wait state select 1 wait state	W45	Open
í Parallel port IRQ select IRQ5	W49	pins 2 & 3 closed
Parallel port IRQ select IRQ7	W49	pins 1 & 2 closed
í On board VGA wait state select 0 wait states	W55	Closed
On board VGA wait state select 1 wait state	W55	Open
í Factory configured - do not alter	W56	N/A

Note: The location of jumpers PL 20, W28, W44, & W45 are unidentified.

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 4M x 9	(4) 256K x 9
17MB	(4) 256K x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 32K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8
1MB	(4) 128K x 8	(4) 128K x 8	(1) 128K x 8

CACHE JUMPER CONFIGURATION					
Size	W18	W26	W30	W31	W32
None	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2
64KB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
128KB	1 & 2	1 & 2	1 & 2	1 & 2	2 & 3
128KB	1 & 2	1 & 2	1 & 2	1 & 2	2 & 3
256KB	1 & 2	1 & 2	1 & 2	1 & 2	2 & 3
1MB	1 & 2	1 & 2	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

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CACHE JUMPER CONFIGURATION (CON'T)					
Size	W33	W34	W35	W36	W37
None	Open	1 & 2	Open	Open	Open
64KB	2 & 3	1 & 2	1 & 2	1 & 2	2 & 3
128KB	1 & 2	1 & 2	Open	1 & 2	1 & 2
128KB	Open	1 & 2	Open	2 & 3	1 & 2
256KB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
1MB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

CACHE JUMPER CONFIGURATION (CON'T)					
Size	W38	W39	W40	W41	W42
None	Open	Open	Open	Open	Open
64KB	2 & 3	2 & 3	2 & 3	Open	Open
128KB	1 & 2	1 & 2	1 & 2	Closed	Open
128KB	1 & 2	1 & 2	1 & 2	Closed	Closed
256KB	2 & 3	2 & 3	2 & 3	Open	Open
1MB	2 & 3	2 & 3	2 & 3	Open	Open

Note: Pins designated should be in the closed position.

CACHE JUMPER CONFIGURATION (CON'T)				
Size	W43	W46	W68	W69
None	Open	Open	Open	Open
64KB	Closed	Open	Open	Open
128KB	Open	Open	Open	pins 1 & 2 closed
128KB	Closed	Open	Open	Open
256KB	Closed	Open	Open	pins 2 & 3 closed
1MB	Closed	Open	Open	pins 2 & 3 closed

CPU TYPE CONFIGURATION					
Type	W48	W54	W57	W60	W61
80386DX-40	2 & 3	1 & 2	Closed	1 & 2	2 & 3
80486SX-25	3 & 4	2 & 3	Open	2 & 3	1 & 2
80486DX-33	3 & 4	2 & 3	Closed	1 & 2	2 & 3
80486DX2-50	3 & 4	2 & 3	Closed	1 & 2	2 & 3

Note: Pins designated should be in the closed position.

CPU TYPE CONFIGURATION (CON'T)				
Type	W62	W63	W64	W65
80386DX-40	pins 2 & 3 closed	Open	pins 1 & 2 closed	Closed
80486SX-25	pins 1 & 2 closed	Closed	pins 2 & 3 closed	Open
80486DX-33	pins 2 & 3 closed	Closed	pins 2 & 3 closed	Open
80486DX2-50	pins 2 & 3 closed	Closed	pins 2 & 3 closed	Open

Note: Pins designated should be in the closed position.

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CPU TYPE CONFIGURATION (CON'T)				
Type	W66	W67	W70	W71
80386DX-40	Open	Open	pins 2 & 3 closed	pins 2 & 3 closed
80486SX-25	Closed	Closed	pins 1 & 2 closed	pins 1 & 2 closed
80486DX-33	Closed	Closed	pins 1 & 2 closed	pins 1 & 2 closed
80486DX2-50	Closed	Closed	pins 1 & 2 closed	pins 1 & 2 closed

CPU SPEED CONFIGURATION	
Speed	W47
1x	Closed
2x	Open

PARALLEL PORT CONFIGURATION		
Setting	W21	W29
LPT1 enabled	pins 2 & 3 closed	pins 1 & 2 closed
LPT2 disabled	pins 1 & 2 closed	pins 2 & 3 closed

Note: The location of W29 is unidentified.

SERIAL PORT 1 CONFIGURATION		
Setting	W17	W22
COM1 enabled	pins 1 & 2 closed	pins 1 & 2 closed
COM1 disabled	pins 2 & 3 closed	pins 2 & 3 closed

SERIAL PORT 2 CONFIGURATION		
Setting	W15	W16
COM2 enabled	pins 1 & 2 closed	pins 1 & 2 closed
COM2 disabled	pins 2 & 3 closed	pins 2 & 3 closed

ON BOARD VGA CONFIGURATION		
Setting	W14	W50
Enabled	Open	Closed
Disabled	Closed	Open

VRAM CONFIGURATION		
Size	Bank 0	Bank 1
512KB	(2) 256K x 4	NONE
1MB	(2) 256K x 4	(2) 256K x 4

VRAM JUMPER CONFIGURATION		
Size	W58	W59
512KB	Closed	Open
1MB	Open	Open

MISCELLANEOUS TECHNICAL NOTE
Note: The location of pin 1 is unidentified.