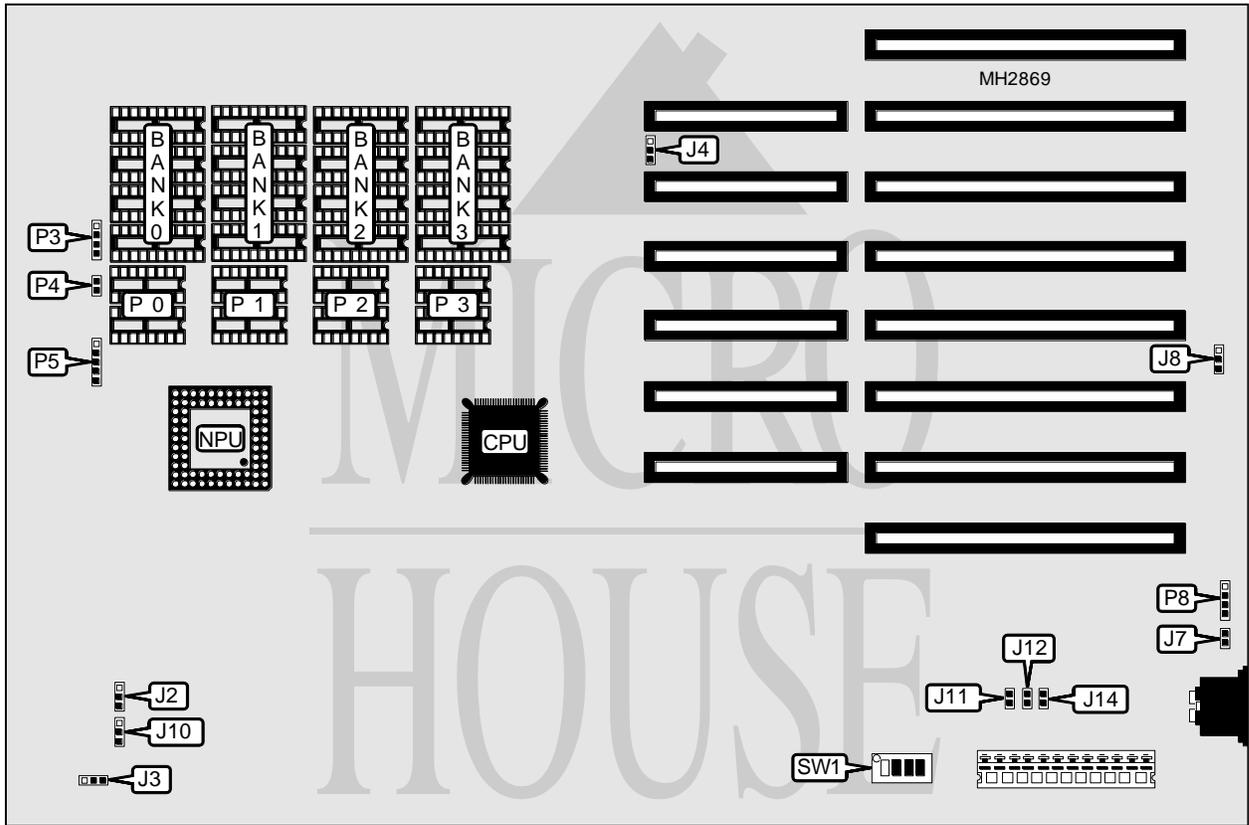


OGIVAR, INC. S X 3 8 6

Processor	80386SX
Processor Speed	12/16MHz
Chip Set	VLSI
Max. Onboard DRAM	2MB
Cache	None
BIOS	Unidentified
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	80387SX



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo switch	J14	Power LED & keylock	P5
Speaker	P3	External battery	P8
Reset switch	P4		

Continued on next page. . .

OGIVAR, INC.
SX386

... continued from previous page

USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í AT bus clock speed select 8MHz	J2	pins 1 & 2 closed
AT bus clock speed select 12MHz	J2	pins 2 & 3 closed
í BIOS type select 27256	J4	pins 2 & 3 closed
BIOS type select 27128	J4	pins 1 & 2 closed
í Battery type select alkali	J7	Closed
Battery type select lithium	J7	Open
í Power good signal detect from power supply	J8	pins 2 & 3 closed
Power good signal detect from board	J8	pins 1 & 2 closed
í Memory wait state select 0 wait states	J11	Closed
Memory wait state select 1 wait state	J11	Open
í Memory page interleave enabled	J12	Open
í Memory page interleave disabled	J12	Closed
í Turbo enabled	J14	Open
Turbo disabled	J14	Closed
í Factory configured - do not alter	SW1/1	Off
í Factory configured - do not alter	SW1/2	Off
í Monitor type select color	SW1/3	On
Monitor type select monochrome	SW1/3	Off
í Factory configured - do not alter	SW1/4	Off

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
512KB	(4) 44256	NONE	NONE	NONE
1MB	(4) 44256	(4) 44256	NONE	NONE
1.5MB	(4) 44256	(4) 44256	(4) 44256	NONE
2MB	(4) 44256	(4) 44256	(4) 44256	(4) 44256

DRAM PARITY CONFIGURATION				
Size	Parity 0	Parity 1	Parity 2	Parity 3
512KB	(2) 41256	NONE	NONE	NONE
1MB	(2) 41256	(2) 41256	NONE	NONE
1.5MB	(2) 41256	(2) 41256	(2) 41256	NONE
2MB	(2) 41256	(2) 41256	(2) 41256	(2) 41256

CPU SPEED CONFIGURATION		
Speed	J3	J10
12MHz	pins 2 & 3 closed	Open
16MHz	pins 1 & 2 closed	pins 1 & 2 closed