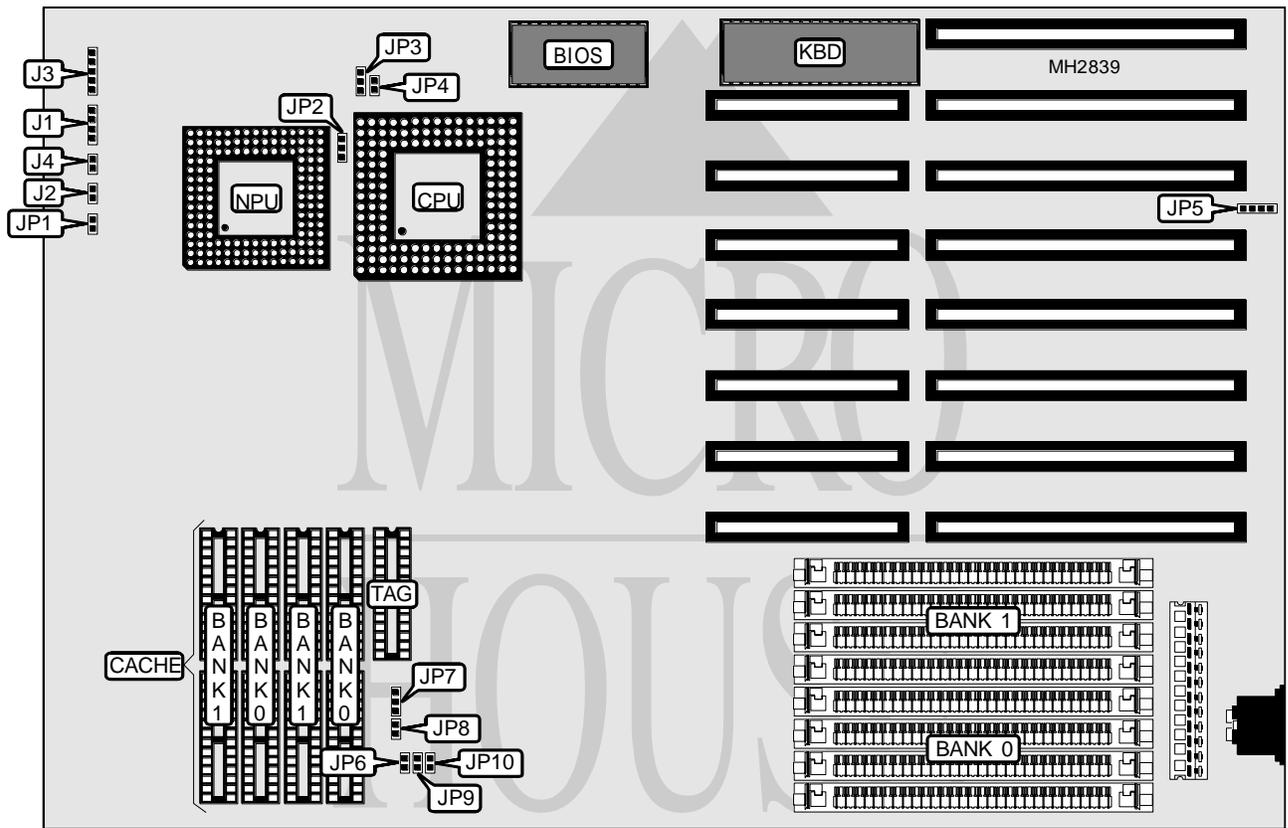


EVEREX SYSTEMS, INC.

MAXLOGIC 486DX-33 (EV-18193)

Processor 80486SX/80486DX/80486DX2
Processor Speed 25/33/50(internal)/50/66(internal)MHz
Chip Set Unidentified
Max. Onboard DRAM 32MB
Cache 64/128/256/512KB
BIOS Unidentified
Dimensions 330mm x 218mm
I/O Options None
NPU Options 4167



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J1	Reset switch	J4
Chassis fan power	J2	Turbo LED	JP1
Power LED & keylock	J3	External battery	JP5

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EVEREX SYSTEMS, INC.
MAXLOGIC 486DX-33 (EV-18193)

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP2	N/A
í Factory configured - do not alter	JP3	N/A
í Factory configured - do not alter	JP4	N/A

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 256K x 9	(4) 4M x 9
17MB	(4) 4M x 9	(4) 256K x 9
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(8) 8K x 8	NONE	(1) 8K x 8
128KB	(8) 8K x 8	(8) 8K x 8	(1) 8K x 8
256KB	(8) 32K x 8	NONE	(1) 32K x 8
512KB	(8) 32K x 8	(8) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION					
Size	JP6	JP7	JP8	JP9	JP10
64KB	Open	pins 2 & 3 closed	Open	Open	Open
128KB	Closed	pins 2 & 3 closed	Open	Open	Closed
256KB	Closed	pins 2 & 3 closed	Open	Closed	Closed
512KB	Closed	pins 1 & 2 closed	Closed	Closed	Closed

MISCELLANEOUS TECHNICAL NOTE
 Note: The orientation of pin 1 is unidentified.