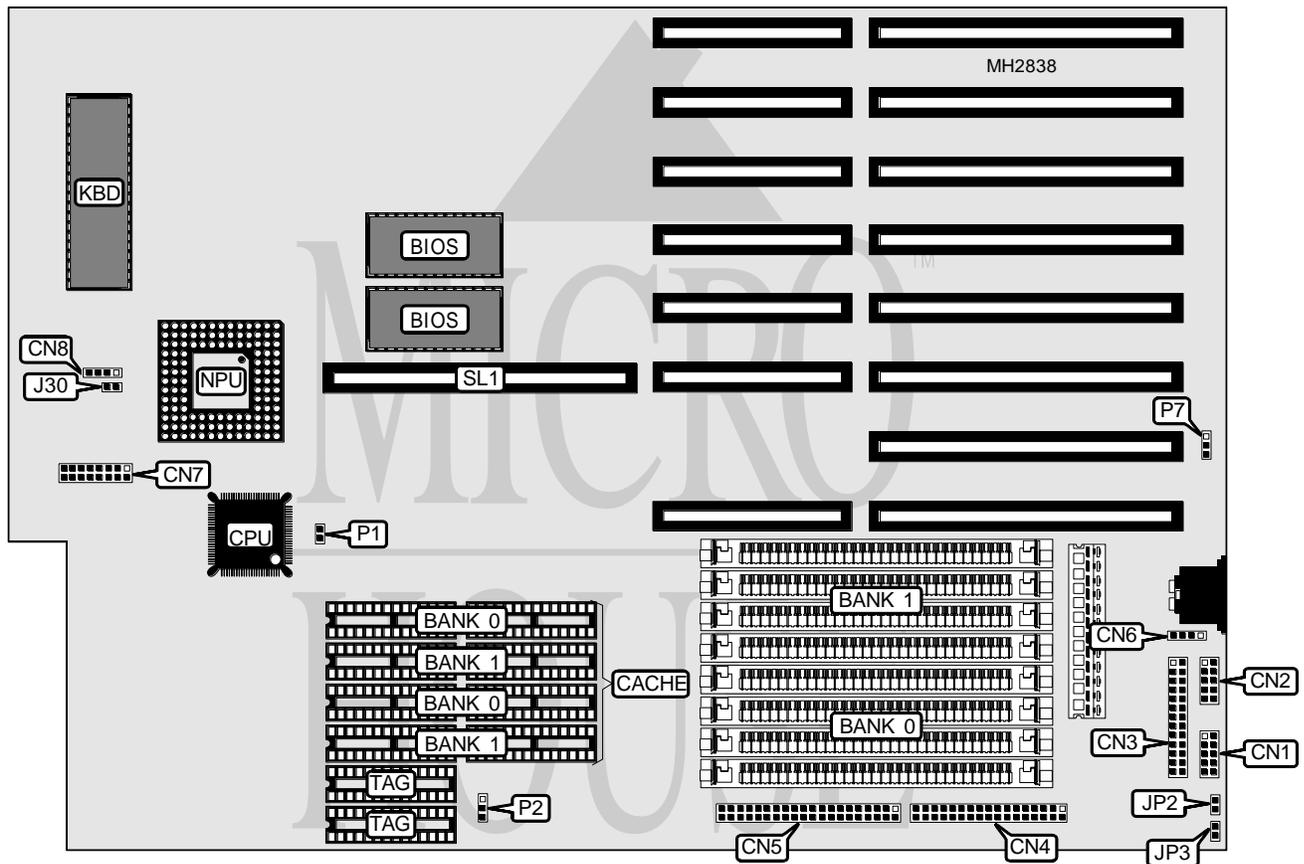


EVEREX SYSTEMS, INC.

EV - 18104

Processor	80386SX
Processor Speed	25/33MHz
Chip Set	Unidentified
Max. Onboard DRAM	32MB
Cache	128/256KB
BIOS	Unidentified
Dimensions	330mm x 218mm
I/O Options	Floppy drive interface, IDE interface, parallel port, serial ports (2), external memory slot
NPU Options	80387DX/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 1	CN1	External battery	CN6
Serial port 2	CN2	Front panel connector	CN7
Parallel port	CN3	Speaker	CN8
Floppy drive interface	CN4	IDE interface LED	J30
IDE interface	CN5	External memory card	SL1

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EVEREX SYSTEMS, INC.
EV - 18104

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
Factory configured - do not alter	P1	Open
í Factory configured - do not alter	P7	pins 2 & 3 closed
Factory configured - do not alter	JP2	Open
í Factory configured - do not alter	JP3	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
128KB	(4) 32K x 8	NONE	(2) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(2) 8K x 8

Note: If external memory card is installed, 256KB cache must be installed.

CACHE JUMPER CONFIGURATION	
Size	P2
128KB	pins 2 & 3 closed
256KB	pins 1 & 2 closed