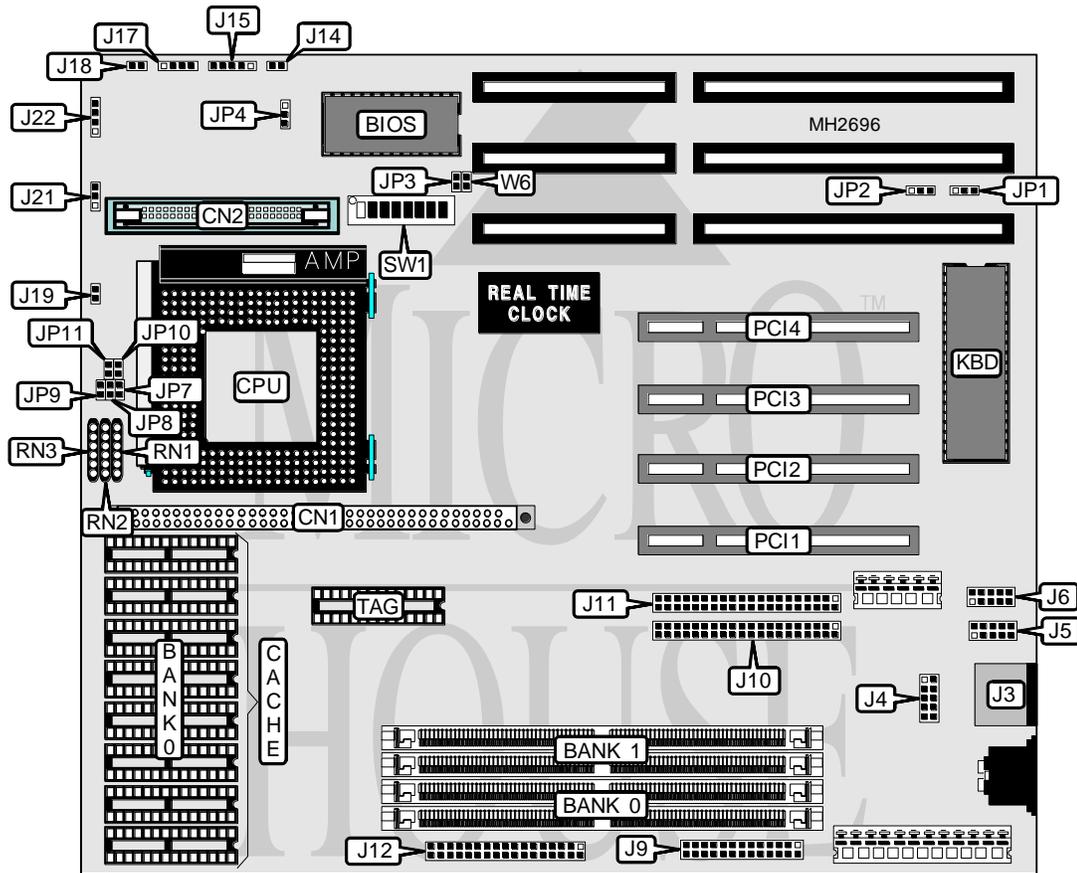


# TMC RESEARCH CORPORATION PCI541T (VER. 0.10)

<b>Processor</b>	Pentium
<b>Processor Speed</b>	75/90/100/120/130/150/166/180/200MHz
<b>Chip Set</b>	Intel
<b>Max. Onboard DRAM</b>	128MB
<b>Cache</b>	256/512KB
<b>BIOS</b>	Unidentified
<b>Dimensions</b>	254mm x 218mm
<b>I/O Options</b>	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), external cache slot, external video slot
<b>NPU Options</b>	None



CONNECTIONS			
Purpose	Location	Purpose	Location
External cache slot	CN1	Reset switch	J14
External video slot	CN2	Power LED & keylock	J15
PS/2 mouse port	J3	Green PC LED	J17
Serial port 1	J5	Turbo LED	J18
Serial port 2	J6	Green PC connector	J19
Parallel port	J9	Chassis fan power	J21
IDE interface (primary)	J10	Speaker	J22
IDE interface (secondary)	J11	32-bit PCI slots	PCI1 - PCI4
Floppy drive interface	J12		

Continued on next page . . .

# TMC RESEARCH CORPORATION

## PCI54IT (VER. 0.10)

... continued from previous page

USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Factory configured - do not alter	J4	N/A
í Flash bios normal mode	JP4	pins 2 & 3 closed
Flash bios programing mode	JP4	pins 1 & 2 closed
í Factory configured - do not alter	RN1	N/A
í Factory configured - do not alter	RN2	N/A
í Factory configured - do not alter	RN3	N/A
í CPU voltage select normal	SW1/2	On
CPU voltage select high	SW1/2	Off
í Password normal operation	SW1/3	Off
Password clear operation	SW1/3	On
í CMOS memory normal operation	SW1/4	Off
CMOS memory clear operation	SW1/4	On
í Flash BIOS write protect enabled	SW1/5	On
Flash BIOS write protect disabled	SW1/5	Off
í Factory configured - do not alter	W6	N/A

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(2) 4M x 36	NONE
16MB	(8) 8M 36	NONE
16MB	(2) 4M x 36	(2) 4M x 36
24MB	(2) 4M x 36	(8) 8M 36
32MB	(2) 16M x 36	NONE
32MB	(8) 8M 36	(8) 8M 36
40MB	(2) 4M x 36	(2) 16M x 36
48MB	(8) 8M 36	(2) 16M x 36
64MB	(2) 32M x 36	NONE
64MB	(2) 16M x 36	(2) 16M x 36
72MB	(2) 4M x 36	(2) 32M x 36
80MB	(8) 8M 36	(2) 32M x 36
96MB	(2) 16M x 36	(2) 32M x 36
128MB	(2) 32M x 36	(2) 32M x 36

CACHE CONFIGURATION		
Size	Bank 0	TAG
256KB	(8) 32K x 8	(1) 8K x 8
512KB	(8) 64K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION					
Size	JP7	JP8	JP9	JP10	JP11
None	Open	Open	Open	Open	Open
256KB (On board)	Closed	Open	Open	Open	Closed
256KB (Cache card)	Closed	Open	Open	Open	Open
512KB (On board)	Open	Closed	Closed	Open	Closed
512KB (Cache card)	Open	Closed	Closed	Open	Open

Continued on next page. . .

# TMC RESEARCH CORPORATION

## PCI541T (VER. 0.10)

... continued from previous page

CPU SPEED CONFIGURATION						
Bus clock	CPU	JP3	SW1/1	SW1/6	SW1/7	SW1/8
50MHz	75MHz	Open	On	Off	Off	Off
60MHz	90MHz	Open	On	Off	On	Off
66MHz	100MHz	Open	On	Off	On	On
60MHz	120MHz	Open	On	On	On	Off
66MHz	133MHz	Open	On	On	On	On
60MHz	150MHz	Closed	On	On	On	Off
66MHz	166MHz	Closed	On	On	On	On
60MHz	180MHz	Closed	On	Off	On	Off
66MHz	200MHz	Closed	On	Off	On	On

DMA CONFIGURATION		
DMA	JP1	JP2
DMA1	pins 1 & 2 cloed	pins 1 & 2 closed
DMA3	plins 2 & 3 closed	pins 2 & 3 closed