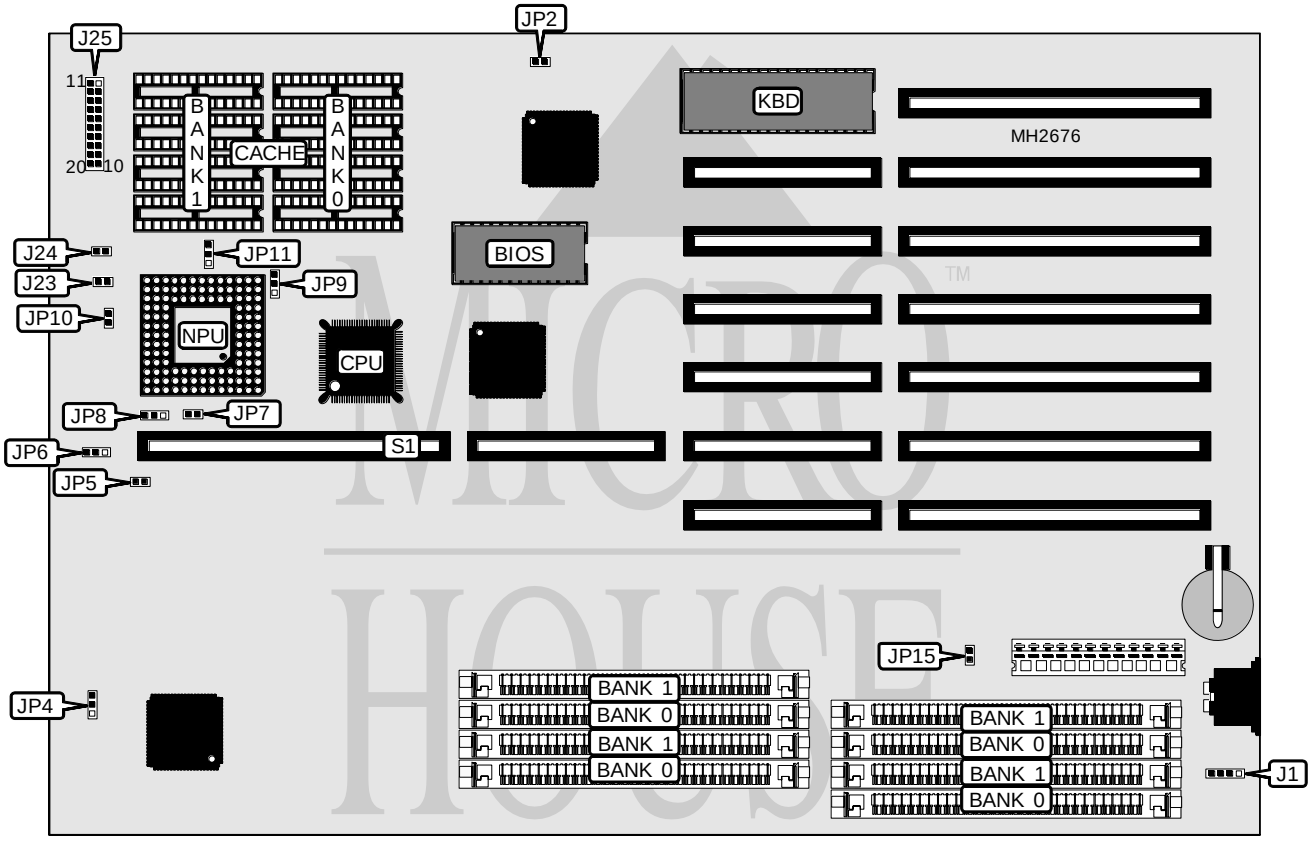
TMC RESEARCH CORPORATION
P A T 3 8 P C

Processor	80386DX
Processor Speed	25/33MHz
Chip Set	Unidentifed
Max. Onboard DRAM	24MB (16MB on external memory card)
Cache	32/64KB
BIOS	Unidentified
Dimensions	330mm x 218mm
I/O Options	External memory card slots
NPU Options	80387/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
IDE interface LED	J24	Reset switch	J25 (9 & 19)
Speaker	J25 (1 - 4)	IDE interface LED	J25 (10 & 20)
Turbo switch	J25 (7 & 17)	Power LED & keylock	J25 (11 - 15)
Turbo LED	J25 (8 & 18)	External memory card slot	S1

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TMC RESEARCH CORPORATION
P A T 3 8 P C

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	J1	N/A
í Factory configured - do not alter	J23	N/A
í Monitor type select color	JP2	Closed
Monitor type select monochrome	JP2	Open
í Pipeline mode enabled	JP9	pins 2 & 3 closed
Pipeline mode disabled	JP9	pins 1 & 2 closed
í NPU disabled	JP10	Open
NPU enabled	JP10	Closed
í Battery type select internal	JP15	Closed
Battery type select external	JP15	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9

CACHE CONFIGURATION		
Size	Bank 0	Bank 1
32KB	(4) 8K x 8	NONE
64KB	(4) 8K x 8	(4) 8K x 8

CACHE JUMPER CONFIGURATION				
Size	JP4	JP5	JP6	JP11
32KB	pins 2 & 3 closed	Open	pins 1 & 2 closed	pins 1 & 2 closed
64KB	pins 2 & 3 closed	Closed	pins 2 & 3 closed	pins 2 & 3 closed

CACHE LINE SIZE CONFIGURATION		
Size	JP7	JP8
4 bytes	Closed	pins 1 & 2 closed
8 bytes	Closed	pins 2 & 3 closed
16 bytes	Open	pins 2 & 3 closed

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TMC RESEARCH CORPORATION
P A T 3 8 P C

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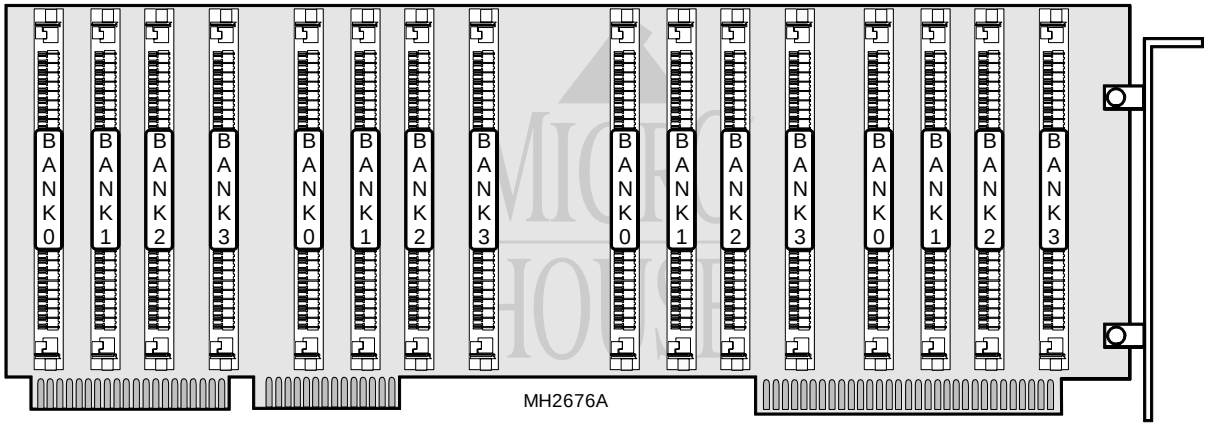
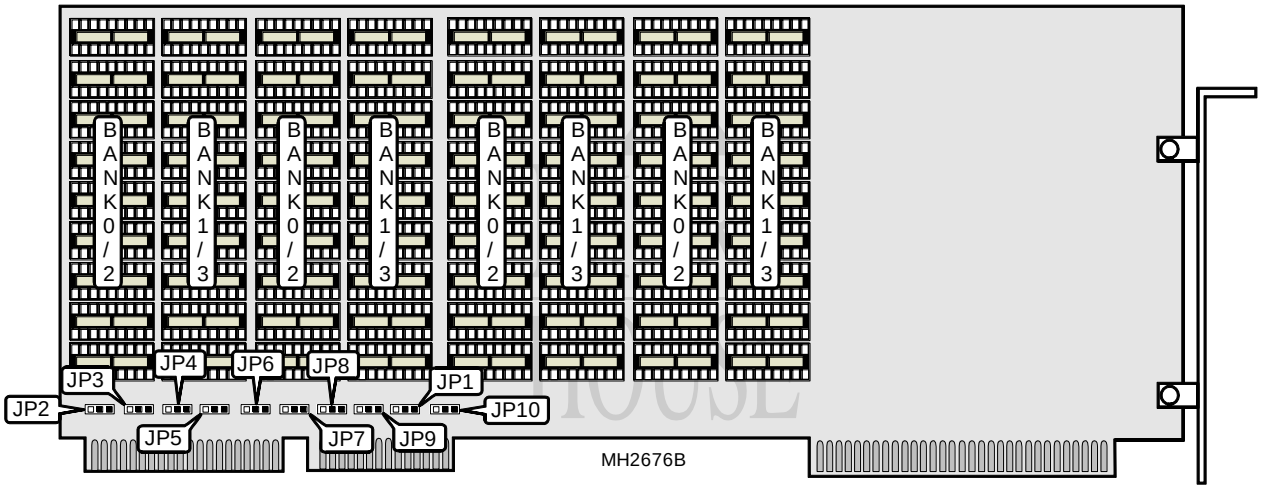


DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 3	Bank 4
1MB	(4) 256K x 9	NONE	NONE	NONE
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
3MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	NONE
4MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE	NONE	NONE
6MB	(4) 1M x 9	(4) 256K x 9	(4) 256K x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE
10MB	(4) 1M x 9	(4) 1M x 9	(4) 256K x 9	(4) 256K x 9
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE
16MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9



DRAM CONFIGURATION		
Size	Bank 0/2	Bank 1/3
4MB	(36) 411000	NONE
8MB	(36) 411000	(36) 411000

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TMC RESEARCH CORPORATION
P A T 3 8 P C

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DRAM JUMPER CONFIGURATION					
Setting	JP1	JP2	JP3	JP4	JP5
Bank 0/1	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
Bank 1/3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
Note: Pins designated should be in closed position.					

DRAM JUMPER CONFIGURATION (CON'T)					
Setting	JP6	JP7	JP8	JP9	JP10
Bank 0/1	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
Bank 1/3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
Note: Pins designated should be in closed position.					

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