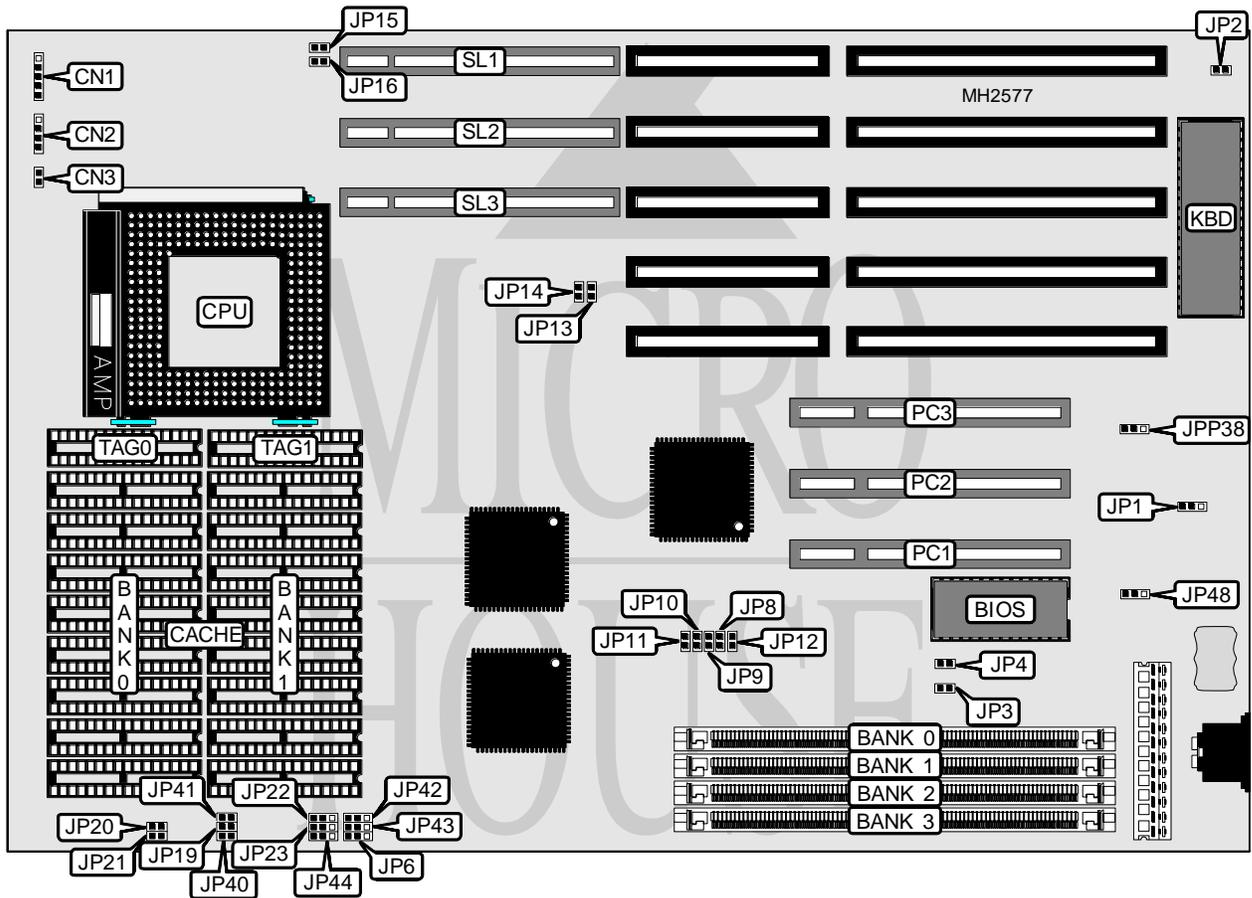


YOUNG MICRO SYSTEMS, INC. CROSS P5-VIP (REV 1.0)

Processor	Pentium
Processor Speed	60/66MHz
Chip Set	OPT1
Max. Onboard DRAM	128MB
Cache	64/128/256/512/1024KB
BIOS	Award
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slots (3), 32-bit PCI slots (3)
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	CN1	32-bit VESA local bus slots	SL1 - SL3
Speaker	CN2	32-bit PCI slots	PC1 - PC3
Reset switch	CN3		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP1	pins 2 & 3 closed
CMOS memory clear	JP1	pins 1 & 2 closed
í Monitor type select color	JP2	Closed
Monitor type select monochrome	JP2	Open
í Factory configured - do not alter	JP3	Closed
í Factory configured - do not alter	JP4	Closed
í Factory configured - do not alter	JP10	Open
í Factory configured - do not alter	JP11	Open
í Factory configured - do not alter	JP12	Open
í Flash BIOS select normal	JP48	pins 1 & 2 closed
Flash BIOS select programming	JP48	pins 2 & 3 closed
í Factory configured - do not alter	JPP38	pins 1 & 2 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
2MB	(1) 256K x 36	(1) 256K x 36	NONE	NONE
4MB	(1) 512K x 36	(1) 512K x 36	NONE	NONE
4MB	(1) 256K x 36			
8MB	(1) 1M x 36	(1) 1M x 36	NONE	NONE
16MB	(1) 1M x 36			
32MB	(1) 4M x 36	(1) 4M x 36	NONE	NONE
40MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36
64MB	(1) 8M x 36	(1) 8M x 36	NONE	NONE
64MB	(1) 4M x 36			
128MB	(1) 8M x 36			

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG 0	TAG 1
64KB	(8) 8K x 8	NONE	NONE	(1) 8K x 8
128KB	(8) 8K x 8	(8) 8K x 8	NONE	(1) 8K x 8
256KB	(8) 32K x 8	NONE	NONE	(1) 8K x 8
512KB	(8) 32K x 8	(8) 32K x 8	(1) 32K x 8	(1) 32K x 8
1MB	(8) 128K x 8	NONE	NONE	(1) 128K x 8

CACHE JUMPER CONFIGURATION					
Size	JP6	JP19	JP20	JP21	JP22
64KB	1 & 2	Open	Open	Open	1 & 2
128KB	2 & 3	Closed	Open	Open	2 & 3
256KB	1 & 2	Closed	Closed	Open	1 & 2
512KB	2 & 3	Closed	Closed	Closed	2 & 3
1MB	1 & 2	Closed	Closed	Closed	1 & 2

Note: Pins designated should be in the closed position.

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CACHE JUMPER CONFIGURATION (CON'T)						
Size	JP23	JP40	JP41	JP42	JP43	JP44
64KB	1 & 2	Open	Open	1 & 2	1 & 2	1 & 2
128KB	2 & 3	Open	Open	2 & 3	2 & 3	1 & 2
256KB	1 & 2	Open	Open	1 & 2	1 & 2	1 & 2
512KB	2 & 3	Open	Open	2 & 3	2 & 3	1 & 2
1MB	1 & 2	Closed	Open	1 & 2	1 & 2	2 & 3

Note: Pins designated should be in the closed position.

CPU SPEED CONFIGURATION		
Speed	JP13	JP14
60MHz	Closed	Open
66MHz	Closed	Closed

AT CLOCK CONFIGURATION		
Setting	JP8	JP9
LCLK/2	Open	Open
LCLK/3	Closed	Open
LCLK/4	Open	Closed
LCLK/5	Closed	Closed

VESA WAIT STATE CONFIGURATION	
Wait states	JP15
0 wait states	Open
1 wait state	Closed

BUS SPEED CONFIGURATION	
CPU speed	JP16
<= 33MHz	Open
> 33MHz	Closed