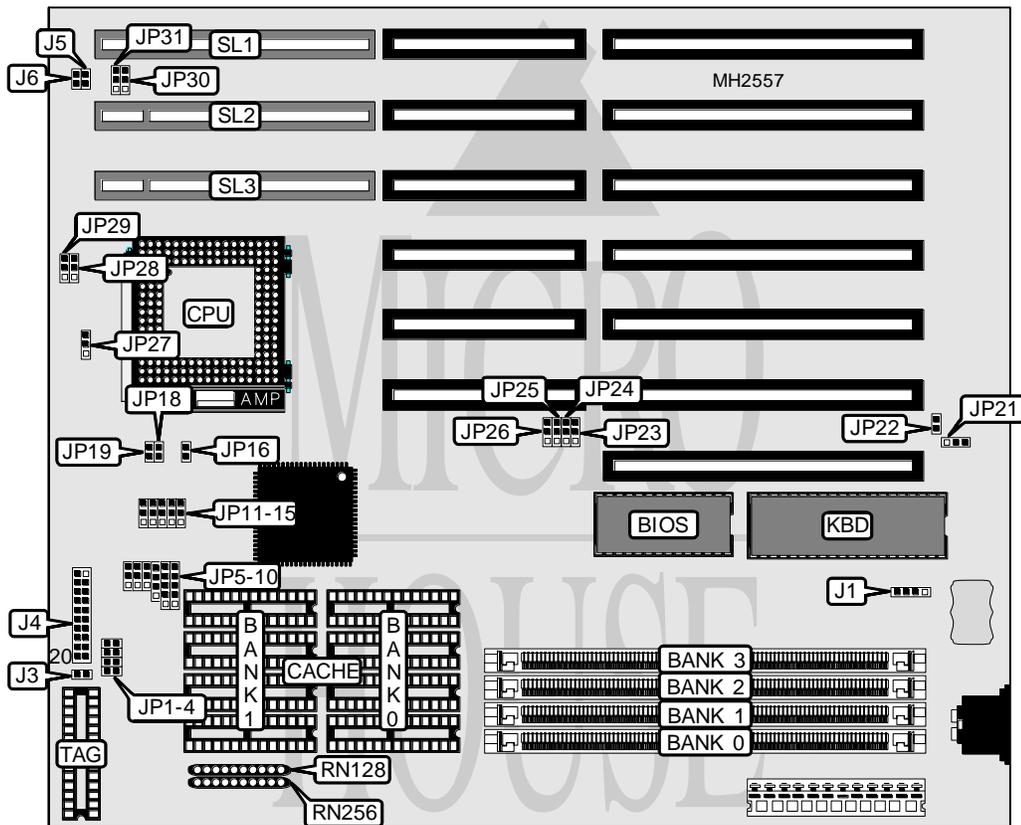


TMC RESEARCH CORPORATION

PAT48SG (VER. 1.1), PAT48SG4(VER. 1.1B)

Processor	CX486SX/SL80486SX/80486SX/CX486DX/SL80486DX/80486DX/SL80486DX280486DX2/80486DX4/Pentium Overdrive
Processor Speed	25/33/40/50(internal)/50/66(internal)/83(internal)/100(internal)MHz
Chip Set	Unidentified
Max. Onboard DRAM	128MB
Cache	128/256KB
BIOS	Unidentified
Dimensions	254mm x 218mm
I/O Options	32-bit VESA local bus slots (3), green PC connector
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	IDE interface LED	J4 pins 10 & 20
IDE interface LED	J3	Power LED & keylock	J4 pins 11 - 15
Speaker	J4 pins 1 - 4	Green PC connector	J5
Turbo switch	J4 pins 7 & 17	Green PC LED	J6
Turbo LED	J4 pins 8 & 18	32-bit VESA local bus slots	SL1 - SL3
Reset switch	J4 pins 9 & 19		

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TMC RESEARCH CORPORATION

PAT48SG (VER. 1.1), PAT48SG4(VER. 1.1B)

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Battery type select internal	JP21	pins 1 & 2 closed
CMOS memory clear	JP21	pins 2 & 3 closed
Battery type select external	JP21	Open
í Monitor type select color	JP22	Closed
Monitor type select monochrome	JP22	Open
í Factory configured - do not alter	JP23	N/A
í Factory configured - do not alter	JP26	N/A
í Factory configured - do not alter	JP27	N/A
í Factory configured - do not alter	JP28	N/A
í Factory configured - do not alter	JP29	N/A

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(1) 256K x 36	NONE	NONE	NONE
2MB	(1) 256K x 36	(1) 256K x 36	NONE	NONE
2MB	(1) 512K x 36	NONE	NONE	NONE
4MB	(1) 256K x 36	(1) 256K x 36	(1) 512K x 36	NONE
4MB	(1) 512K x 36	NONE	(1) 512K x 36	NONE
4MB	(1) 1M x 36	NONE	NONE	NONE
5MB	(1) 256K x 36	(1) 1M x 36	NONE	NONE
6MB	(1) 256K x 36	(1) 256K x 36	(1) 1M x 36	NONE
6MB	(1) 512K x 36	NONE	(1) 1M x 36	NONE
8MB	(1) 1M x 36	(1) 1M x 36	NONE	NONE
8MB	(1) 2M x 36	NONE	NONE	NONE
10MB	(1) 256K x 36	(1) 256K x 36	(1) 1M x 36	(1) 1M x 36
12MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	NONE
16MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
16MB	(1) 2M x 36	NONE	(1) 2M x 36	NONE
16MB	(1) 4M x 36	NONE	NONE	NONE
17MB	(1) 256K x 36	(1) 4M x 36	NONE	NONE
18MB	(1) 256K x 36	(1) 256K x 36	(1) 4M x 36	NONE
18MB	(1) 512K x 36	NONE	(1) 4M x 36	NONE
20MB	(1) 1M x 36	(1) 4M x 36	NONE	NONE
24MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	NONE
32MB	(1) 4M x 36	(1) 4M x 36	NONE	NONE
32MB	(1) 8M x 36	NONE	NONE	NONE
36MB	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36	NONE
40MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36
40MB	(1) 1M x 36	(1) 1M x 36	(1) 8M x 36	NONE
48MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	NONE
64MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
64MB	(1) 16M x 36	NONE	NONE	NONE
64MB	(1) 4M x 36	(1) 4M x 36	(1) 8M x 36	NONE

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TMC RESEARCH CORPORATION

PAT48SG (VER. 1.1), PAT48SG4(VER. 1.1B)

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DRAM CONFIGURATION (CON'T)				
Size	Bank 0	Bank 1	Bank 2	Bank 3
64MB	(1) 8M x 36	NONE	(1) 8M x 36	NONE
65MB	(1) 256K x 36	(1) 16M x 36	NONE	NONE
68MB	(1) 1M x 36	(1) 16M x 36	NONE	NONE
72MB	(1) 1M x 36	(1) 1M x 36	(1) 16M x 36	NONE
80MB	(1) 4M x 36	(1) 16M x 36	NONE	NONE
96MB	(1) 4M x 36	(1) 4M x 36	(1) 16M x 36	NONE
128MB	(1) 16M x 36	(1) 16M x 36	NONE	NONE

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 16K x 8 or (1) 32K x 8

CACHE RESISTOR CONFIGURATION		
Size	RN128	RN256
128KB	Installed	Not installed
256KB	Not installed	Installed

CPU TYPE CONFIGURATION					
Type	JP5	JP6	JP7	JP8	JP9
CX486SX	2 & 3, 4 & 5	2 & 3	1 & 2, 3 & 4	2 & 3	2 & 3
SL80486SX	3 & 4	4 & 5	2 & 3	2 & 3	2 & 3
80486SX	Open	Open	Open	2 & 3	2 & 3
CX486DX	2 & 3	2 & 3	1 & 2, 3 & 4	1 & 2	2 & 3
SL80486DX	3 & 4	4 & 5	2 & 3	1 & 2	2 & 3
80486DX	Open	Open	Open	1 & 2	2 & 3
SL80486DX2	3 & 4	4 & 5	2 & 3	1 & 2	2 & 3
80486DX2	1 & 2, 3 & 4	4 & 5	2 & 3	1 & 2	1 & 2
80486DX4	3 & 4	4 & 5	2 & 3	1 & 2	2 & 3
Pentium Overdrive	3 & 4	1 & 2	2 & 3	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

CPU TYPE CONFIGURATION (CON'T)					
Type	JP10	JP11	JP12	JP13	JP14
CX486SX	Closed	Open	2 & 3	2 & 3	Open
SL80486SX	Closed	1 & 2	Open	Open	Open
80486SX	Closed	Open	Open	Open	Open
CX486DX	Closed	Open	2 & 3	2 & 3	2 & 3
SL80486DX	Closed	1 & 2	Open	Open	2 & 3
80486DX	Closed	Open	Open	Open	2 & 3
SL80486DX2	Closed	1 & 2	Open	Open	2 & 3
80486DX2	Closed	2 & 3	Open	1 & 2	2 & 3
80486DX4	Open	1 & 2	Open	Open	2 & 3
Pentium Overdrive	Closed	Open	1 & 2	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

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TMC RESEARCH CORPORATION

PAT48SG (VER. 1.1), PAT48SG4(VER. 1.1B)

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CPU TYPE CONFIGURATION (CON'T)					
Type	JP16	JP18	JP19	JP24	JP25
CX486SX	Open	Open	Open	1 & 2	2 & 3
SL80486SX	Open	Open	Open	2 & 3	1 & 2
80486SX	Open	Open	Open	2 & 3	1 & 2
CX486DX	Open	Open	Closed	1 & 2	2 & 3
SL80486DX	Open	Open	Closed	2 & 3	1 & 2
80486DX	Open	Open	Closed	2 & 3	1 & 2
SL80486DX2	Open	Open	Closed	2 & 3	1 & 2
80486DX2	Closed	Closed	Closed	2 & 3	1 & 2
80486DX4	Open	Open	Closed	2 & 3	1 & 2
Pentium Overdrive	Open	Open	Closed	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

CPU SPEED CONFIGURATION						
Speed	JP1	JP2	JP3	JP4	JP30	JP31
25MHz	Open	Closed	Closed	Open	1 & 2	1 & 2
33MHz	Open	Open	Closed	Closed	1 & 2	1 & 2
40MHz	Open	Closed	Open	Open	2 & 3	2 & 3
50iMHz	Open	Closed	Closed	Open	1 & 2	1 & 2
50MHz	Closed	Closed	Closed	Open	2 & 3	2 & 3
66iMHz	Open	Open	Closed	Closed	1 & 2	1 & 2
83iMHz	Open	Open	Closed	Closed	1 & 2	1 & 2
100iMHz	Open	Open	Closed	Closed	1 & 2	1 & 2
100iMHz	Closed	Closed	Closed	Open	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

CPU CORE FREQUENCY CONFIGURATION	
CPU Type	JP15
Non 80486DX4	Open
80486DX4-33/83	pins 2 & 3 closed
80486DX4-33/100	Open
80486DX4-50/100	pins 1 & 2 closed