TMC RESEARCH CORPORATION
PCI58AL (VER. 1.0)

Processor	Pentium
Processor Speed	60/66MHz
Chip Set	ALI
Max. Onboard DRAM	128MB
Cache	256/512KB
BIOS	Unidentified
Dimensions	250mm x 220mm
I/O Options	32-bit PCI slots (4)
NPU Options	None
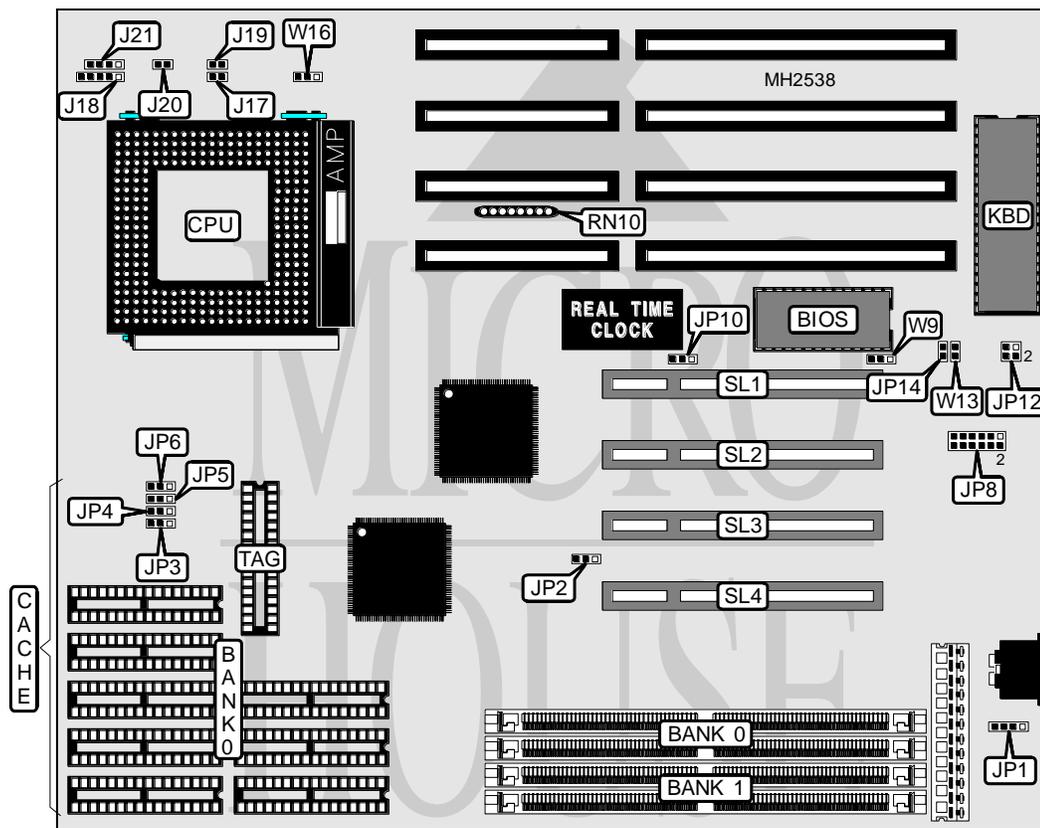
CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo switch	J17	Speaker	J21
Power LED & keylock	J18	External battery	JP1
Turbo LED	J19	32-bit PCI slots	SL1 - SL4
Reset switch	J20		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP8	N/A
í BIOS type select flash	JP10	pins 1 & 2 closed
BIOS type select EPROM	JP10	pins 2 & 3 closed
í Factory configured - do not alter	JP14	N/A
í Factory configured - do not alter	RN10	N/A
í Factory configured - do not alter	W9	N/A
í Factory configured - do not alter	W16	N/A

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
2MB	(2) 256K x 36	NONE
4MB	(2) 256K x 36	(2) 256K x 36
4MB	(2) 512K x 36	NONE
6MB	(2) 512K x 36	(2) 256K x 36
8MB	(2) 512K x 36	(2) 512K x 36
8MB	(2) 1M x 36	NONE
10MB	(2) 1M x 36	(2) 256K x 36
12MB	(2) 1M x 36	(2) 512K x 36
16MB	(2) 1M x 36	(2) 1M x 36
16MB	(2) 2M x 36	NONE
18MB	(2) 2M x 36	(2) 256K x 36
20MB	(2) 2M x 36	(2) 512K x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 2M x 36	(2) 2M x 36
32MB	(2) 4M x 36	NONE
32MB	(2) 4M x 36	(2) 256K x 36
36MB	(2) 4M x 36	(2) 512K x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 4M x 36	(2) 4M x 36
64MB	(2) 8M x 36	NONE
66MB	(2) 8M x 36	(2) 256K x 36
68MB	(2) 8M x 36	(2) 512K x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36

CACHE CONFIGURATION		
Size	Bank 0	TAG
256KB	(8) 32K x 8	(1) 8K x 8
512KB	(8) 64K x 8	(1) 16K x 8

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CACHE JUMPER CONFIGURATION				
Size	JP3	JP4	JP5	JP6
256KB	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
512KB	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed

CPU SPEED CONFIGURATION	
Speed	JP12
60MHz	pins 1 & 2 closed
66MHz	pins 2 & 3 closed

CMOS MODE SELECTION (ON-BOARD BATTERY)	
Mode	JP1
í Normal operation	pins 2 & 3 closed
Clear	pins 1 & 2 closed

CMOS MODE SELECTION (ON-CHIP BATTERY)	
Mode	W13
í Normal operation	Open
Clear	Closed

ADS DELAY CONFIGURATION	
Mode	JP2
í Disabled	pins 1 & 2 closed
Enabled	pins 2 & 3 closed