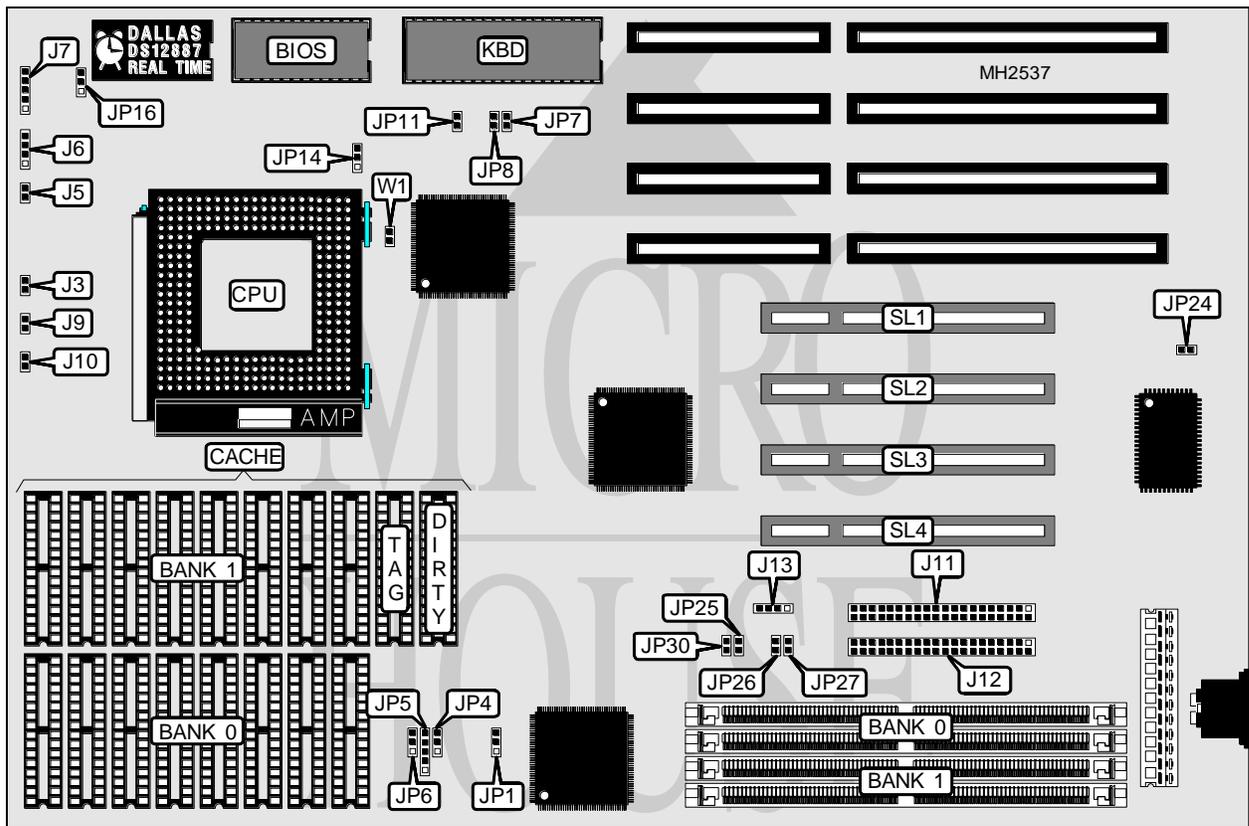


# TMC RESEARCH CORPORATION PCI58SP (VER. 1.0)

<b>Processor</b>	Pentium
<b>Processor Speed</b>	60/66MHz
<b>Chip Set</b>	OPTI
<b>Max. Onboard DRAM</b>	128MB
<b>Cache</b>	256/512KB
<b>BIOS</b>	Unidentified
<b>Dimensions</b>	330mm x 254mm
<b>I/O Options</b>	32-bit PCI slots (4), green PC connector
<b>NPU Options</b>	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo switch & LED	J3	Green PC connector	J9
Reset switch	J5	Green PC LED	J10
Speaker	J6	Hard drive LED	J13
Power LED & keylock	J7	32-bit PCI slots	SL1 - SL4

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# TMC RESEARCH CORPORATION

## PCI58SP (VER. 1.0)

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	JP11	Closed
Monitor type select monochrome	JP11	Open
í Pipeline mode disabled	JP14	pins 1 & 2 closed
Pipeline mode enabled	JP14	pins 2 & 3 closed
í CMOS memory normal operation	JP16	pins 1 & 2 closed
CMOS memory clear	JP16	pins 2 & 3 closed
í Secondary IDE interface enabled	JP24	Closed
Secondary IDE interface disabled	JP24	Open
í PCI IDE clock select 33MHz	JP27	Closed
PCI IDE clock select 25MHz	JP27	Open
í Factory configured - do not alter	W1	N/A

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
2MB	(2) 256K x 36	NONE
4MB	(2) 512K x 36	NONE
8MB	(2) 512K x 36	(2) 512K x 36
8MB	(2) 1M x 36	NONE
16MB	(2) 2M x 36	NONE
20MB	(2) 512K x 36	(2) 2M x 36
32MB	(2) 2M x 36	(2) 2M x 36
32MB	(2) 4M x 36	NONE
36MB	(2) 512K x 36	(2) 4M x 36
48MB	(2) 2M x 36	(2) 4M x 36
64MB	(2) 8M x 36	NONE
80MB	(2) 2M x 36	(2) 8M x 36
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	NONE

CPU SPEED CONFIGURATION		
Speed	JP7	JP8
60MHz	Closed	Open
66MHz	Closed	Closed

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG	DIRTY
256KB	(8) 32K x 8	NONE	(1) 16K x 8	(1) 8K x 8
512KB <sup>1</sup>	(8) 32K x 8	(8) 32K x 8	(1) 32K x 8	(1) 32K x 8
512KB <sup>2</sup>	(8) 64K x 8	NONE	(1) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION				
Size	JP1	JP4	JP5	JP6
256KB	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2, 3 & 4 closed	pins 2 & 3 closed
512KB <sup>1</sup>	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3, 4 & 5 closed	pins 2 & 3 closed
512KB <sup>2</sup>	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2, 3 & 4 closed	pins 1 & 2 closed

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IDE CYCLE TIME CONFIGURATION			
Mode	Time	JP25	JP26
í 0	≥600ns	Closed	Closed
1	≥383ns	Open	Closed
2	≥240ns	Closed	Open
3	≥180ns	Open	Open