OLIVETTI
M 4 - 6 0 / 6 2 / 6 4 / 6 5 / 6 6

Processor	80486SX/80487SX/80486DX/80486DX2
Processor Speed	25/33/50(internal)/50/66(internal)MHz
Chip Set	Unidentified
Max. Onboard DRAM	36MB
Cache	None
BIOS	Unidentified
Dimensions	330mm x 218mm
I/O Options	Floppy drive interface, IDE interface, parallel port, PS/2 mouse port, serial ports (2), VGA port
NPU Options	None
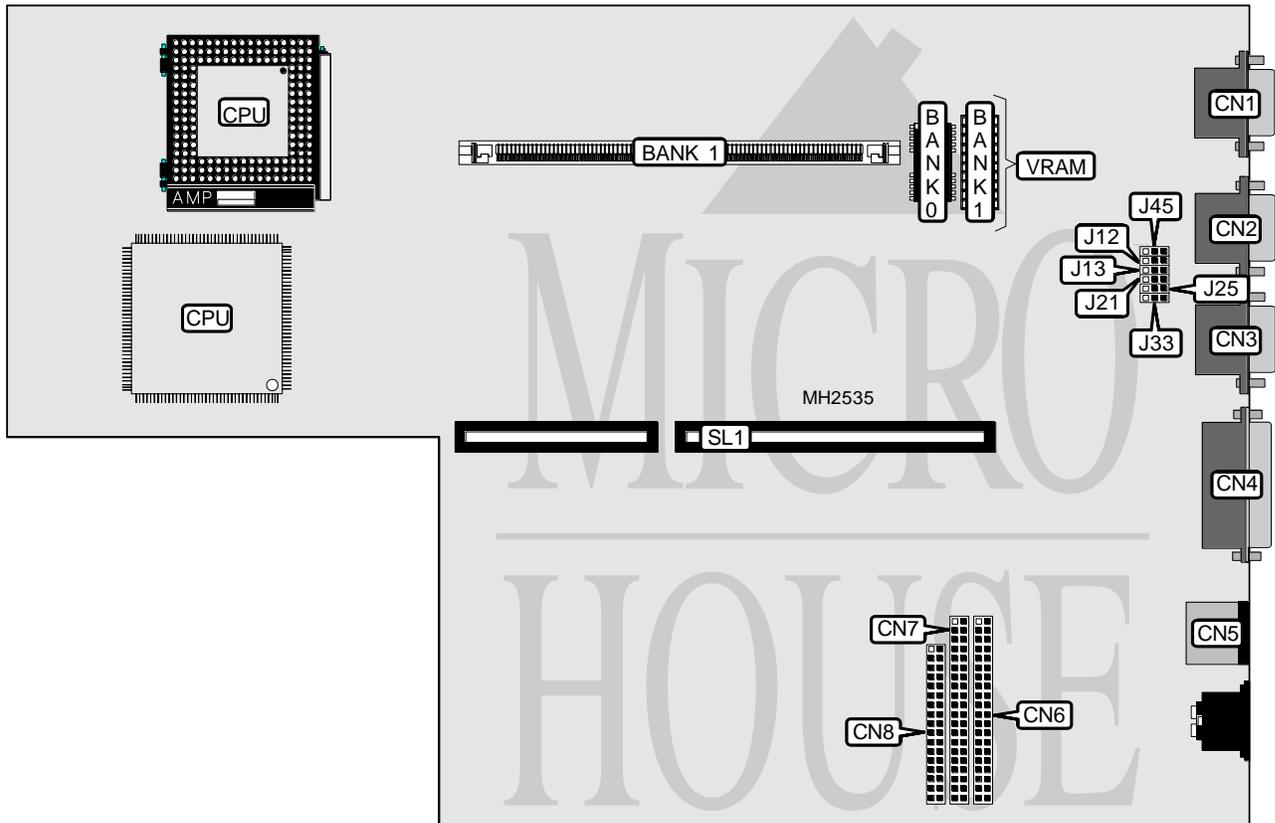
CONNECTIONS			
Purpose	Location	Purpose	Location
VGA port	CN1	CD ROM interface	CN6
Serial port 2	CN2	IDE interface	CN7
Serial port 1	CN3	Floppy drive interface	CN8
Parallel port	CN4	Riser Card	SL1
PS/2 mouse port	CN5		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	J12	pins 2 & 3 closed
CMOS memory clear	J12	pins 1 & 2 closed
í IRQ12 enabled for PS/2 mouse	J13	pins 2 & 3 closed
IRQ12 disabled	J13	pins 1 & 2 closed
í Serial port 1 bootstrapping enabled	J21	pins 2 & 3 closed
Serial port 1 bootstrapping disabled	J21	pins 1 & 2 closed
í Serial port 2 bootstrapping enabled	J25	pins 2 & 3 closed
Serial port 2 bootstrapping disabled	J25	pins 1 & 2 closed
í Floppy drive write enabled	J33	pins 2 & 3 closed
Floppy drive write disabled	J33	pins 1 & 2 closed
í On board VGA enabled	J45	pins 2 & 3 closed
On board VGA disabled	J45	pins 1 & 2 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
4MB	4MB	NONE
8MB	4MB	(1) 1M x 36
12MB	4MB	(1) 2M x 36
20MB	4MB	(1) 4M x 36
36MB	4MB	(1) 8M x 36
Note: Bank 0 is factory installed and is not configurable. The location is unknown.		

VRAM CONFIGURATION		
Size	Bank 0	Bank 1
512KB	(1) 411000	NONE
1MB	(1) 411000	(1) 411000