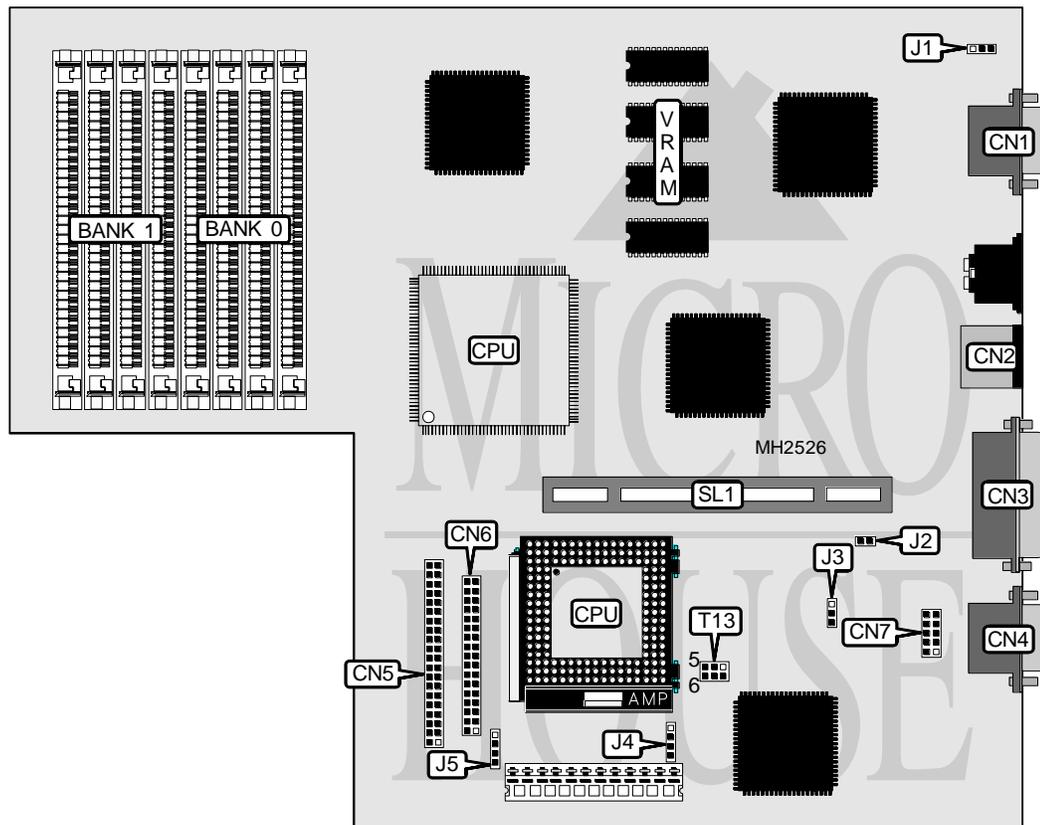


# ICL CL486, CL486S/25

<b>Processor</b>	80486SX/80487SX/80486DX/80486DX2
<b>Processor Speed</b>	25/33/50(internal)/66(internal)MHz
<b>Chip Set</b>	Symphony
<b>Max. Onboard DRAM</b>	32MB
<b>Cache</b>	None
<b>BIOS</b>	ICL
<b>Dimensions</b>	430mm x 352mm
<b>I/O Options</b>	Floppy drive interface, IDE interface, parallel port, PS/2 mouse port, serial ports (2), VGA port
<b>NPU Options</b>	None



CONNECTIONS			
Purpose	Location	Purpose	Location
VGA port	CN1	Floppy drive interface	CN6
PS/2 mouse port	CN2	Serial port 2	CN7
Parallel port	CN3	Chassis fan power	J4
Serial port 1	CN4	IDE interface LED	J5
IDE interface	CN5	Riser Card	SL1

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# ICL

## CL486, CL486S/25

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í On board VGA enabled	J1	pins 2 & 3 closed
On board VGA disabled	J1	pins 1 & 2 closed
í CMOS memory normal operation	J2	Open
CMOS memory clear	J2	Closed
í Factory configured - do not alter	J3	N/A

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CPU TYPE CONFIGURATION	
Type	T13
80486SX	pins 2 & 3, 5 & 6 closed
80487SX	pins 2 & 3, 5 & 6 closed
80486DX	pins 1 & 2, 4 & 5 closed
80486DX2	pins 1 & 2, 4 & 5 closed