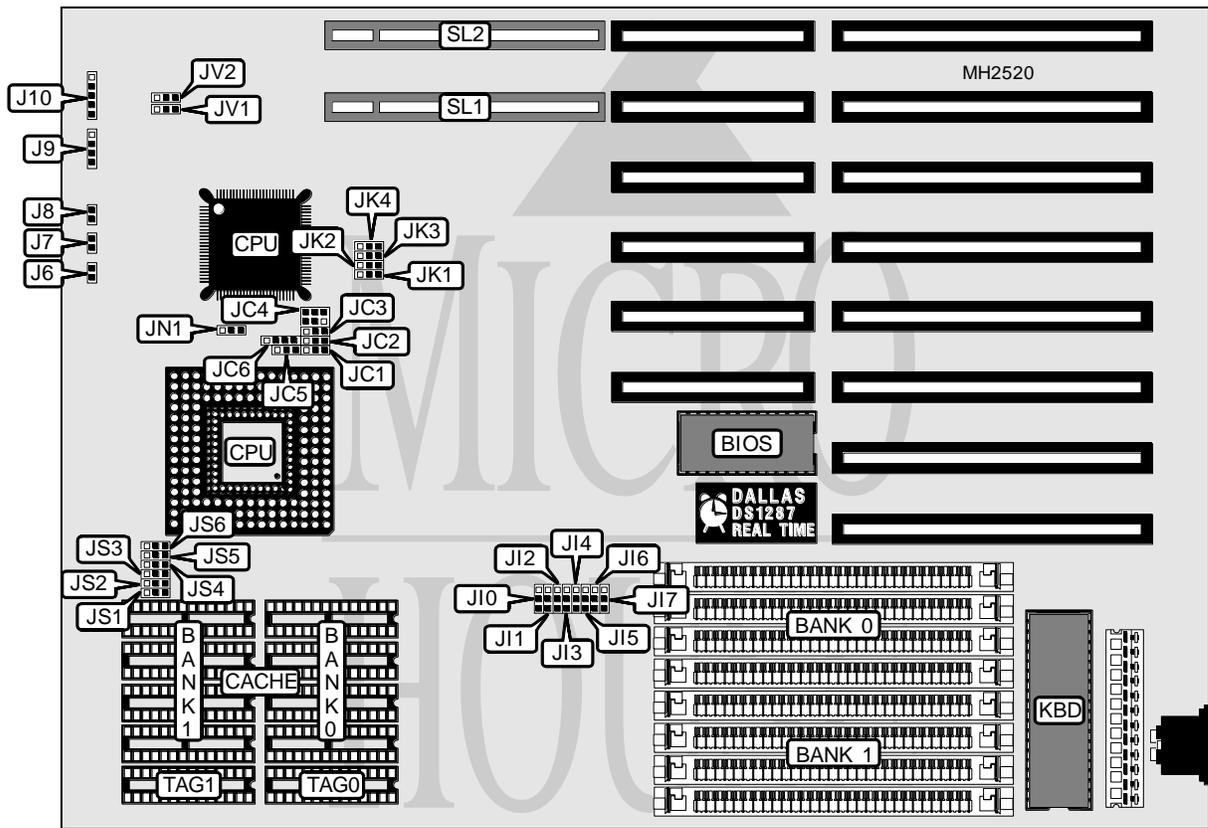


# UNIDENTIFIED 4386-V C-V

<b>Processor</b>	80386DX/CX486DLC/80486SX/80487SX/80486DX/80486DX2
<b>Processor Speed</b>	20/25/33/40/50(internal)/50/66(internal)MHz
<b>Chip Set</b>	VIA
<b>Max. Onboard DRAM</b>	64/128MB (depends on CPU installed)
<b>Cache</b>	32/64/128/256KB
<b>BIOS</b>	Award
<b>Dimensions</b>	330mm x 218mm
<b>I/O Options</b>	32-bit VESA local bus slots (2)
<b>NPU Options</b>	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	J6	Speaker	J9
Turbo switch	J7	Power LED & keylock	J10
Reset switch	J8	32-bit VESA local bus slots	SL1 & SL2

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select monochrome/EGA/VGA	J1	Open
Monitor type select color	J1	Closed
í CMOS memory normal operation	J2	Open
CMOS memory clear	J2	Closed
í Factory configured - do not alter	JC5	N/A
í Factory configured - do not alter	JC6	N/A
í Factory configured - do not alter	JK1	N/A
í NPU synchronous with CPU	JN1	pins 1 & 2 closed
NPU asynchronous with CPU	JN1	pins 2 & 3 closed

Note: The location of J1 & J2 are unidentified.

DRAM CONFIGURATION (80386)		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 256K x 9	(4) 4M x 9
17MB	(4) 4M x 9	(4) 256K x 9
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9
64MB	(4) 16M x 9	NONE

DRAM CONFIGURATION (80486)		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 256K x 9	(4) 4M x 9
17MB	(4) 4M x 9	(4) 256K x 9
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9
64MB	(4) 16M x 9	NONE
65MB	(4) 256K x 9	(4) 16M x 9
65MB	(4) 16M x 9	(4) 256K x 9

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DRAM CONFIGURATION (80486 CON'T)		
Size	Bank 0	Bank 1
68MB	(4) 1M x 9	(4) 16M x 9
68MB	(4) 16M x 9	(4) 1M x 9
80MB	(4) 4M x 9	(4) 16M x 9
80MB	(4) 16M x 9	(4) 4M x 9
128MB	(4) 16M x 9	(4) 16M x 9

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG 0	TAG 1
32KB	(4) 8K x 8	NONE	(1) 8K x 8	(1) 8K x 8
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8	(1) 32K x 8

Note: TAG 1 is alter RAM and may not be present.

CACHE JUMPER CONFIGURATION						
Size	JS1	JS2	JS3	JS4	JS5	JS6
32KB	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3	2 & 3
64KB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
128KB	1 & 2	2 & 3	2 & 3	2 & 3	1 & 2	1 & 2
256KB	2 & 3	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

CPU TYPE CONFIGURATION						
Type	J10	J11	J12	J13	J14	J15
80386DX	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
80486SX	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
80487SX	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
CX486DLC	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
80486DX	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
80486DX2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2

Note: Pins desingated should be in the closed position.

CPU TYPE CONFIGURATION (CON'T)						
Type	J16	J17	JC1	JC2	JC3	JC4
80386DX	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2	Open
80486SX	1 & 2	1 & 2	2 & 3	2 & 3	Open	Open
80487SX	1 & 2	1 & 2	1 & 2	1 & 2	2 & 3	Open
CX486DLC	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2	1 & 2, 3 & 4, 5 & 6
80486DX	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	Open
80486DX2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	Open

Note: Pins desingated should be in the closed position.

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CPU SPEED CONFIGURATION			
Speed	JK2	JK3	JK4
20MHz	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
25MHz	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
33MHz (80386 & CX486)	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed
33MHz	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed
40MHz (80386 & CX486)	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed
50iMHz	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
50MHz	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed
66iMHz	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed

VESA WAIT STATE CONFIGURATION	
Wait states	JV2
0 wait states	pins 1 & 2 closed
1 wait state	pins 2 & 3 closed

BUS SPEED CONFIGURATION	
CPU speed	JV1
<= 33MHz	pins 1 & 2 closed
> 33MHz	pins 2 & 3 closed