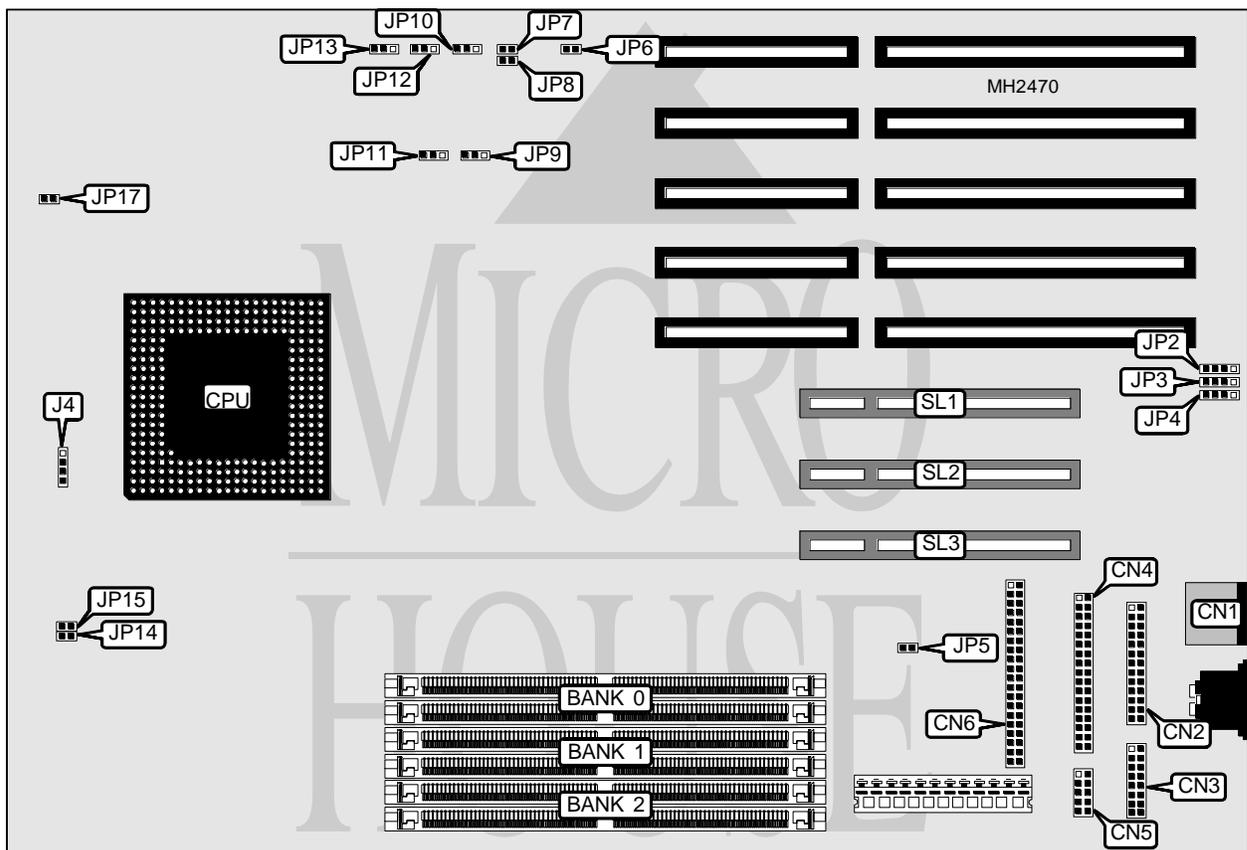


ACER, INC.

ACERPOWER 560P/PT/TU (V12P PROJECT)

Processor	Pentium
Processor Speed	60MHz
Chip Set	Unidentified
Max. Onboard DRAM	192MB
Cache	256/512KB(location unidentified)
BIOS	Unidentified
Dimensions	330mm x 218mm
I/O Options	Floppy drive interface, IDE interface, parallel port, PS/2 mouse port, serial ports (2), 32-bit PCI slots (3)
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
PS/2 mouse port	CN1	Serial port 1	CN5
Parallel port	CN2	IDE interface	CN6
Serial port 2	CN3	32-bit PCI slots	SL1 - SL3
Floppy drive interface	CN4		

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ACERPOWER 560P/PT/TU (V12P PROJECT)

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	J4	N/A
í On board M5105 enabled	JP5	Closed
On board M5105 disabled	JP5	Open
í Parallel port bidirectional	JP6	Closed
Parallel port unidirectional (printer)	JP6	Open
í Factory configured - do not alter	JP9	N/A
í Password disabled	JP11	pins 2 & 3 closed
Password enabled	JP11	pins 1 & 2 closed
í Cache type select standard	JP13	pins 1 & 2 closed
Cache type select burst	JP13	pins 2 & 3 closed
í Keylock disabled	JP17	Closed
Keylock enabled	JP17	Open

DRAM CONFIGURATION			
Size	Bank 0	Bank 1	Bank 2
8MB	(2) 1M x 36	NONE	NONE
16MB	(2) 1M x 36	(2) 1M x 36	NONE
16MB	(2) 2M x 36	NONE	NONE
24MB	(2) 1M x 36	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36	NONE
32MB	(2) 2M x 36	(2) 1M x 36	(2) 1M x 36
32MB	(2) 2M x 36	(2) 2M x 36	NONE
32MB	(2) 4M x 36	NONE	NONE
40MB	(2) 2M x 36	(2) 2M x 36	(2) 1M x 36
40MB	(2) 4M x 36	(2) 1M x 36	NONE
48MB	(2) 2M x 36	(2) 2M x 36	(2) 2M x 36
48MB	(2) 4M x 36	(2) 1M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36	NONE
56MB	(2) 4M x 36	(2) 2M x 36	(2) 1M x 36
64MB	(2) 4M x 36	(2) 2M x 36	(2) 2M x 36
64MB	(2) 4M x 36	(2) 4M x 36	NONE
64MB	(2) 8M x 36	NONE	NONE
72MB	(2) 4M x 36	(2) 4M x 36	(2) 1M x 36
72MB	(2) 8M x 36	(2) 1M x 36	NONE
80MB	(2) 4M x 36	(2) 4M x 36	(2) 2M x 36
80MB	(2) 8M x 36	(2) 1M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36	NONE
88MB	(2) 8M x 36	(2) 2M x 36	(2) 1M x 36
96MB	(2) 4M x 36	(2) 4M x 36	(2) 4M x 36
96MB	(2) 8M x 36	(2) 2M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36	NONE
104MB	(2) 8M x 36	(2) 4M x 36	(2) 1M x 36
112MB	(2) 8M x 36	(2) 4M x 36	(2) 2M x 36

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ACERPOWER 560P/PT/TU (V12P PROJECT)

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DRAM CONFIGURATION (CON'T)			
Size	Bank 0	Bank 1	Bank 2
128MB	(2) 8M x 36	(2) 4M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36	NONE
136MB	(2) 8M x 36	(2) 8M x 36	(2) 1M x 36
144MB	(2) 8M x 36	(2) 8M x 36	(2) 2M x 36
160MB	(2) 8M x 36	(2) 8M x 36	(2) 4M x 36
192MB	(2) 8M x 36	(2) 8M x 36	(2) 8M x 36

CACHE CONFIGURATION
Note: The cache locations and configurations are unidentified.

CACHE JUMPER CONFIGURATION		
Size	JP10	JP12
None installed	pins 1 & 2 closed	pins 1 & 2 closed
256KB	pins 1 & 2 closed	pins 2 & 3 closed
512KB	pins 2 & 3 closed	pins 2 & 3 closed

L2 PARITY CONFIGURATION		
Setting	JP7	JP8
Not supported	Closed	Closed
Supported	Open	Open

ON BOARD CACHE CONFIGURATION		
Setting	JP14	JP15
256KB OE# signal enabled	Closed	Closed
256KB OE# signal disabled	Open	Open

PCI INTERRUPT B CONFIGURATION	
IRQ	JP2
Disabled	pins 2 & 3 closed
IRQ14	pins 3 & 4 closed
IRQ15	pins 1 & 2 closed

PCI INTERRUPT C CONFIGURATION	
IRQ	JP3
Disabled	pins 2 & 3 closed
IRQ11	pins 1 & 2 closed
IRQ15	pins 3 & 4 closed

PCI INTERRUPT D CONFIGURATION	
IRQ	JP4
Disabled	pins 2 & 3 closed
IRQ10	pins 1 & 2 closed
IRQ15	pins 3 & 4 closed