SEANIX TECHNOLOGY, INC.
S B - S Y M - 4 8 6 C

Processor	80486SX/80486DX
Processor Speed	16/20/25/33/50MHz
Chip Set	Symphony
Max. Onboard DRAM	32MB
Cache	64/128/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	4167
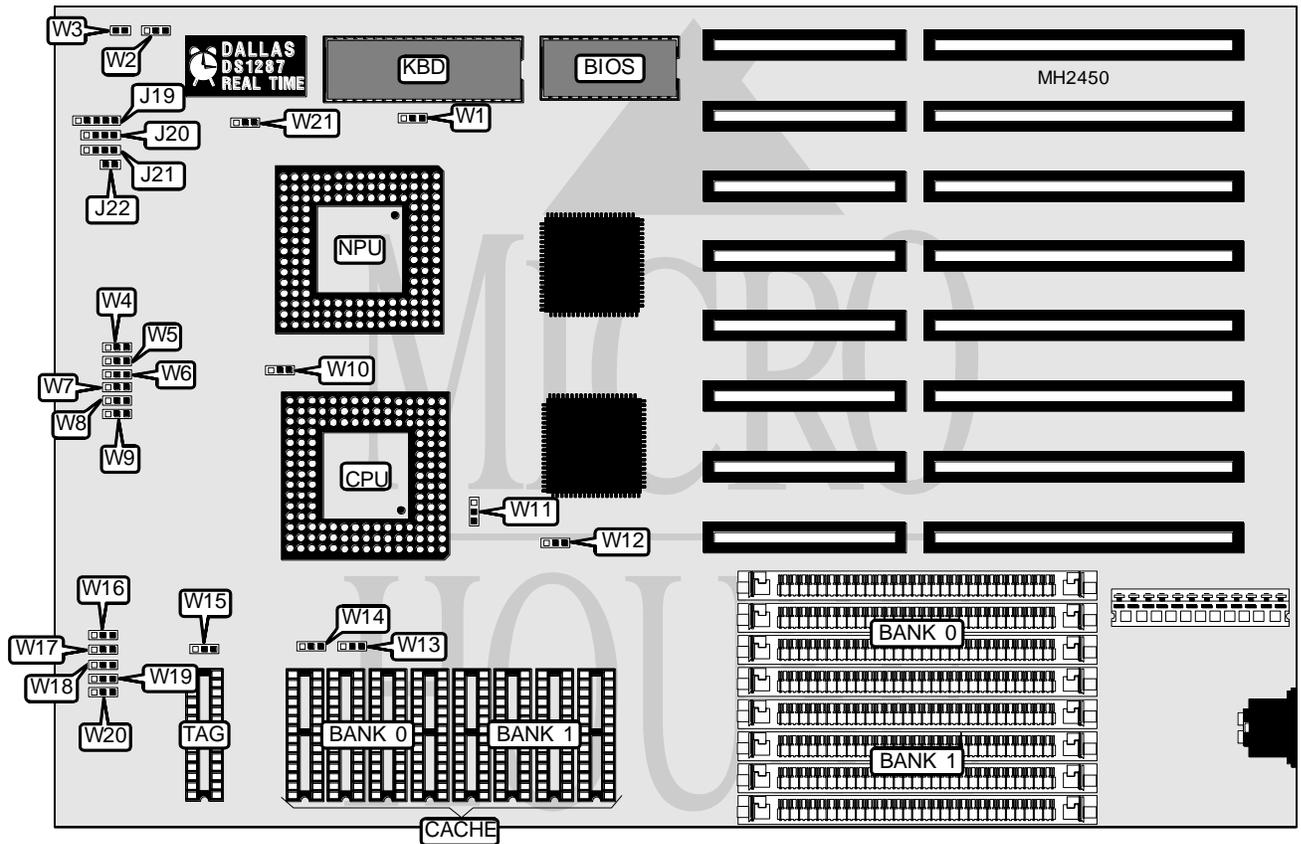
CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	J19	Turbo switch	J21 (pins 3 & 4)
Speaker	J20	Reset switch	J22
Turbo LED	J21 (pins 1 & 2)		

Continued on next page. . .

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	W2	pins 1 & 2 closed
Monitor type select monochrome	W2	pins 2 & 3 closed
í Factory configured - do not alter	W3	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
3MB	(4) 1M x 9	NONE
4MB	(4) 1M x 9	(4) 256K x 9
4MB	(4) 1M x 9	(4) 1M x 9
5MB	(4) 4M x 9	NONE
6MB	(4) 4M x 9	(4) 256K x 9
7MB	(4) 4M x 9	(4) 1M x 9
8MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION					
Size	W4	W5	W6	W7	W8
64KB	2 & 3	Open	1 & 2	1 & 2	2 & 3
128KB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
256KB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
Note: Pins designated should be in the closed position.					

CACHE JUMPER CONFIGURATION (CON'T)					
Size	W9	W15	W17	W19	W20
64KB	1 & 2	2 & 3	2 & 3	2 & 3	2 & 3
128KB	2 & 3	1 & 2	1 & 2	1 & 2	1 & 2
256KB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
Note: Pins designated should be in the closed position.					

CACHE JUMPER CONFIGURATION (CON'T)	
Setting	W21
Cache installed (shows size at boot up)	pins 1 & 2 closed
Cache installed (does not show size at boot up)	pins 1 & 2 closed
Cache not installed	pins 2 & 3 closed

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CPU TYPE CONFIGURATION			
Type	W13	W14	W18
80486SX	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
80486DX	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed

CPU SPEED CONFIGURATION					
Speed	W1	W10	W11	W12	W16
16MHz	1 & 2	1 & 2	2 & 3	2 & 3	2 & 3
20MHz	1 & 2	1 & 2	2 & 3	2 & 3	2 & 3
25MHz	1 & 2	1 & 2	2 & 3	2 & 3	2 & 3
33MHz	1 & 2	1 & 2	2 & 3	2 & 3	2 & 3
50MHz	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2
Note: Pins designated should be in the closed position.					