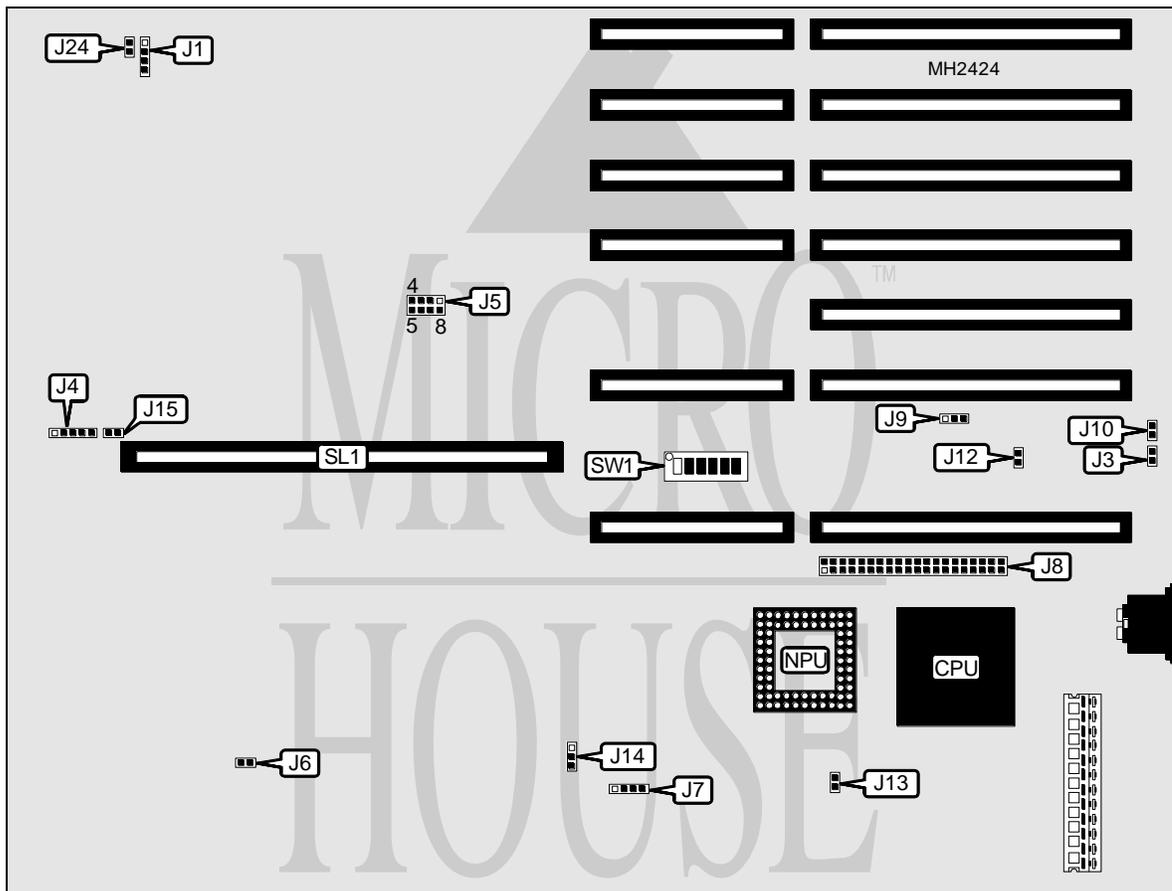


COMPUADD, INC. 386 SERIES MODELS 320/325

Processor	80386DX
Processor Speed	20/25MHz
Chip Set	Unidentified
Max. Onboard DRAM	16MB (on external memory card)
Cache	32KB (exact location unidentified)
BIOS	Unidentified
Dimensions	330mm x 218mm
I/O Options	32-bit external memory card, IDE interface, serial ports (2), parallel port
NPU Options	80387DX



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J1	IDE interface LED	J15
Power LED & keylock	J4	Reset switch	J24
External battery	J7	32-bit external memory card	SL1
IDE interface	J8		

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Monitor type select color	J3	Closed
Monitor type select monochrome	J3	Open
í Cache enabled	J6	Closed
Cache disabled	J6	Open
í IDE active high reset	J9	pins 1 & 2 closed
IDE active low reset	J9	pins 2 & 3 closed
í IDE interface enabled	J10	Closed
IDE interface disabled	J10	Open
í Cache enabled	J14	pins 1 & 2 closed
Cache disabled	J14	pins 2 & 3 closed
í Serial port 1 enabled (IRQ4)	SW1/1	On
Serial port 1 disabled (IRQ4)	SW1/1	Off
í Serial port 2 enabled (IRQ3)	SW1/2	On
Serial port 2 disabled (IRQ3)	SW1/2	Off
í Parallel port enabled	SW1/3	On
Parallel port disabled	SW1/3	Off
í Parallel port IRQ select IRQ7	SW1/4	On
Parallel port IRQ select IRQ5	SW1/4	Off
í Factory configured - do not alter	SW1/5	Off
í Factory configured - do not alter	SW1/6	Off

Note : On some revisions, switch SW1 may be a 4-position switch.

CACHE CONFIGURATION
Note: The location and configuration of the cache is unidentified.

NPU CONFIGURATION		
Setting	J12	J13
NPU not installed	Closed	Open
NPU installed	Open	Closed

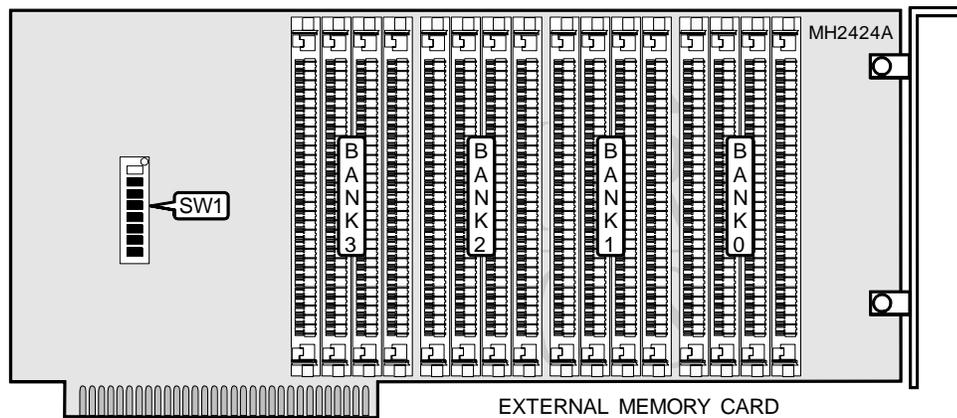
BIOS CONFIGURATION	
Size	J5
27128	pins 1 & 8, 2 & 7 closed
27256	pins 3 & 6, 4 & 5 closed

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USER CONFIGURABLE SETTINGS

Function	Switch	Position
í Memory between 512KB & 640KB enabled	SW1/5	On
Memory between 512KB & 640KB disabled	SW1/5	Off
í Memory between 256KB & 512KB enabled	SW1/6	On
Memory between 256KB & 512KB disabled	SW1/6	Off
í Factory configured - do not alter	SW1/7	Off
í Factory configured - do not alter	SW1/8	Off

DRAM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(4) 256K x 9	NONE	NONE	NONE
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
3MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	NONE
4MB	(4) 256K x 9			
4MB	(4) 1M x 9	NONE	NONE	NONE
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE
16MB	(4) 1M x 9			

DRAM SWITCH CONFIGURATION

Size	SW1/1	SW1/2	SW1/3	SW1/4
1MB	Off	Off	Off	Off
2MB	On	Off	Off	Off
3MB	On	On	Off	Off
4MB	On	On	On	Off
4MB	Off	Off	Off	On
8MB	On	Off	Off	On
12MB	On	On	Off	On
16MB	On	On	On	On