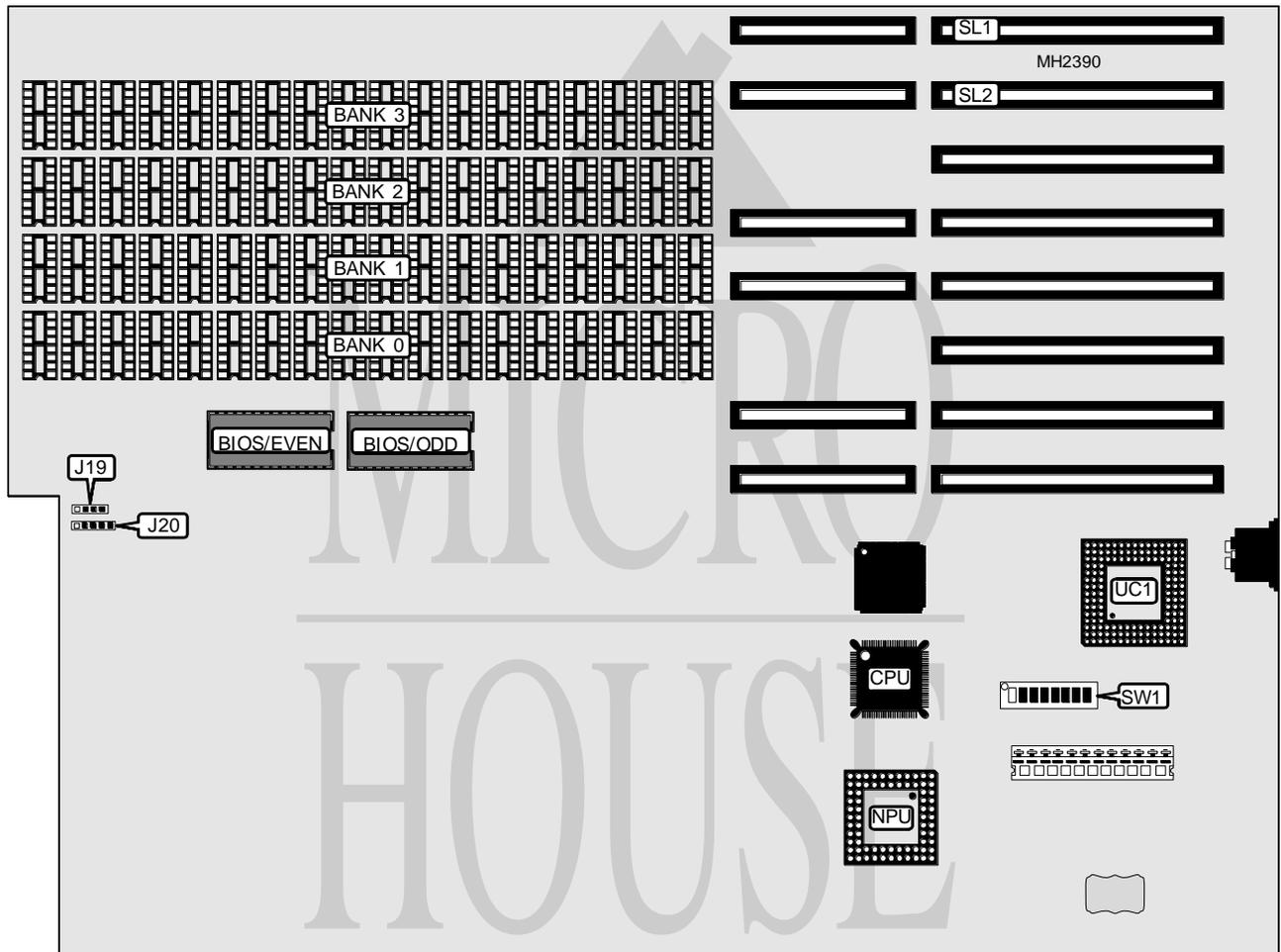


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FLEXCACHE 16386/20386

Processor	80386DX
Processor Speed	16/20MHz
Chip Set	ALR
Max. Onboard DRAM	6MB (4MB on external memory card)
Cache	None
BIOS	Phoenix
Dimensions	330mm x 218mm
I/O Options	32-bit external memory card
NPU Options	80387DX



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J19	32-bit memory slots	SL1 & SL2
Power LED & keylock	J20	Socket for factory use	UC1

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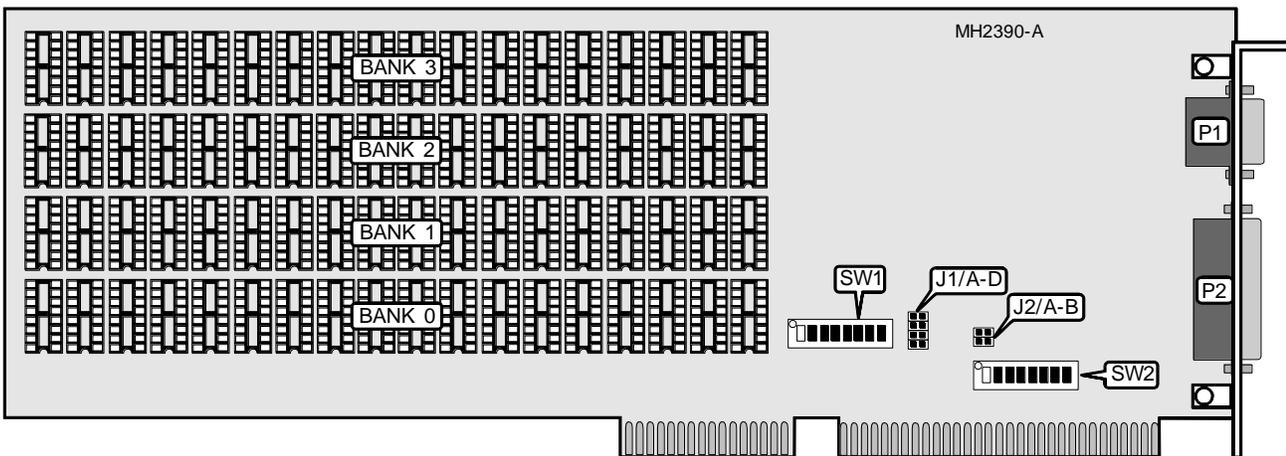
FLEXCACHE 16386/20386

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USER CONFIGURABLE SETTINGS		
Function	Switch	Position
í Monitor type select color	SW1/1	On
Monitor type select monochrome	SW1/1	Off
í Factory configured - do not alter	SW1/2	Off
í Factory configured - do not alter	SW1/3	Off
í On board memory size select 1MB	SW1/4	Off
On board memory size select 2MB	SW1/4	On
í Cache (ROM to RAM) enabled	SW1/5	On
Cache (ROM to RAM) disabled	SW1/5	Off
í Speed select high	SW1/8	Off
Speed select low	SW1/8	On

NUMERIC COPROCESSOR CONFIGURATION		
Type	SW1/6	SW1/7
Not installed	Off	On
80387	Off	Off

DRAM CONFIGURATION (ON BOARD)				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(18) 41256	(18) 41256	NONE	NONE
2MB	(18) 41256	(18) 41256	(18) 41256	(18) 41256



CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port	P1	Parallel port	P2

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FLEXCACHE 16386/20386

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Factory configured - do not alter	J1/A-D	N/A
í Factory configured - do not alter	J2/A-B	N/A
í Factory configured - do not alter	SW1/1 & 2	N/A
í Factory configured - do not alter	SW2/5 & 6	N/A

DRAM CONFIGURATION (32-BIT CARD)				
Size	Bank 0	Bank 1	Bank 2	Bank 3
512KB	(18) 41256	NONE	NONE	NONE
1MB	(18) 41256	(18) 41256	NONE	NONE
1.5KB	(18) 41256	(18) 41256	(18) 41256	NONE
2MB	(18) 41256	(18) 41256	(18) 41256	(18) 41256

Note: Two 32-bit memory cards must be used simultaneously allowing for a total of 6MB of 32-bit memory. Cards must be placed in SL1 & SL2 & each board must have the same configuration.

DRAM SWITCH CONFIGURATION (32-BIT CARD)						
Total Memory	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7	SW1/8
3MB	On	On	On	On	On	Off
4MB	On	Off	On	On	On	Off
5MB	Off	On	On	On	On	Off
6MB	Off	Off	On	On	On	Off

SERIAL PORT CONFIGURATION (32-BIT CARD)				
Port Address	SW2/1	SW2/2	SW2/7	SW2/8
COM1	On	Off	Off	On
COM2	Off	On	On	Off
Disabled	Off	Off	Off	Off

PARALLEL PORT CONFIGURATION (32-BIT CARD)		
Port Address	SW2/3	SW2/4
LPT1	On	Off
LPT2	Off	On
Disabled	Off	Off