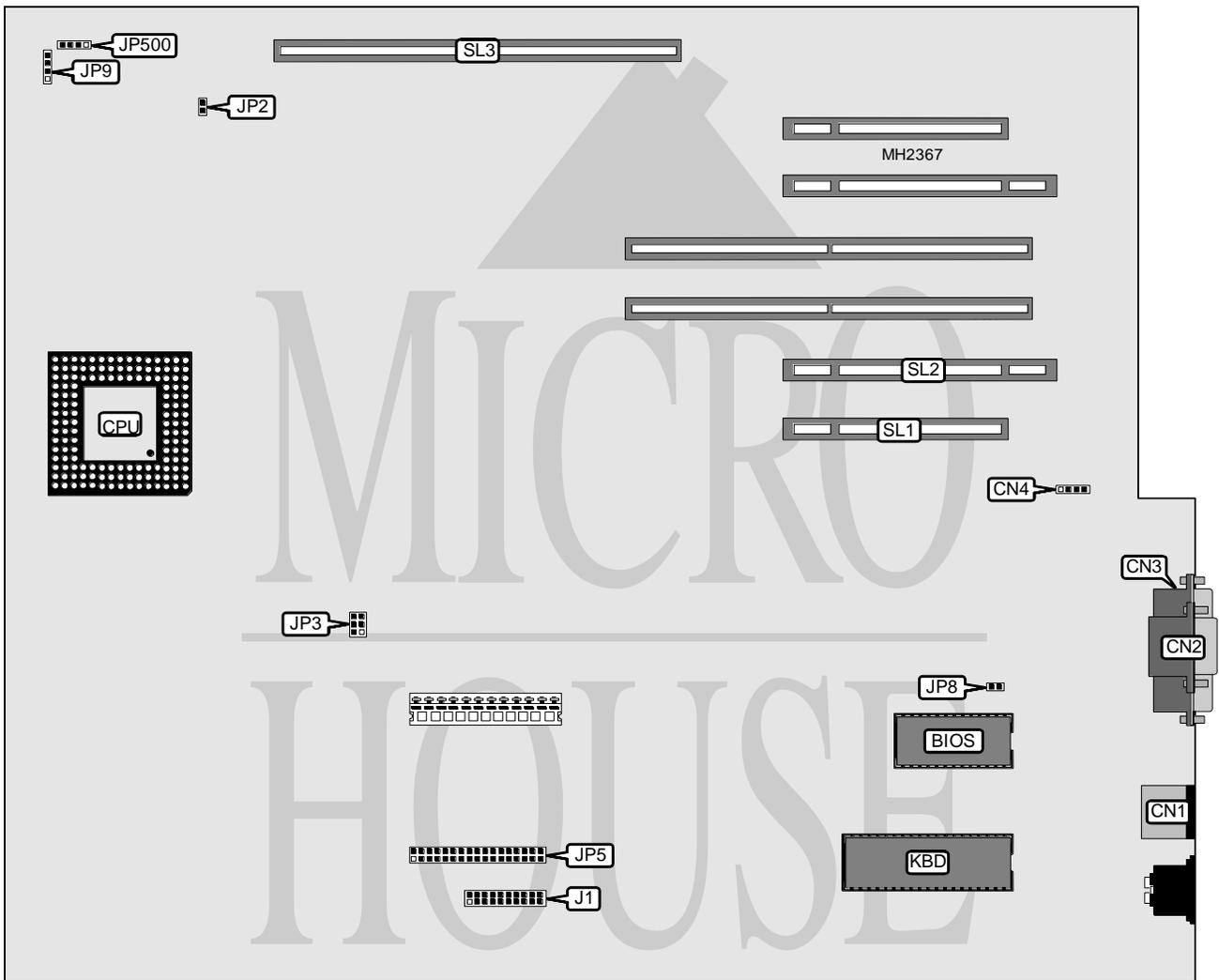


# ADVANCED LOGIC RESEARCH, INC. POWERCACHE 4 REV. A

<b>Processor</b>	80486DX
<b>Processor Speed</b>	25MHz
<b>Chip Set</b>	Unidentified
<b>Max. Onboard DRAM</b>	32MB (on external memory card)
<b>Cache</b>	128KB
<b>BIOS</b>	Phoenix
<b>Dimensions</b>	355mm x 304mm
<b>I/O Options</b>	32-bit external memory card, floppy drive interface, parallel port, PS/2 mouse port, serial port, hard drive controller slot, VGA slot
<b>NPU Options</b>	None



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**POWERCACHE 4 REV. A**

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CONNECTIONS			
Purpose	Location	Purpose	Location
PS/2 mouse port	CN1	Speaker	JP9
Serial port	CN2	Front panel connector	JP500
Parallel port	CN3	Hard drive controller slot	SL1
Chassis fan power	CN4	VGA slot	SL2
Reset switch	JP2	32-bit external memory card	SL3
Floppy drive interface	JP5		

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	J1	Open
í Factory configured - do not alter	JP3	Open
í Factory configured - do not alter	JP8	Open

DRAM CONFIGURATION (MEMORY CARD)		
Size	Bank 0	Bank 1
2MB	(8) 256K x 9	NONE
4MB	(8) 256K x 9	(8) 256K x 9
8MB	(8) 1M x 9	NONE
10MB	(8) 256K x 9	(8) 1M x 9
10MB	(8) 1M x 9	(8) 256K x 9
16MB	(8) 1M x 9	(8) 1M x 9
32MB	(8) 4M x 9	NONE

CACHE CONFIGURATION	
Size	Bank 0
128KB	(4) 32K x 8
Note: The chip configuration and location of cache is unidentified.	