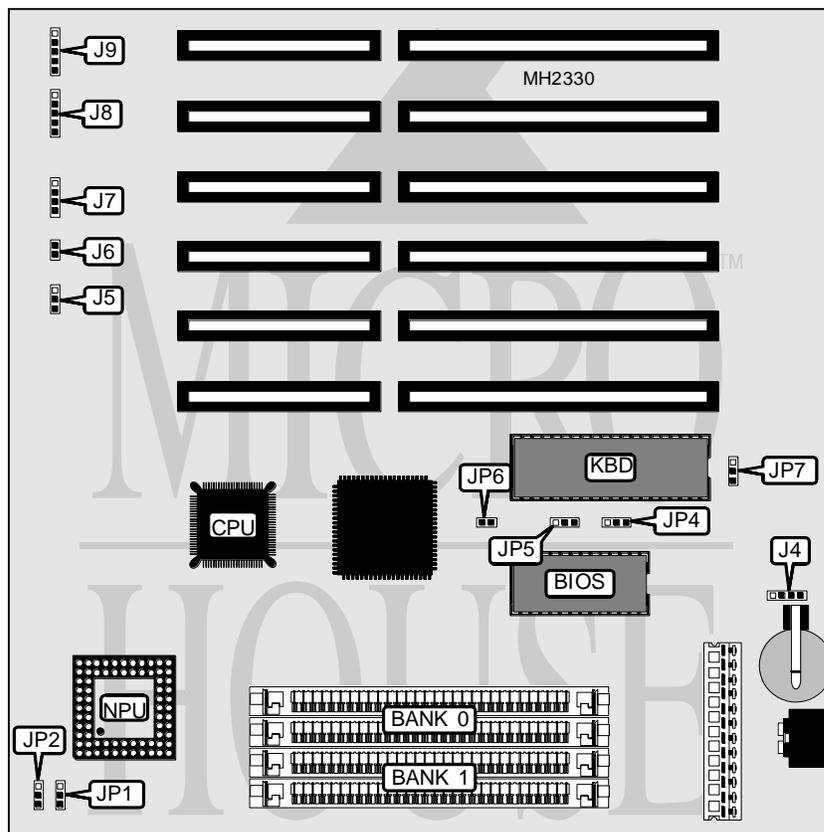


AUVA

NPM16-A0/ NPM25-A0/ NPM33-A0/
 NPM25-A1/ NPM33-A1

Processor	80386SX/CX486SLC
Processor Speed	16/25/33MHz
Chip Set	ACER
Max. Onboard DRAM	16MB
Cache	None
BIOS	Unidentified
Dimensions	220mm x 220mm
I/O Options	None
NPU Options	80387SX



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J4	Turbo switch	J8
Reset switch	J6	Turbo LED	J8
Speaker	J7	Power LED & keylock	J9

Note: Relative pins of J8 are unidentified.

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AUVA

NPM16-A0/ NPM25-A0/ NPM33-A0/
 NPM25-A1/ NPM33-A1

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í ADS delay select disabled	J5	pins 2 & 3 closed
ADS delay select enabled	J5	pins 1 & 2 closed
í NPU synchronous with CPU	JP2	pins 1 & 2 closed
NPU asynchronous with CPU	JP2	pins 2 & 3 closed
í Monitor type select color	JP4	pins 2 & 3 closed
Monitor type select monochrome	JP4	pins 1 & 2 closed
í Parity check enabled	JP6	Open
Parity check disabled	JP6	Closed
í CMOS memory select normal	JP7	pins 2 & 3 closed
CMOS memory clear	JP7	pins 1 & 2 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
512KB	(2) 256K x 9	NONE
1MB	(2) 256K x 9	(2) 256K x 9
2MB	(2) 1M x 9	NONE
4MB	(2) 1M x 9	(2) 1M x 9
8MB	(2) 4M x 9	NONE
16MB	(2) 4M x 9	(2) 4M x 9

BUS SPEED CONFIGURATION		
CPU Speed	JP1	JP5
í 33MHz	pins 2 & 3 closed	pins 2 & 3 closed
16MHz	pins 1 & 2 closed	pins 1 & 2 closed
20MHz	pins 2 & 3 closed	pins 1 & 2 closed
25MHz	pins 1 & 2 closed	pins 2 & 3 closed